

RESEARCH PAPER

Bandwidth enhancement of a planar monopole microstrip patch antenna

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This paper presents a simple broadband planar monopole microstrip patch antenna with curved slot and partial ground plane. The proposed antenna is designed and fabricated on commercially available FR4 material with $\epsilon_r = 4.3$ and 0.025 loss tangent. Bandwidth enhancement has been achieved by introducing a curved slot in the patch and optimizing the gap between the patch and the partial ground plane and the gap between the curved slot and the edge of the patch. Simulated peak gain of the proposed antenna is 4.8 dB. The impedance bandwidth (defined by 10 dB return loss) of the proposed antenna is 109% (2–6.8 GHz), which shows bandwidth enhancement of 26% as compared with simple monopole antenna. The antenna is useful for 2.4/5.2/5.8-GHz WLAN bands, 2.5/3.5/5.5-GHz WiMAX bands, and other wireless communication services. Measured results show good agreement with the simulated results. The proposed antenna details are described and measured/simulated results are elaborated.

Keywords: Antennas and propagation for wireless systems, Antenna design, Modeling and measurements

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I. INTRODUCTION

With the rapid advancement in wireless technology printed monopole antennas have generated keen interest among the researchers worldwide as they have high-gain, high-efficiency, omnidirectional radiation pattern, and robustness. U, E, Elliptical, and Rectangular slots have been introduced for bandwidth enhancement of the microstrip patch antenna [1–5]. A wide slot antenna fed by microstrip line with fork-like tuning stub is presented [6]. Bandwidth enhancement is achieved by properly adjusting the parameters of the fork-like tuning stub. T, E, U, and ring shaped slots has been introduced in patch to obtain band notch characteristics [7–10]. Bandwidth enhancement of a printed slot antenna is reported by rotating the slot to an appropriate angle [11]. A pair of parasitic strip lines is used in slot antenna [12] to achieve 80% fractional bandwidth and miniaturization of about 70%. Bandwidth enhancement has been proposed by introducing a parasitic patch in slot antenna [13]. Higher-order modes are excited due to introduction of the parasitic patch. A pair of parasitic patches, curved slots, and EBG structure has been introduced in a slot antenna for bandwidth enhancement [14, 15]. Radial slots have been proposed for the dual-frequency operation [16], where the operating frequency can be controlled by varying the angle between the slots.

This paper presents a simple broadband planar monopole microstrip patch antenna with curved slot and partial ground

plane. Initially by adding a rectangular slot the bandwidth of the monopole antenna is enhanced due to excitation of higher-order modes and on blending the corners of the slot, the impedance bandwidth is further enhanced due to impedance matching. Partial ground plane plays a crucial role in the impedance matching, as the size of the ground plane is increased capacitive effect has been observed. The proposed antenna shows 109% fractional bandwidth. Stable radiation pattern throughout the operating band has been observed.

II. ANTENNA DESIGN

The configuration of the proposed planar monopole microstrip patch antenna and simple monopole antenna is shown in Fig. 1. Both antennas have overall dimensions of $50 \times 55 \text{ mm}^2$ and are printed on the FR4 dielectric substrate having relative permittivity $\epsilon_r = 4.3$ and loss tangent of 0.025. The thickness of the dielectric substrate is 1.5 mm and 50 Ω microstrip lines with 3 mm width are used to feed both antennas. The patch and microstrip line are printed on one side of the substrate and the partial ground plane is printed on the other side. The length of the microstrip line is denoted by l . The distance between the curved slot and the edge of the patch is denoted by g . This gap is optimized for better impedance matching. A curved slot has been introduced in the patch to achieve bandwidth enhancement.

The length of the microstrip line and the length of the partial ground plane are kept same for obtaining the best possible impedance matching. Exact values of the different parts of the proposed antenna are mentioned in Fig. 1. Simulation is carried out using CST simulation software, a finite integration technique (FIT)-based commercial electromagnetic simulator.

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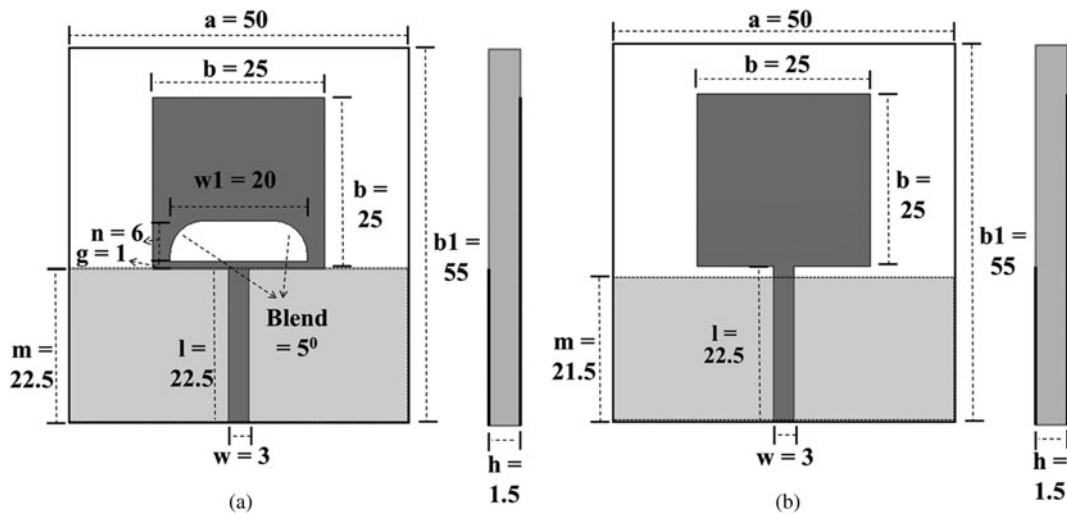


Fig. 1. Geometrical configurations of (a) the proposed antenna and (b) simple monopole antenna. (all dimensions are in mm)

In this design, to enhance the bandwidth a curved slot has been introduced in the patch of the simple monopole antenna. Initially a rectangular slot with dimension $n \times w_1$ has been introduced to enhance bandwidth. Two corners of the rectangular slots are blended to 5° for further bandwidth enhancement. Signals are radiated from the patch as well as from the ground plane. From the simulated results, it is found that the proposed antenna has achieved bandwidth enhancement of about 2.2 GHz as compared with the simple monopole antenna.

III. PARAMETER STUDY

In this section, the study of various parameters of the proposed antenna is carried out to understand their effect and to find out the best possible design parameters. Figure 2 shows the simulated return loss of the simple monopole antenna without curved slot and the proposed antenna. It is observed that the impedance bandwidth of the simple monopole antenna and the proposed antenna is 83% (1.9–4.6 GHz) and 109% (2–6.8 GHz), respectively. All other parameters are

same for both the cases except the introduction of curved slot in the proposed design and the gap between the edge of the patch and the ground plane as shown in Fig. 1.

Simulated return loss of the proposed antenna with various values of the ground plane (value = m) is shown in Fig. 3. Size of the ground plane actually shows the gap between the edge of the patch and the ground plane. It has been observed that as this gap is increased the bandwidth of the proposed antenna is degraded due to impedance mismatch. Return loss graph clearly shows that on increasing this gap either in upward direction or downward directions drastically affects the return loss. So the optimized value of the ground plane is kept as 22.5 mm, which shows that there is no gap between the edge of the patch and the ground plane as shown in Fig. 1.

Effects of various values of w_1 and n are shown in Figs 4 and 5, respectively. It is obvious from the results that as the values of w_1 and n are increased the bandwidth of the proposed antenna increases. The lower resonant frequency f_1 (2 GHz) is nearly constant and is same as that of simple monopole antenna but on increasing the values of w_1 and n , the upper resonant frequency f_2 is increased, due to excitation of higher-order resonating modes. On increasing the

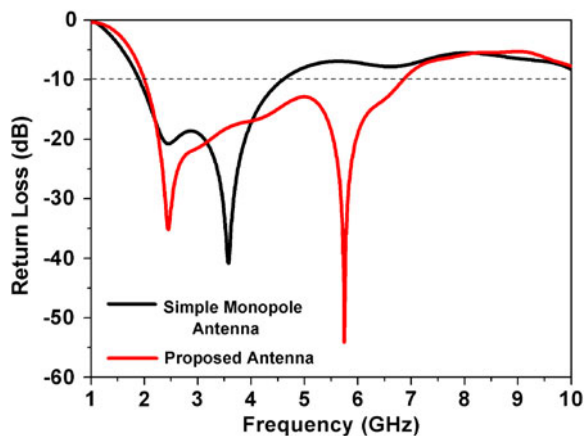


Fig. 2. Simulated return loss of simple monopole antenna and the proposed antenna.

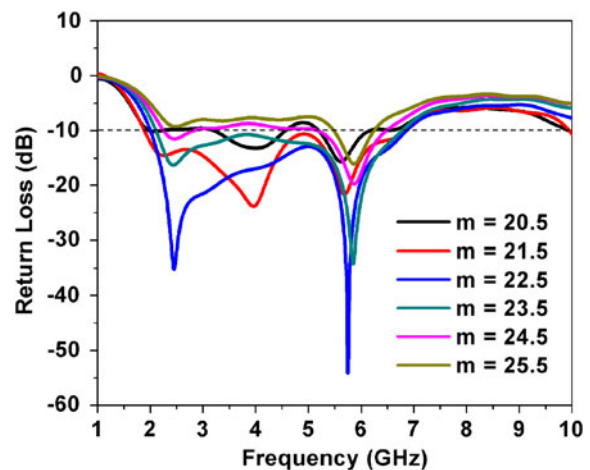


Fig. 3. Simulated return loss of proposed antenna with various values of m . (other parameters are same as in Fig. 1)

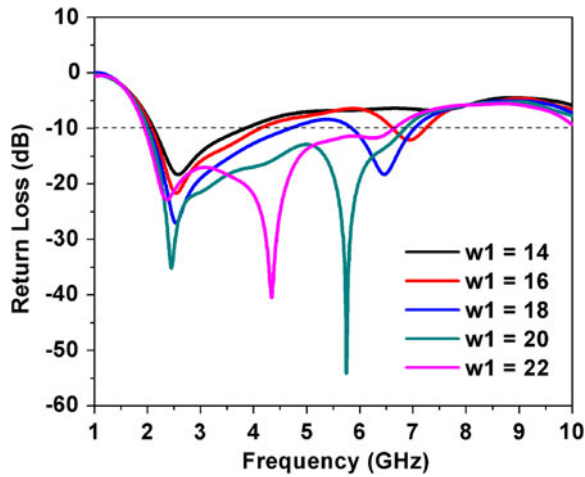


Fig. 4. Simulated return loss of proposed antenna with various values of w_1 . (other parameters are same as in Fig. 1)

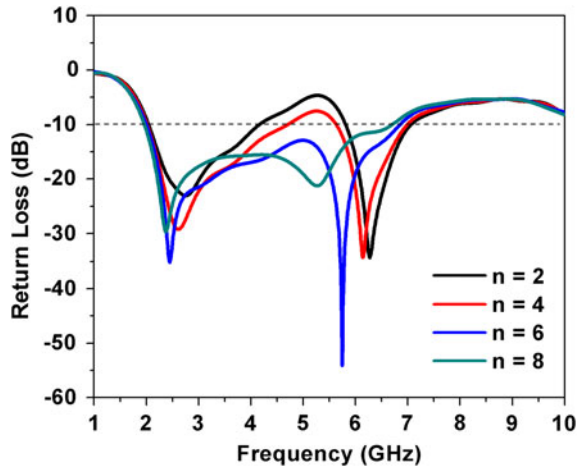


Fig. 5. Simulated return loss of the proposed antenna with various values of n . (other parameters are same as in Fig. 1)

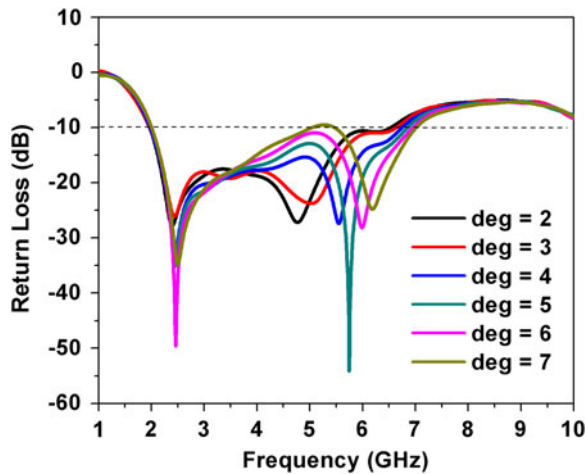


Fig. 6. Simulated return loss of the proposed antenna with various values of deg (other parameters are same as in Fig. 1.)

respective sizes the upper resonant frequency f_2 increases till n reaches to 6 mm and w_1 reaches to 20 mm, after that upper resonant frequency f_2 starts degrading due to impedance mismatch. So the optimized values of w_1 and n are set to 20 and 6 mm, respectively.

After introduction of the rectangular slot two edges are blended. Figure 6 shows simulated return loss with various values of blending (value = deg). As the value of deg is increased the lower resonant frequency f_1 remains constant but the upper resonant frequency f_2 is increased due to better impedance matching. It is observed that the upper resonant frequency f_2 has enhanced from 6.5 to 6.8 GHz, showing bandwidth enhancement of 0.3 GHz. The optimized value of deg is set to 5° . In the next section, various results obtained after simulation and measurement are described and elaborated.

IV. RESULTS

The prototype of the proposed antenna is fabricated using LPKF PCB prototyping machine as shown in Fig. 7 and the return loss has been measured using Agilent Vector Network Analyzer. Figure 8 shows measured and simulated return loss of the proposed antenna. The measured and simulated results are in good agreement which validates the simulation process. Measured impedance bandwidth of the proposed antenna is 109% (2–6.8 GHz). Simulated gain comparison of simple monopole antenna and proposed antenna is shown in Fig. 9. The peak gain of the proposed antenna is 4.8 dB. Gain enhancement of about 0.6 dB can be observed in 5.2–6.2 GHz range, whereas there is slight deviation of about 0.3 dB at other operating frequencies.

Simulated surface current distribution of the proposed antenna is demonstrated in Fig. 10. From the current distribution it is imminent that due to introduction of the curved slot the current path is diverted. As a result higher-order modes are excited and signals starts radiating from the edges of the slot. It is clear that at higher frequencies the current distribution is changed and the peak values are higher as compared with lower frequencies. Peak values of the surface current

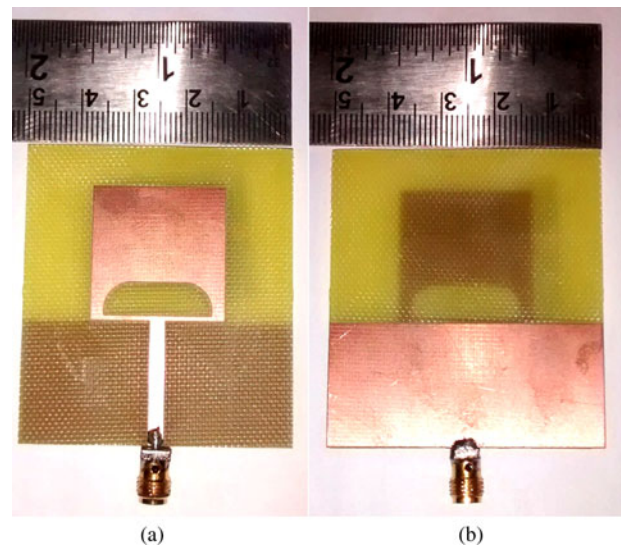


Fig. 7. Photograph of the fabricated antenna. (a) Top view. (b) Bottom view.

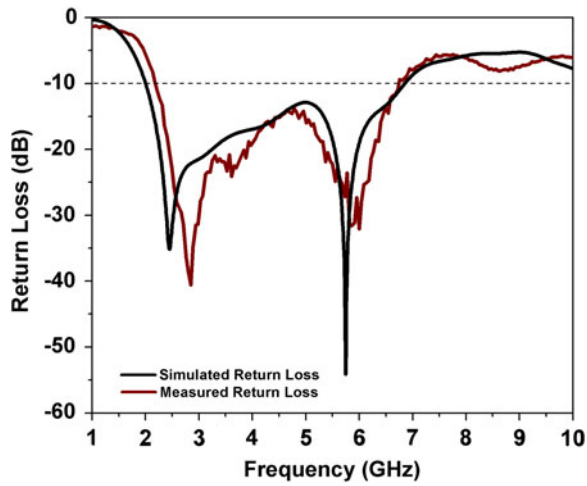


Fig. 8. Simulated and measured return losses of the proposed antenna.

and the current distribution for various frequencies at constant phase (226°) is shown in Fig. 10. Stable radiation pattern has been observed across the operating bandwidth. Figure 11 shows simulated and measured *E*- and *H*-plane

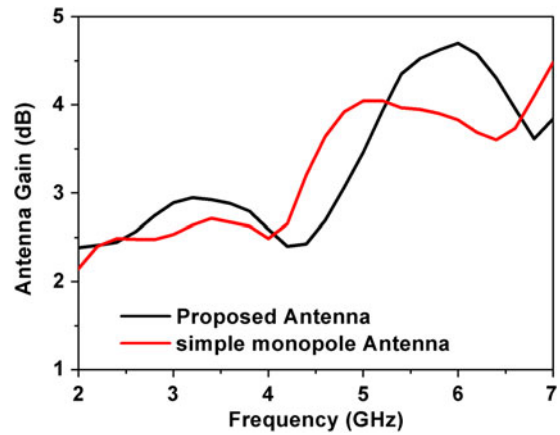


Fig. 9. Simulated gain of simple monopole antenna and proposed antenna.

radiation patterns of the proposed antenna at various frequencies. Simulated and measured results are in good agreement, validating the simulation process. Omnidirectional *H*-plane and bidirectional *E*-plane radiation pattern has been observed which are generally required for monopole antenna.

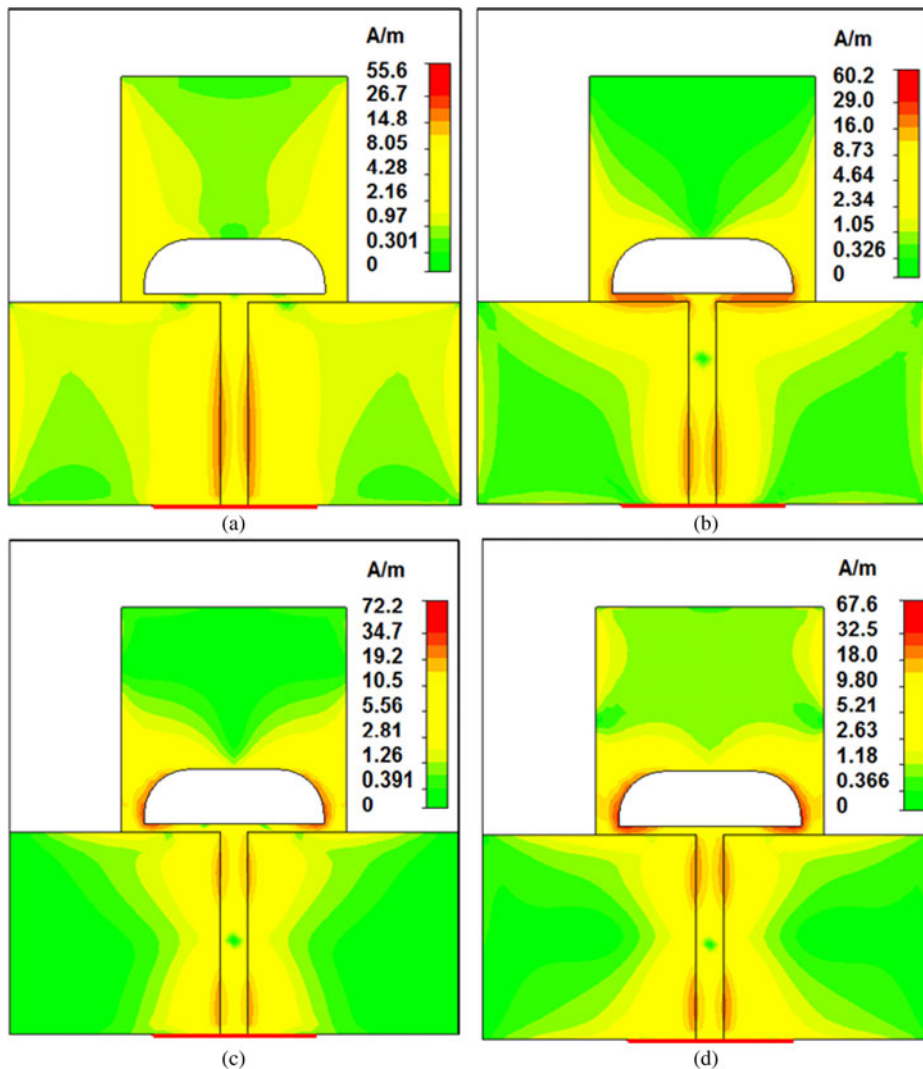


Fig. 10. Simulated surface current distribution of the proposed antenna at (a) 2.4 GHz, (b) 3.5 GHz, (c) 5.2 GHz, and (d) 5.5 GHz.

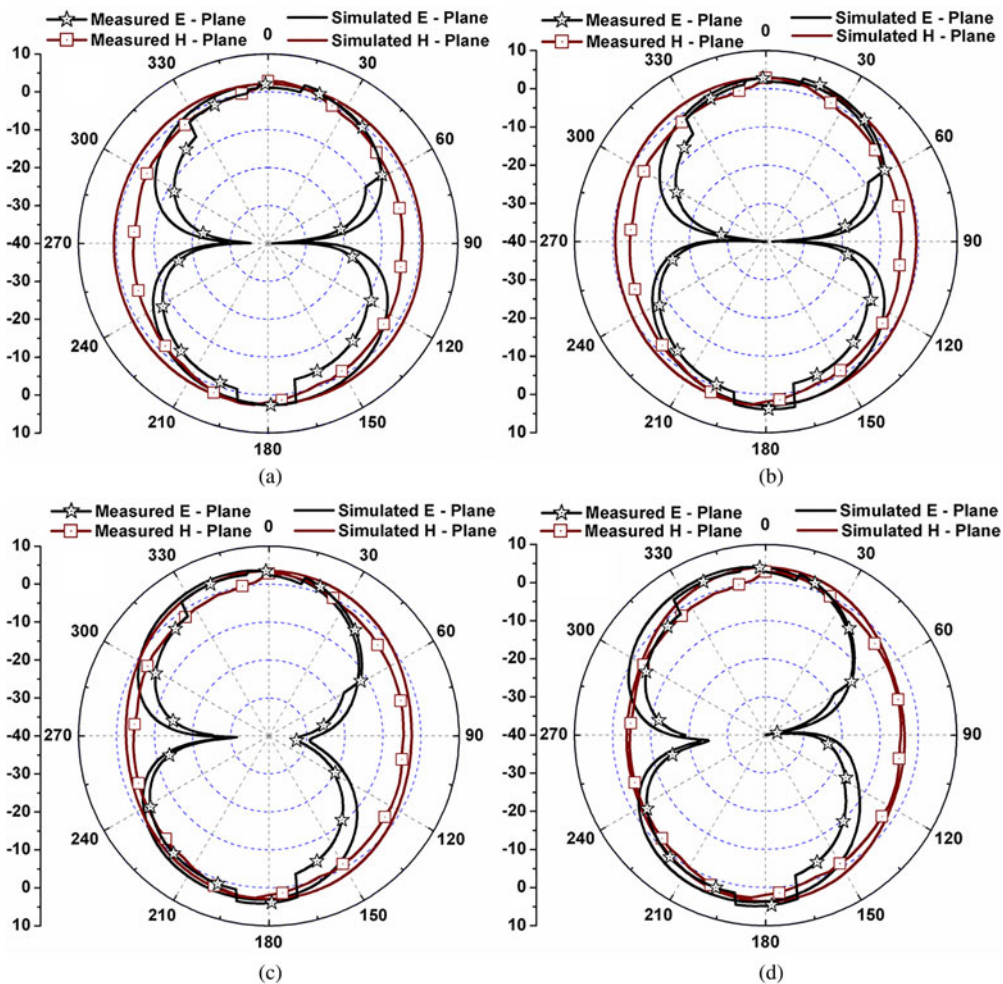


Fig. 11. Simulated and measured radiation pattern of the proposed antenna at (a) 2.4 GHz, (b) 3.5 GHz, (c) 5.2 GHz, and (d) 5.5 GHz.

V. CONCLUSION

A simple broadband planar monopole microstrip patch antenna with curved slot and partial ground plane is proposed. Bandwidth enhancement has been achieved by cutting a curved slot in the patch of the antenna due to excitation of higher-order modes and better impedance matching. From the results obtained in this study, the impedance bandwidth of the proposed antenna determined by 10 dB return loss is 109%, which shows broadband nature and bandwidth enhancement of 26% in comparison with the simple monopole antenna. The gain of the proposed structure is maintained as compared with the simple monopole antenna with slight deviation of about 0.3 dB in the operating band. The proposed antenna exhibits stable radiation pattern over the entire operation frequency range. This suggests that the proposed antenna is useful for 2.4/5.2/5.8-GHz WLAN bands, 2.5/3.5/5.5-GHz WiMAX bands, and other wireless communication services.

REFERENCES

[1] Wong, K.L.: Compact and Broadband Microstrip Antennas, Wiley, New York, 2002.

[2] Kazim, J.; Bibi, A.; Rauf, M.; Tariq, M.; Owais, M.: A compact planar dual band-notched monopole antenna for UWB application. *Microw. Opt. Technol. Lett.*, **56** (5) (2014), 1095–1097.

[3] Wang, X.; Wang, L.; Zhou, H.; Lu, W.: A compact CPW-fed antenna with dual band-notched characteristics for UWB applications. *Microw. Opt. Technol. Lett.*, **56** (5) (2014), 1047–1049.

[4] Ray, K.P.; Thakur, S.S.; Deshmukh, A.A.: Slot cut printed elliptical UWB monopole antenna. *Microw. Opt. Technol. Lett.*, **56** (3) (2014), 631–635.

[5] Telsang, T.M.; Kakade, A.B.: Ultra wideband slotted semicircular patch antenna. *Microw. Opt. Technol. Lett.*, **56** (2) (2014), 362–369.

[6] Sze, Y.; Wong, K.L.: Bandwidth enhancement of a microstrip line-fed printed wide-slot antenna. *IEEE Trans. Antennas Propag.*, **49** (7) (2001), 1020–1024.

[7] Rezaeieh, S.A.; Abbak, M.: A novel compact antenna enhanced with variable notches. *Microw. Opt. Technol. Lett.*, **54** (4) (2012), 946–949.

[8] Sarkar, M.; Dwari, S.; Daniel, A.: Compact printed monopole antenna for ultra-wideband application with dual band notched characteristic. *Microw. Opt. Technol. Letters*, **55** (11) (2013), 2595–2600.

[9] Nguyen, T.D.; Lee, D.H.; Park, H.C.: Design and analysis of compact printed triple band-notched UWB antenna. *IEEE Antennas Wireless Propag. Lett.*, **10** (2011), 403–406.

- [10] Ojaroudi, N.; Ojaroudi, M.: Novel design of dual band-notched monopole antenna with bandwidth enhancement for UWB applications. *IEEE Antennas Wireless Propag. Lett.*, **12** (2013), 698–701.
- [11] Jan, J.Y.; Su, J.W.: Bandwidth enhancement of a printed wide-slot antenna with a rotated slot. *IEEE Trans. Antennas Propag.*, **53** (6) (2005), 2111–2114.
- [12] Jan, J.Y.; Wang, L.C.: Printed wideband rhombus slot antenna with a pair of parasitic strips for multiband applications. *IEEE Trans. Antennas Propag.*, **57** (4) (2009), 1267–1270.
- [13] Sung, Y.: Bandwidth enhancement of a microstrip line-fed printed wide-slot antenna with a parasitic centre patch. *IEEE Trans. Antennas Propag.*, **60** (4) (2012), 1712–1716.
- [14] Fan, T.; Yen, Y.Z.; Lee, B.; Hu, W.; Yang, X.: Bandwidth enhancement of a printed slot antenna with a pair of parasitic patches. *IEEE Antennas Wireless Propag. Lett.*, **11** (2012), 1230–1233.
- [15] Guo, Z.; Tian, H.; Wang, X.; Luo, Q.; Ji, Y.: Bandwidth enhancement of monopole UWB antenna with new slots and EBG structures. *IEEE Antennas Wireless Propag. Lett.*, **12** (2013), 1550–1553.
- [16] Chen, S.Y.; Hsu, P.: Broad-band radial slot antenna fed by coplanar waveguide for dual-frequency operation. *IEEE Trans. Antennas Propag.*, **53** (11) (2005), 3448–3452.



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