


# A compact UWB antenna with dynamically switchable band-notched characteristic using broadband rectenna and DC-DC booster

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## Research Paper

**Cite this article:** Fakharian MM (2021).

A compact UWB antenna with dynamically switchable band-notched characteristic using broadband rectenna and DC-DC booster. *International Journal of Microwave and Wireless Technologies* **13**, 1086–1095. <https://doi.org/10.1017/S1759078721000027>

Received: 7 August 2020

Revised: 24 December 2020

Accepted: 30 December 2020

First published online: 2 February 2021

### Key words:

Broadband rectenna; DC-DC booster; energy harvesting; reconfigurable antenna; UWB

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## Abstract

In this article, a dynamically switchable ultra-wideband (UWB) planar monopole antenna employing defected ground structure (DGS) with a folded stepped impedance resonator (SIR) that can operate as either a UWB mode or the single band-notched mode is introduced. The UWB monopole antenna contains a novel whirligig-shaped radiating patch and a chambered conductor as a partial ground plane. The switchable UWB antenna uses one PIN diode as switching elements in the DGS-SIR structure without any biasing network. When the state of diode is OFF, the planar monopole antenna changes to the UWB mode, and when the diode is turned ON, a frequency notch is created at 5–6 GHz. The state of diode is set to the “ON” state dynamically in the presence of a 5–6 GHz RF signal that is detected by using a wireless power management unit (PMU) that contains a broadband rectenna and a DC-DC passive booster. The rectenna consists of a novel cypress-shaped monopole antenna as a signal receiving part and two sub-rectifiers which are connected to a 3 dB branch-line coupler with a grounded isolation port. The antenna switches from UWB to single band-notched when an RF input signal ( $\geq 8.5$  dBm) in the 5.25 GHz is sensed by the RF PMU with a conversion efficiency of 26% and DC output voltage of 0.36 V, and it fades immediately in real time when the external RF signal is eliminated. In the three-tone signals, the efficiency and input signal improvements are about 10% and  $-5.5$  dBm in the low-power levels, especially, and so develop and enhance the performance of the dynamic reconfigurability.

## Introduction

Ultra-wideband (UWB) communication system in the wireless technology has become attractive since the Federal Communications Commission (FCC) declared a range of frequency from 3.1 to 10.6 GHz as unlicensed, in 2002 [1]. However, there are narrow band technologies (e.g. WLAN and HiperLAN) that occupy the UWB spectrum and cause interference which reduces the performance of these systems [2]. To improve the system performance, agile radios can be used for switchable UWB antennas with the ability to stop the single-, dual-, or multi-band interferences with band-notch characteristics. For this purpose, various techniques for designing reconfigurable band-notched UWB antennas have been presented to avoid these frequency interferences. Some techniques such as cutting slots with various shapes in the microstrip feed-line [3], loading resonators or slots etched on the radiator [3–8] or on the ground plane [8, 9], loading parasitic elements with different shapes [6, 10–12], and utilizing electromagnetic band gap and defected structures [13] have been presented. With these designs, the antenna can be worked in the UWB mode when there is no coexisting system while cancelling interferences when required. In [14, 15], the advantages and disadvantages of different techniques and various notch antennas for application in the band-notch characteristics in the UWB systems have been verified and discussed.

To achieve the agile radios, electrical switches such as radio-frequency micro-electromechanical systems (RF-MEMS) [4], varactors [7] using continuously adjustable voltage sources, or PIN diodes [11] to switch between “ON”/“OFF” states can be used in the antenna parts so as to redistribute the currents on the antenna surface and cause changes in the antenna electrical length. Among these switches, the switching times of the varactors or PIN diodes are in the range of 1–100 ns and are more compact as compared to RF-MEMS [16]. Besides, biasing voltages for the varactors and RF-MEMS are very high (10–100 V) alternative to the PIN diodes (0.3–1.1 V). Since the dynamical activation, which is mainly focused in this study, relies on the voltage of bias, a proper PIN diode is applied in the form of a switching element.

Most reconfigurable band-notched UWB antennas alter their state through a decision, made by the operator. The presented UWB planar antenna with dynamical features such as switchable band-notched characteristics alters its structure based on the absence or the existence of surrounding wireless power that is wirelessly obtained by an RF harvesting unit. The proposed antenna can operate in the UWB mode, and when an ambient wireless signal at the

**Table 1.** Comparison of the proposed work with previous work

| Ref.      | UWB range, GHz | Notch-band range, GHz | Operating band of rectenna, GHz | Detecting RF signal, dBm | Switching method | Size of rectenna, mm <sup>3</sup> | Size of UWB antenna, mm <sup>3</sup> | Type of DC-DC booster |
|-----------|----------------|-----------------------|---------------------------------|--------------------------|------------------|-----------------------------------|--------------------------------------|-----------------------|
| [28]      | 2.3–11.6       | 5–6                   | 5.4–5.8                         | –10                      | PIN diode        | 200 × 180 × 0.8                   | 25 × 35 × 0.8                        | Active                |
| This work | 3.1–10.75      | 5–6                   | 5–6                             | –8                       | PIN diode        | 90 × 65 × 0.8                     | 21 × 19 × 0.8                        | Passive               |

WLAN frequency band (5.1–5.8 GHz) is received, it activates a power management unit (PMU) that delivers the required DC power to change the state of PIN diode to “ON” state and so the antenna is converted from UWB mode into the single band-rejection mode at 5–6 GHz.

Recently, with the quick increase in RF systems, there has been important research work toward wireless energy harvesting applications in the various bands such as UHF, GSM900, GSM1800, UMTS band, Wi-Fi 2.4 GHz, LTE band 7, and so on [17]. The variation of power density ( $\mu\text{W}/\text{cm}^2$ ) in different bands depends on the time and frequency. The wireless energy harvesting by using a passive device called rectifying antenna (rectenna) is a possible solution to change the environmental wireless energy to a usable DC power efficiently [17–21]. However, in most cases, the DC power is insufficient to supply an independent task. Therefore, a PMU with a DC-DC power converter should be applied to supply the necessary DC power for the rectenna load. For the dynamically reconfigurable band-notched UWB antennas introduced in this article, the harvested wireless energy is not stored; however, it is used as an alternative for a trigger signal, empowering the PMU to supply the required DC power to the PIN diode. Moreover, to maximize sensitivity and gathered wireless energy in the rectenna, the receiving antenna should be operating in the broadband frequency and omnidirectional radiation pattern. However, due to the non-linearity behavior of the rectifier, designing a broadband rectenna is very challenging. Nevertheless, several techniques have been reported for broadband rectenna designing for both low- and high-power wireless energy harvesting [22–27]. So far, there is only a UWB antenna with a dynamically switchable notch-band at 5.6 GHz which is introduced in [28]. In this work, the UWB antenna has been realized by a rectangular-shaped slot and a J-shaped stub that electrically linked to a radiating patch using a PIN diode with a complicated biasing network. Detecting a  $-10$  dBm of an external RF signal in 5.6 GHz signal activates the dynamic making of the notch in the return loss plot which results in the elimination of the interference. While the level of received interferer signal falls below  $-10$  dBm, the frequency notch instantly disappears, and yields to a typical UWB radiator mode. The rectenna operates at a single narrow band of 5.6 GHz that contains a patch antenna linked to a conventional voltage doubler rectifier circuit with active DC-DC booster. However, the design of a single narrow band leads to a low RF-dc conversion efficiency for low-power levels of input power density, and it is not very appropriate for wireless energy scavenging applications. A broadband rectenna design can capture more power from the existing signals of WLAN standards from RF energy sources in air and then produce more output power than a narrowband antenna. Moreover, the active DC-DC power booster needs at least a DC biasing voltage of 0.9 V and it is activated with an incident power of  $-11$  dBm that it is not proper for actual environments. A comparison between previous work [28] and the proposed UWB antenna

with dynamically switchable band-notched characteristic in terms of different parameters is given in Table 1.

In this article, a dynamically switchable single band-notched UWB planar monopole antenna with a novel whirligig-shaped radiation patch is introduced. The band-notched performance is achieved by injecting a defected ground structure (DGS) with a folded stepped impedance resonator (SIR). The equivalent filament and quasi-static equivalent circuit models of the folded DGS-SIR are investigated and extracted in our previous work [29] to explain its performance as a band rejection structure. By inserting a PIN diode into the DGS-SIR and setting it in “ON” state, the notched band can be controlled based on an RF-activated PMU from a broadband rectenna booster system. To design a broadband rectenna, a novel wideband cypress-shaped monopole antenna as a receiving element and a broadband rectifier based on a wideband matching network [30] and a branch-line coupler [31, 32] are applied so that the reflected power can be returned and the RF-dc efficiency can be enhanced. The microstrip technology was applied to both the band-notched UWB antenna and the rectenna. These two were tested independently. Then, based on [33], an efficient power management system is used to design a DC-DC passive power booster for implementing an RF PMU. The combined system, fabricated as a system-on-package, can control the bias of PIN diode to switch the mode of UWB antenna, dynamically.

### Switchable band-notched UWB antenna

The geometry of the proposed UWB planar monopole antenna with a DGS-SIR on the ground plane as notched band configuration is shown in Fig. 1. The fabricated prototype is also illustrated in Fig. 1(b). The antenna is fabricated on a 0.8 mm-thick low-cost FR4 substrate with  $\epsilon_r = 4.4$  and  $\tan\delta = 0.02$ , and a compact overall dimensions of 21 ( $L$ ) × 19 ( $W$ ) × 0.8 mm<sup>3</sup>. The radiating structure is a novel whirligig-shaped antenna to decrease the lowest resonance frequency with increasing the antenna perimeter and get wideband operational bandwidth [34], and it is fed by a 50  $\Omega$  microstrip transmission line. Besides that, a chambered conductor as a partial ground is applied to provide impedance matching over a wide frequency range and a radiation pattern with omnidirectional characteristics as a monopole antenna, so that the chambered edges are previously presented in [7, 28]. Moreover, the folded DGS-SIR contains low–high–low impedance slots, which they are inserted on the ground plane. From [29], the resonant frequency of a half-wavelength resonator which perturbs the resonant response can be assumed as follows:

$$L = Ws + 2Ls + 2Wa + 2La + 2S1 + 2S2 + 2g1, \quad (1)$$

$$f_{notch} = \frac{c}{2L \times \sqrt{\epsilon_{eff}}}, \quad (2)$$

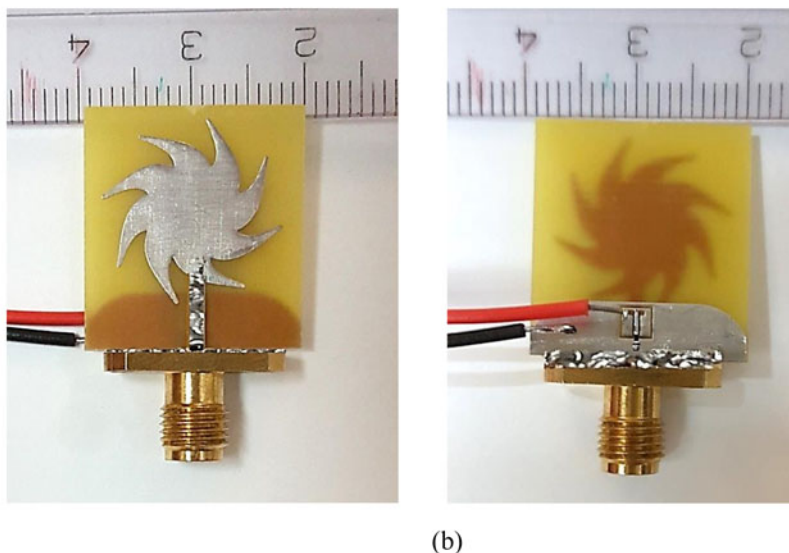
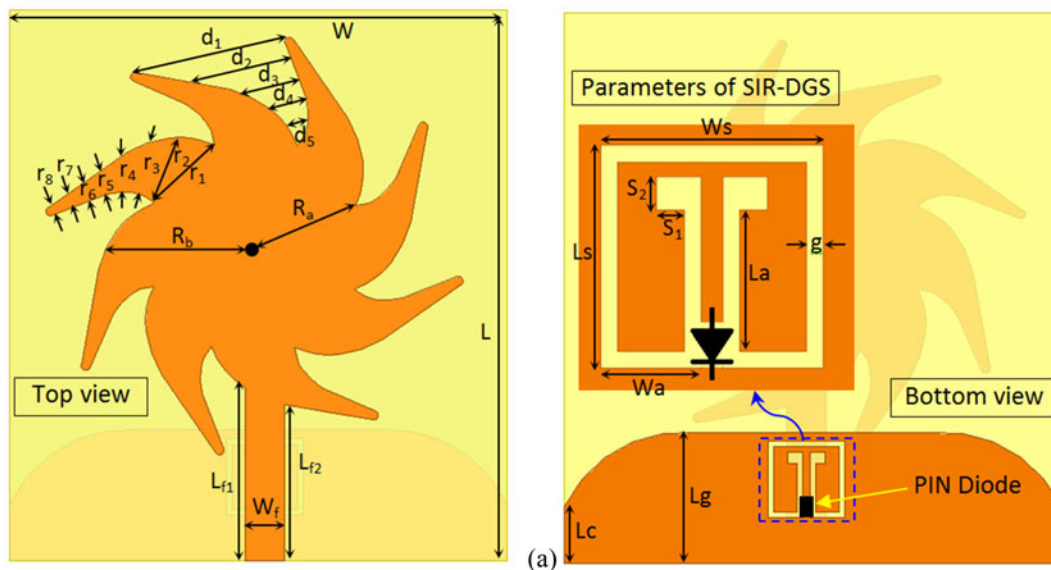


Fig. 1. (a) Configuration of the designed switchable band-notch UWB antenna. (b) The fabricated antenna prototype.

where  $L$  is the entire length of the folded DGS-SIR,  $c$  indicates the light speed in free space, and  $\epsilon_{eff}$  is the effective dielectric constant of the antenna, which is assigned by  $\epsilon_{eff} = (\epsilon_r + 1)/2$ . Through the physical dimensions of DGS, the quasi-static model of an equivalent circuit of the DGS-SIR is verified in [29]. The DGS-SIR is designed to be able to make a band-notch in the frequency band of 5–6 GHz for stopping HIPERLAN/2 (5.47–5.725 GHz) and WLAN (5.15–5.35 and 5.725–5.825 GHz). The final design parameters of the proposed UWB antenna are as follows:  $L = 21$  mm,  $W = 19$  mm,  $L_{f1} = 6.6$  mm,  $L_{f2} = 6$  mm,  $W_f = 1.5$  mm,  $R_a = 4.3$  mm,  $R_b = 5.5$  mm,  $r_1 = 3.1$  mm,  $r_2 = 2.5$  mm,  $r_3 = 2.1$  mm,  $r_4 = 1.25$  mm,  $r_5 = 0.85$  mm,  $r_6 = 0.72$  mm,  $r_7 = 0.54$  mm,  $r_8 = 0.35$  mm,  $d_1 = 6.1$  mm,  $d_2 = 3.7$  mm,  $d_3 = 2.1$  mm,  $d_4 = 1.2$  mm,  $d_5 = 0.6$  mm,  $L_g = 5$  mm,  $L_c = 2.2$  mm,  $W_s = 2.9$  mm,  $L_s = 2.9$  mm,  $W_a = 1.1$  mm,  $L_a = 1.85$  mm,  $S_1 = 0.35$  mm,  $S_2 = 0.45$  mm,  $g = 0.21$  mm.

As seen in Fig. 1, the DGS-SIR is controlled by applying a PIN diode as an electronic switch to accommodate the

reconfigurability. Therefore, the proposed antenna can be operating in two modes (a normal UWB mode or a single band-notched mode) depending on the PIN diode state. In the ON state of the PIN diode, the DGS-SIR acts as a resonator slot which impacts on the current flow distribution at the band-notch frequency and causes to yield band-notch characteristic. However, in the OFF state, the DGS-SIR acts as a square ring slot which lets the current to flow in the feed line and radiating patch routinely, and so lets the desired signal at 5.6 GHz to pass at the notch-band. In this work, a PIN diode by Infineon (model BAR50-02V) is selected, which is proper for WLAN bands. However, the actual PIN diode as a “switch element” has an additional parasitic effect on the performance of the antenna, which requires to be considered into simulation and account [35]. Figure 2 shows the circuit model applied in the ON/OFF states in the full-wave simulation. It can be seen that the ON/OFF states have inductance  $L$  that is the packaging inductive effect. In the forward biased (ON state), the equivalent circuit has a low forward resistance  $R_S$  that is the

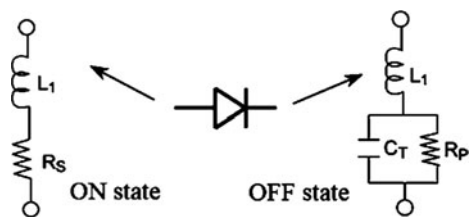


Fig. 2. The PIN diode-equivalent RF circuit at the OFF and ON states.

greatest limiting factor for this state and determines the insertion losses. The equivalent circuit for zero or reverse biased (OFF state) has a parallel combination of  $R_p$  (reversed bias resistance) and  $C_T$  (total capacitance), that gives the isolation. These parameters need to construct the model of the circuit which they are set in a datasheet of the selected PIN diode by the manufacturing company. Based on the datasheet of PIN diode [36], for the OFF state, it has a parallel circuit with a capacitance of  $C_T = 0.15$  pF and a resistance of around  $R_p = 5$  k $\Omega$ , while for the ON state, it has a forward resistance of  $R_s = 3$   $\Omega$ . For this PIN diode, the series parasitic inductance because of chip packaging is only  $L_1 = 0.6$  nH, and so it can be ignored for the simplicity.

In the proposed structure, owing to the configuration of the folded DGS-SIR, there is no need for incorporating parasitic components such as capacitors which may destroy the antenna performance; therefore, this configuration can reduce the complexity of biasing circuit. Moreover, the DC lines can be directly linked to the PIN diode on the antenna backside in the ground plane and therefore, the characteristics of the monopole antenna such as radiation do not experience any perturbation. Furthermore, an inductor could be applied in series with each DC lines to choke the RF signal in the DC lines, but since the DC voltage of biasing for PIN diode is supplied from the output of the DC-DC power converter, which is clarified in section “DC-DC power booster”, due to the internal circuit of the power converter, it is not essential to use RF choke.

In order to better find the phenomenon behind the band-notch UWB performance, the surface current distributions at the notched frequency of 5.5 GHz for both “OFF” and “ON” states are shown in Fig. 3. It can be seen that the current is focused at the edges of the folded DGS-SIR and oppositely directed between the exterior and interior edges, while the current return path is completely disturbed and the current is limited to the margin of the DGS-SIR at 5.5 GHz in the “ON” state. As a result, the resulting radiation fields can be suppressed, and high attenuation near the resonant frequency is attained, therefore the notched band produced. By adapting the dimensions of the folded DGS-SIR, the center frequencies of the notched band can be adjusted.

As a parametric study for finding out the performance of the resonator slot, the effects of the resonator gap ( $g$ ), the arm lengths ( $La$ ) and ( $Ws$ ) of the DGS-SIR on the impedance matching characteristics are carried out in the OFF state in which resonator is active. Figure 4(a) shows the reflection coefficient ( $|S_{11}|$ ) at different  $g$  of the band-notched resonator. It is clear that  $g$  effects the band-notched frequency position. The center frequency position is shifted from 5 to 5.75 GHz when  $g$  is varied from 0.15 to 0.29 mm and  $g$  of 0.21 mm is selected in order to realize the favorite band-notched characteristics. The variation of the band-notched frequency position with the gap distance  $g$  is through the fact that the gap capacitance decreases, when the gap distance increases. Therefore, by increasing the gap distance,

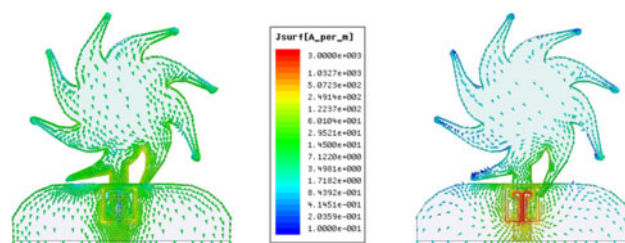


Fig. 3. The surface current distributions on the antenna at 5.5 GHz in the (a) “OFF” state and (b) “ON” state.

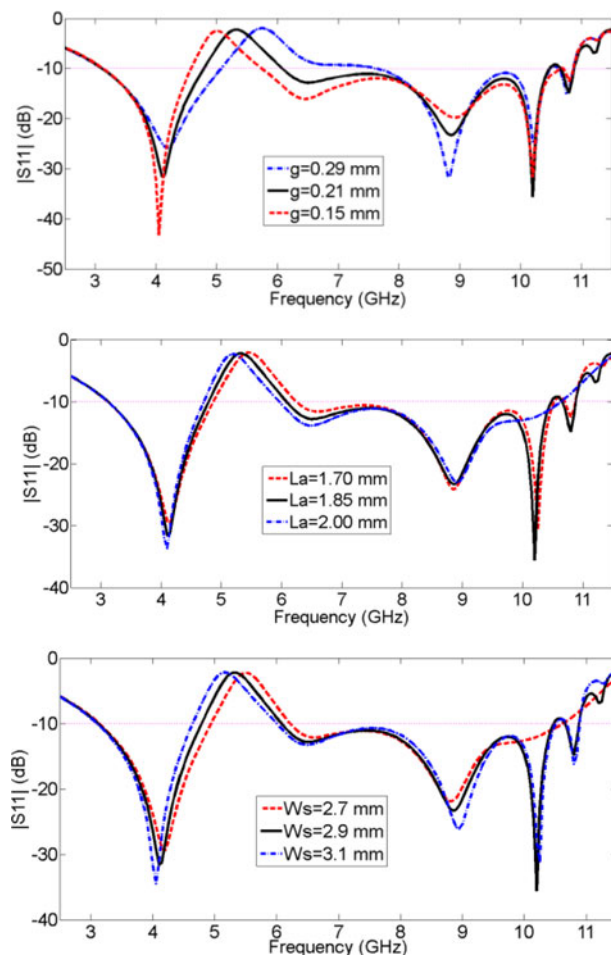


Fig. 4. Simulated  $|S_{11}|$  characteristics of the band-notched UWB antenna in the OFF state with different values of (a)  $g$ , (b)  $La$ , and (c)  $Ws$  in the folded DGS-SIR.

the resonant frequency increases. Figures 4(b) and 4(c) illustrate the return loss at different  $La$  and  $Ws$ , respectively. When the length of  $La$  and  $Ws$  increases, the notched band moves to the lower frequency. The changes of the resonant frequency of the DGS-SIR with the arm lengths are also because of the fact that when these parameters increase, the DGS-SIR inductance increases; therefore, the center frequency of the notched band is decreased.

In Fig. 5, measurement and simulation results for  $|S_{11}|$  parameter of the proposed band-notched UWB antenna in the both “ON” and “OFF” states are given. In the “OFF” state of the diode, the antenna radiates as a normal UWB frequency band



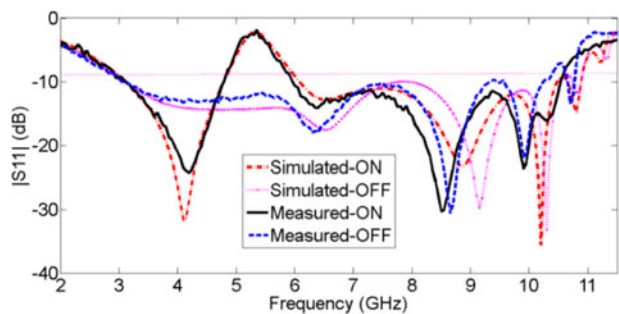
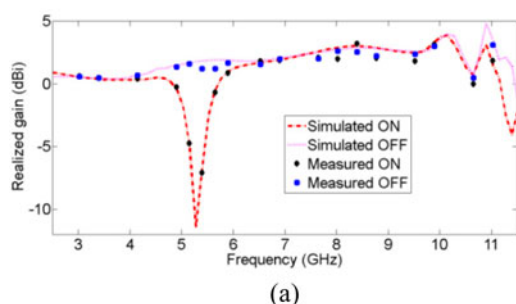


Fig. 5. Simulated and measured  $|S_{11}|$  of the designed band-notched UWB antenna in the ON and OFF states.

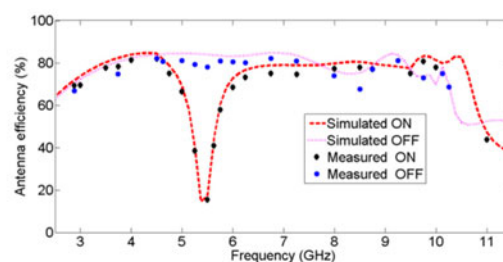
from 3.1 to 10.75 GHz, while in the “ON” state, a band-notch from 4.8 to 6 GHz with a center frequency of 5.35 GHz appears. The measured  $|S_{11}|$  is corresponding with the simulated one for most of the UWB frequency range. The difference between the measurement and simulation may be produced because of the imperfect soldering of the SMA connector, the variation of the substrate permittivity, or the misalignment of the PIN diode and the current limit of the PIN diode at the ON state. In this section, for turning “ON” the PIN diode, a supplied voltage of 1.1 V [36] from a DC source is applied. While in section “Implementation of combined modules and testing”, it can be active with the existence of a wireless interfering signal at 5–6 GHz as low as  $-8.5$  dBm detected by the rectenna.

Figure 6 shows the simulated and measured values of antenna realized gain and antenna efficiency in term of the operation frequency in both states. As shown in Fig. 6(a), the average measured realized gain of the proposed antenna is nearly constant about 2 dBi over the operating band from 3 to 10 GHz, except the notched frequency bands with values equal to  $-8$  dBi at 5.35 GHz in the “ON” state. Moreover, as shown in Fig. 6(b), the measured efficiency of the antenna is more than 75% over the desired band, except at 5.3 GHz in the “ON” state. The measured results are a little below the simulated one because of the misalignment of antenna and surrounding environment, especially in the higher bands [7].

Measured radiation patterns of the proposed antenna at 4.2 and 9.8 GHz for the “ON” and “OFF” states are depicted in Fig. 7. It can be observed that the antenna provides almost bidirectional radiation pattern in the *E*-plane (*yz*-plane) and almost omnidirectional pattern in the *H*-plane (*xz*-plane) in both states, especially at lower bands. However, ripples are noted in the measured radiation patterns, because of the patch small dimension [7].



(a)



(b)

Fig. 6. Measured and simulated (a) realized gains and (b) efficiency of the proposed antenna in the OFF and ON states.

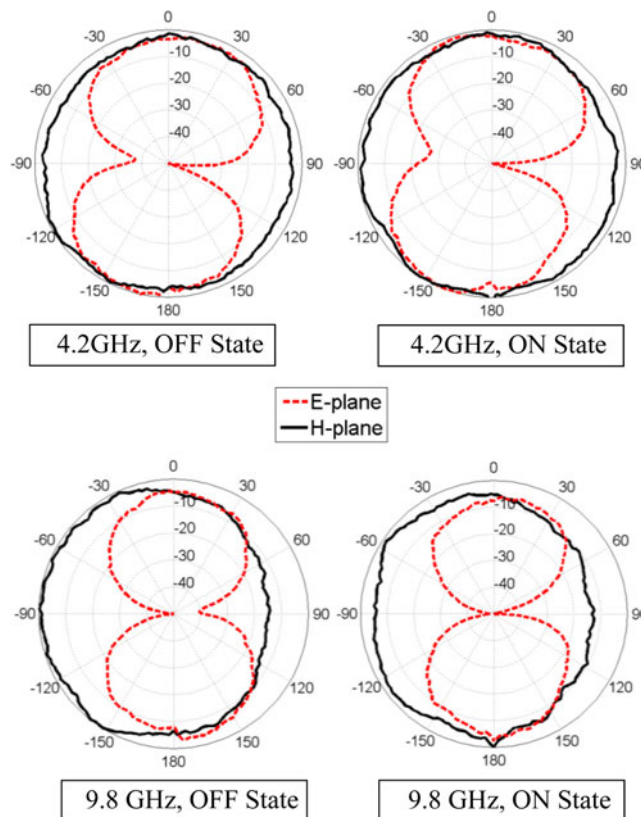
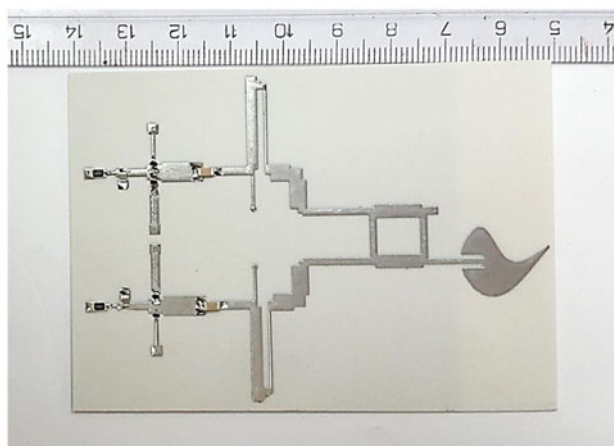
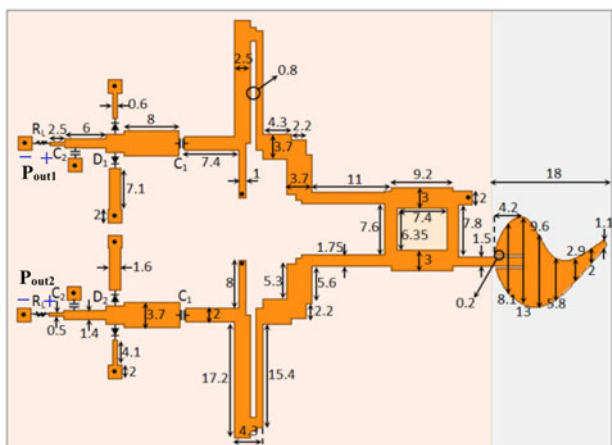


Fig. 7. Measured H- and E-plane radiation patterns of the antenna at the ON and OFF state at resonant frequencies of (a) 4.2 GHz and (b) 9.8 GHz.

### Broadband rectenna

As previously mentioned, an RF harvesting system is required for the dynamic and effective PIN diode biasing in the proposed band-notched UWB antenna. For this purpose, as shown in Fig. 8, a broadband rectenna is designed and fabricated to convert 5–6 GHz RF receiving signal into a DC power. The rectenna is printed on a Rogers 4003 substrate with  $\epsilon_r = 3.55$ ,  $\tan\delta = 0.0027$ , and the overall dimension of  $90 \times 65 \times 0.8$  mm<sup>3</sup>. The designed rectenna consists of a novel cypress-shaped monopole antenna as a signal-receiving part and two sub-rectifiers which are connected to a 3 dB branch-line coupler with a grounded isolation port for increasing RF-dc conversion efficiency at the desired frequency band. Each sub-rectifier contains a network of matching, Schottky diodes SMS7630-079LF of Skyworks [37], DC pass filter, and a resistive load. Each section

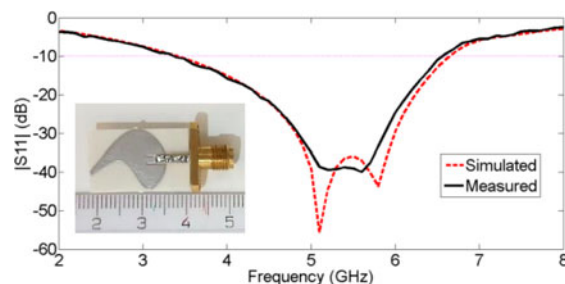


**Fig. 8.** The configuration of the designed rectenna (unit: mm). (b) The fabricated rectenna prototype.

is connected to the next section using stubs for transferring maximum power [38].

In order to realize the requirements of rectenna design, the first step is to develop and design the receiving antenna. Here, an inset microstrip-line-fed cypress-shaped monopole antenna is selected and applied which attend as a capable solution for these requirements as it is low-profile and compact size with the wideband operation and omnidirectional pattern [34, 39]. The inset feed method is applied, as it is easy to provide and implement a technique of impedance adjustment with a planar feed structure [40]. Figure 9 depicts the measured and simulated  $|S_{11}|$  of the receiving antenna, while the enclosure picture is independently given a fabricated antenna prototype to measure the performance of the antenna before it was linked straightforwardly to the proposed rectifier to produce the rectenna. It can be observed from the  $|S_{11}|$  plot that the designed antenna shows a wide bandwidth of 3 GHz with a central resonant frequency of 5.4 GHz. The dimensions of the designed receiving antenna are depicted in Fig. 8.

A high-performance rectifying circuit is very important in the rectenna design to get excellent power sensitivity, wideband operation, and a wide range of load with low power consumption [23]. Moreover, because of the non-linearity performance of the rectifier circuit, the rectifier input impedance differs in term of frequency, output load impedance, and also input power



**Fig. 9.** Measured and simulated  $|S_{11}|$  comparison of the receiving antenna. The scheme of fabricated antenna is shown in the inset photograph.

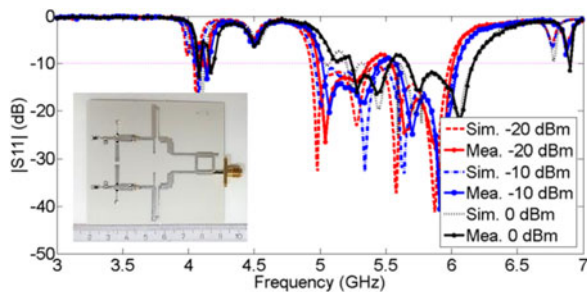
level [41]. Here, based on [31, 32], a proper coupler is designed and optimized that works at the working frequency and then two identical single rectifier circuits are linked to the output ports of the couplers in order to enhance the RF-dc efficiency by applying the reused reflected power in the different input power and termination load. In other words, the reflected waves from two sub-rectifiers can be partially reused by the coupler. In the configuration, as seen in Fig. 8, a wideband matching network is designed by applying an improved coupled-line using one stub with a short end before each of the branches of the divider as in [30, 42]. The coupled line can be complicated to realize the wideband transformation of impedance. In fact, it can create a broadband impedance matching through multiple frequencies that can be adjusted for wideband performance. It is also seen in Fig. 8 that two capacitors C1, for avoiding the DC power from returning back to signal source, can be considered as a DC-block. Meanwhile, capacitors C2 are applied as DC-pass filter to store the energy of the output DC and smooth waveform before the load  $R_L$ . It should be noted that in the following simulation, the model of capacitors is from MURATA and the dielectric loss of the substrate and the losses of the microstrip line are taken into account. The  $|S_{11}|$  and RF-dc efficiency of the designed rectifier circuit are optimized using the large-signal S-parameters and the harmonic-balance simulation in Keysight-ADS for various conditions such as different frequency bands, diverse levels of input power, and a wide range of output load. The optimized parameters of the proposed rectifier circuit after optimization are shown in Fig. 8 that are the result of a gradient algorithm-based multivariable optimization.

A separate fabricated rectifier circuit is given in the inset of Fig. 10 and the measured and simulated  $|S_{11}|$  of it at three levels of input power are depicted in the same figure. A separate rectifier prototype was first constructed to measure its performance before the receiving antenna is attached. A load resistance ( $R_L$ ) of 2.7 K $\Omega$  is selected as an optimal termination load. It can be observed that the rectifier circuit covers the desired band for different levels of input power of interest (-20 to 0 dBm). There is a little difference between simulated and measured data due to the unknown parasitic behavior of the SMD components applied in the rectifier [43].

The RF-dc conversion efficiency of the proposed circuit rectifier can be expressed by

$$\eta_{Rf-dc} = \frac{P_{dc}}{P_{in}}, \tag{3}$$

where,  $P_{in}$  is the input power, and  $P_{dc}$  is the output DC power. In the proposed design, the rectifier is linked straightforwardly to the single tone signal generator at a specific frequency in the desired band. The reason was to measure the efficiency of the proposed



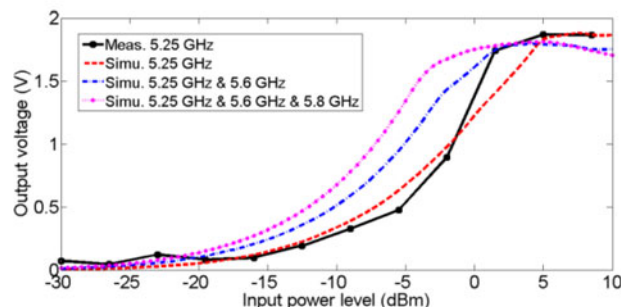
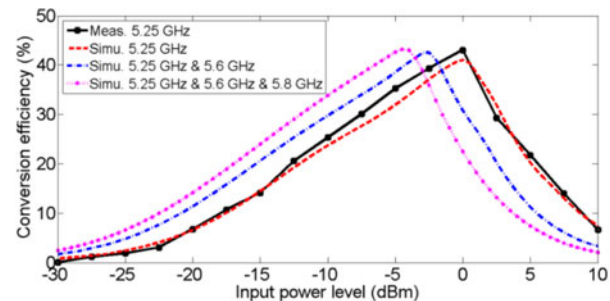
**Fig. 10.** The simulated and measured  $|S_{11}|$  of the stand-alone rectifier at three input power levels from  $-20$  to  $0$  dBm at  $5.6$  GHz with  $RL = 2.7$  k $\Omega$ . The fabricated prototype is shown in the inset photograph.

rectifier in term of the input power. Meanwhile, the DC voltage across RL is carried out by voltage-meter and the RF-dc conversion efficiency is given by

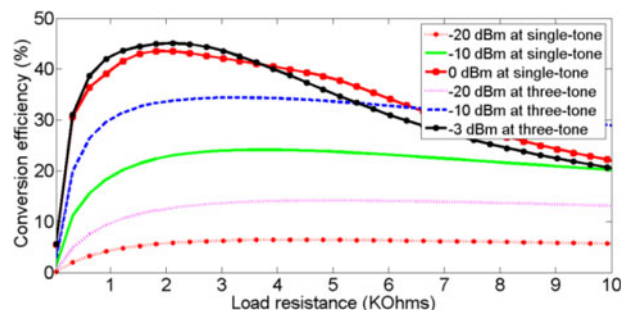
$$\eta(\%) = \frac{P_{out1} + P_{out2}}{P_{in}}, \quad (4)$$

where  $P_{out1}$  and  $P_{out2}$  are the output power of the two sub-rectifier circuits. It is also worth stating that the two DC outputs can be linked together in parallel or in series, providing additional selections for current or output voltage. In this work, the series connection is applied for the proposed design to double the output voltage so the optimal load resistor is doubled to  $5.4$  k $\Omega$ .

The conversion efficiency and the voltage across the termination load ( $RL = 2.7$  k $\Omega$ ) at single frequency input signal ( $5.25$  GHz), two-tone ( $5.25$  and  $5.6$  GHz), and three-tone ( $5.25$ ,  $5.6$ , and  $5.8$  GHz) input signals as a function of input power level are depicted in Figs 11(a) and 11(b), respectively. For single-tone input, the measured efficiency at  $5.25$  GHz is of around  $26\%$  and the measured output voltage is of around  $0.35$  V at an input power of  $-8.5$  dBm while the maximum efficiency and voltage can reach  $43\%$  and  $1.85$  V with  $0$  dBm input power, respectively. It can be also observed that the simulated conversion efficiency and output voltage are about  $33\%$  and  $0.7$  V in the  $-10$  dBm input power if the rectifier can receive the signals at the three bands simultaneously. Hence, in multi-tone cases, the efficiency and voltage improvements are about  $10\%$  and  $0.35$  V in the low-power levels, respectively. For the three-tone measurement, the three equal-power tones at  $5.25$ ,  $5.6$ , and  $5.8$  GHz are made from three signal generators and summed up using a combiner of power, then input to the rectifier circuit. But, here there is only one single tone signal generator in the laboratory and it is not possible to test multi-tone signals, therefore just the simulated results are presented. Next, the conversion efficiency is simulated as a function of output load impedance for multiple input power levels at a single frequency and three-tone input signals, as depicted in Fig. 12. Based on the results, the rectifier works within its maximum efficiency area, in the output load of  $1500$ – $4000$   $\Omega$  and it can be seen that the maximum efficiency is obtained at an optimal load of  $2.7$  k $\Omega$ . Since the proposed rectifier has more non-linear behavior at higher input power levels, the efficiency has dropped for higher output loads. But despite all these, the results confirm that the RF-triggered PMU efficiently works at much variable RL values for a vast range of input power which is very important in many real uses. Since the DC power and resulting voltage of rectifier circuit were not adequate to bias the applied Schottky diode, a DC-DC booster was necessary. It can be seen that the designed



**Fig. 11.** The simulated and measured (a) RF-dc conversion efficiency and (b) output voltage of the proposed rectifier versus input power levels at three cases for the load resistance of  $2.7$  k $\Omega$ .



**Fig. 12.** The simulated RF-dc conversion efficiency of the proposed rectifier versus output load at two cases for different input power levels.

rectifier circuit has generally the most compact size and high voltage compared with other lately reported designs at the  $5.6$  GHz [28, 44]. The decrease in the conversion efficiency than [28] is due to applying Rogers 4003 in the substrate instead of RT/Duroid 5880 which has lower quality.

### DC-DC power booster

Since the rectenna output voltage is usually lower than the level of practical voltages, a PMU with a DC-DC power boost converter is typically required to use the scavenged power of the proposed rectenna. So that in accordance with the PIN diode's datasheet, at least around  $1.1$  V and  $10$  mA current ( $11$  mW) are required to set the diode state to "ON" and effectively and dynamically change the operation mode of the UWB antenna.

Recently, the usage of a DC-DC power converter within a rectenna has been stated in a number of published papers [45–48]. For example, in [45], a DC-DC converter with switched capacitors in three stages is suggested. In [46, 47], BQ25504 as an ultra-low-power booster with a battery management is applied



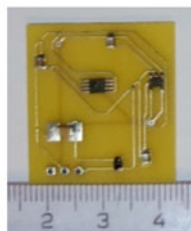
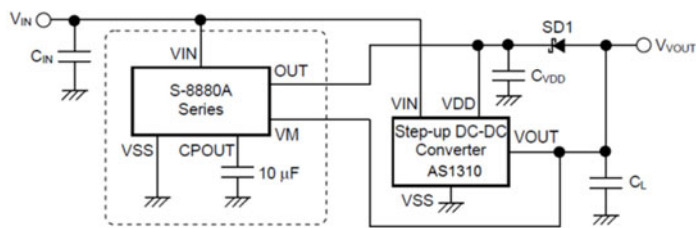


Fig. 13. Circuit diagram of the DC-DC power booster [33]. The fabricated prototype is also shown.

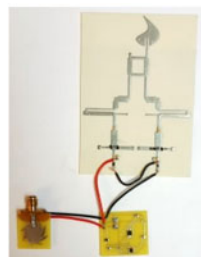
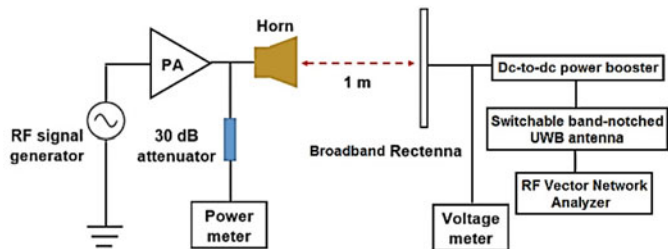


Fig. 14. Measurement setup for dynamically reconfigurable switchable band-notched UWB antenna. The fabricated combined module is also shown.

to charge a storage element such as a capacitor. In [48, 49], 1:100 step-up transformers with LTC3108 module have proposed an RF energy harvester. On the same lines, in [33], a PMU is applied including S-882Z and AS1310 modules for regulating the output voltage and boosting the ultra-low power.

In this work, as shown in Fig. 13, a PMU based on [33] is employed in the proposed design. AS1310 is a hysteric dc-dc booster with ultralow quiescent current ( $<1 \mu A$ ) and can be operated even when the harvested dc voltage is smaller than 0.7 V [50], while S-8880A is an efficient charge pump IC that enhances the capability of any dc-dc booster [51]. Actually, S-8880A is a new pin-compatible replacement for S-882Z compared to [33]. When a 0.35 V or higher voltage is scavenged, the circuit of oscillation inside S-8880A starts itself and generates a clock signal. Then the power activates to flow to AS1310 chip which is then transformed into a practical output DC voltage ( $V_{VOUT} = 1.8 V$  in this study) for powering up PIN diode. Their operation is completely explained in [33]. Four  $10 \mu F$  capacitors were associated in the layout to create an effective DC-DC power booster.  $C_{VDD}$ ,  $C_{IN}$ , and  $C_L$  are the capacitors as filters that bypass any noise or pulse to the ground plane. It should be distinguished that the CPOUT is a low-leakage capacitor to guarantee that sufficient energy is stored before AS1310 becomes active and feeds the PIN diode. The efficiency of the rectifier relies on the output load  $R_L$  and the boost converter impedance depends on the input voltage of  $V_{IN}$ . It must be considered that the input impedance of the DC-DC power booster differs between 3100 and 6900  $\Omega$  in the  $V_{IN}$  variation response in the 0–1.5 V range. Therefore, according to Fig. 12, the rectifier with the booster can operate in the region of maximum efficiency. Figure 11 also depicts that the rectifier efficiency for power levels near its sensitivity (−8.5 dBm at single tone, −12 dBm at two-tone, and −14 dBm at three-tone) remains almost its maximum value.

### Implementation of combined modules and testing

For the implementation of the dynamically reconfigurable switchable band-notched UWB antenna using RF PMU, the PIN diodes on the back side of the switchable band-notched UWB antenna, the rectenna, and the terminals of the DC-DC power converter should be connected together using insulated wires. In such a

case, the rectenna with the DC-DC power converter works as a PMU and can be applied to enable the sending of the dc power to the PIN diode used for the dynamic reconfiguration of the proposed UWB antenna designed in section “Switchable band-notched UWB antenna”. In the earlier sections, the UWB antenna, the rectenna, and the DC-DC power converter were discussed as separate devices independently.

The setup of measurement was realized inside an anechoic chamber as depicted in Fig. 14. The signal is produced by an RF signal generator and amplified by a power amplifier (PA) with 37 dB gain, and transmitted using a horn standard antenna. The distance between the rectenna and the transmitting horn antenna ( $G_t = 12.2$  dBi) is 1 m. The transmitting power to the rectenna has been measured using a power meter while the received power is accounted by using the equation of Friis transmission:

$$P_{rec} = G_{rec} + P_t + G_t + 20 \log_{10} \frac{\lambda}{4\pi r}, \quad (5)$$

where  $P_{rec}$  is the rectifier input RF power in dBm,  $P_t$  is the horn antenna transmitting power in dBm,  $G_{rec} = 1.4$  dBi is the realized peak gain of the rectenna,  $G_t$  is the realized peak gain of the horn antenna in dBi,  $\lambda$  is the interest wavelength, and  $r$  is the distance ( $r = 1$  m). The output voltage of the DC-DC power booster and the rectified accessible power at the distance  $r$ , on the output load are examined by a voltage meter. During the process of measurement, it was seen that when the rectifier output voltage (which is also the enabling voltage of the DC-DC converter) rose over 0.36 V, the convertor effectively activated the PIN diode that was employed on the switchable UWB antenna. The experiment was repeated for different levels of power at 5.25 GHz and the measurement results. This repetition continued and it is seen that the PIN diode actuation was efficient enough when the incident power  $P_{rec}$  at the rectenna was more than −8 dBm for single-tone.

### Conclusion

In this paper, a novel UWB planar antenna using a dynamically switchable notch-band at 5–6 GHz with a broadband rectenna and a passive DC-DC convertor has been introduced and



designed. The dynamic notch enables when exposed to the external single-tone RF signal  $\geq -8.5$  dBm, while it can be active with the power levels of  $-12$  dBm at two-tone signals and  $-14$  dBm at three-tone signals. The UWB antenna is realized by a whirligig-shaped radiation patch and band-notched performance is obtained by etching a reconfigurable DGS-SIR on the ground plane. The PIN diode switch without any biasing network is used for electrically connecting (“ON” state) or disconnecting (“OFF” state) the resonator slot and therefore switches between the single band-rejection mode and the UWB mode, respectively. The diode biasing can occur dynamically when the interfering signal is sensed and received from a broadband rectenna that contains a cypress-shaped monopole antenna and two sub-rectifiers which are connected together with a 3 dB branch-line coupler. So the rectified 5–6 GHz RF input signal from the rectifier with a dc output voltage of 0.37 V through a 5.4 k $\Omega$  output load is used as an input voltage to a cascaded passive DC-DC converter with an output terminal linked to the PIN diode biasing and turn it to the “ON” state with a single band-notched UWB radiator mode. When the wirelessly received RF signal at 5–6 GHz is stopped, the antenna becomes a common UWB radiator mode. Therefore, the dynamically switchable UWB antenna allows the 5–6 GHz pass-band only when an RF signal in this band is not received.

**Acknowledgement.** The author gratefully acknowledges the experimental support provided by Sharif Center for Laboratory Services (Center Lab) at the Sharif University of Technology and Antenna Laboratory at Iran Telecommunication Research Center (ITRC). This research has also been supported by the University of Garmsar.

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