# **RESEARCH PAPER**

# On the oscillation limits of HBT cross-coupled oscillators

ATHEER BARGHOUTHI, CORRADO CARTA, UDO JÖRGES AND FRANK ELLINGER

In evaluating the potential of a given integrated-circuit technology, the negative conductance approach is used to estimate a maximum frequency limit for the ability of Heterojunction Bipolar Transistor (HBT) cross-coupled architecture to function as an oscillator at high frequencies. In this paper, the simple low-frequency estimation of the negative conductance, commonly used in the literature, is extended by deriving a new expression that takes into account the HBT parasitics. An expression for the frequency at which the negative conductance of the circuit crosses zero is given, which gives maximum frequency of oscillation for an ideal tank (no tank losses). In addition, the effect of the feedback capacitor, which is commonly added for direct current (DC) decoupling and boosting the negative conductance, is analyzed and new insight on the choice of the value of this capacitance is introduced. The results were proven by simulations using SiGe HBT transistors with an  $f_t$  of 190 GHz.

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# I. INTRODUCTION

High-frequency LC-tank is a well known circuit of inductor and capacitor, no expansion oscillators are key components of widely adopted architectures for wireless communication systems. Their performance is crucial in many applications such as wireless transceivers, modulators, and radar systems. Furthermore, the availability of larger bandwidths at higher frequencies, such as the 60 GHz frequency band, combined with the growing demand for higher data rates makes it necessary to design such oscillators for operation closer to the limits of the available technology. Several kinds of LC-tank oscillators can be found in the literature, such as the cross-coupled and the Colpitts oscillators. Even though it was shown in [1] that the Colpitts oscillator outperforms the cross-coupled oscillator in terms of phase noise, which makes it a better choice, the simpler architecture of the crosscoupled oscillator makes it a more favorable choice in applications where phase noise is not of high concern. Therefore, it is very beneficial to estimate the frequency limits of this topology to perform as an oscillator. In [2], an expression for the negative resistance of Colpitts oscillators at high frequencies was derived. In [3], Barhausen's criteria were used to derive expressions for the oscillation conditions of Colpitts oscillator. In [4], large signal analysis for Complementary Metal Oxide Semiconductor (CMOS) cross-coupled oscillators was presented. Unfortunately, a high-frequency analysis of the negative conductance of HBT cross-coupled oscillators, to the knowledge of the authors, has never been considered. Even

Chair for Circuit Design and Network Theory, Dresden University of Technology, Helmholzstrasse 18, 01069 Dresden, Germany. Phone: +49 351 463 32702 **Corresponding author:** 

F. Ellinger

Email: frank.ellinger@tu-dresden.de

though, such analysis can bring a lot of insight into the frequency limits of the topology. In this paper, we derive analytical expressions to show how the HBT transistor parasitics impose a limit on the ability of the cross-coupled topology to oscillate at higher frequencies. This is shown by deriving an improved estimation of the negative conductance, which is valid at high frequencies, and using it to find an expression for the frequency at which the negative conductance crosses zero.

### II. CIRCUIT ANALYSIS

The cross-coupled oscillator architecture is shown in Fig. 1. In order to understand its behavior at high frequencies, the negative conductance approach is used for the circuit analysis. The differential circuit small signal model is shown in Fig. 2. First, the circuit is analyzed by considering a short circuit in-place of  $C_{mp}$  and then, the effect of  $C_m$  is taken into account.

The negative conductance of the oscillator can be defined as

$$G_{neg} = R_e[\underline{Y}_{in}] = R_e[\frac{1}{\underline{Z}_{in}}] = R_e\left[\frac{\underline{I}_{in}}{2\underline{V}_1}\right]$$
(1)

In the literature [5–7], equation (2) is commonly used in estimating the negative conductance  $G_{neg}$  of the oscillator:

$$G_{neg} = -\frac{g_m}{2} \tag{2}$$

where  $g_m$  is the transistor trans-conductance.

Unfortunately, this is not valid for high frequencies, due to the effect of transistor parasitics, such as the base resistance



Fig. 1. Cross-coupled VCO with  $C_{bc}$  compensation.

 $R_b$ , the base emitter capacitance  $C_{be}$ , the base collector capacitance  $C_{bc}$ , the collector resistance  $R_c$  (not shown in Fig. 2), and the emitter resistance  $R_e$ . Even at low frequencies and small transistor size, the estimation is inaccurate due to  $R_e$ . In the following, the effect of the parasitics and  $C_m$  is analyzed:

1. The effect of  $R_e$ : by neglecting all other parasitics and replacing  $C_m$  with a short circuit, equation (3) can be derived. It can be concluded that  $R_e$  reduces  $G_{neg}$  by a factor of  $1/(1 + g_m R_e)$  for low frequencies.

$$G_{neg} = -\frac{g_m}{2} \left( \frac{1}{1 + g_m R_e} \right). \tag{3}$$

2. The effect of  $C_{be}$  and  $R_b$ : equation (3) predicts a flat negative conductance over the whole frequency range, in reality the magnitude of the negative conductance decreases with

frequency. This is explained by analyzing the circuit taking into account  $C_{be}$  and  $R_b$ , while neglecting the other effects. As a result, equation (3) has to be modified to

$$G_{neg} = \frac{1}{2} \left( \frac{-g_m + \omega^2 R_b C_{be}^2}{\omega^2 C_{be}^2 R_b^2 + 1} \right).$$
(4)

As can be noticed, a pole that decreases the magnitude of the negative conductance appears in the equation. The pole-frequency is

$$f_{pole} = \frac{1}{2\pi R_b C_{be}}.$$
(5)

The  $G_{neg}$  crosses zero at a frequency of

$$f_{zero,neg} = \frac{1}{2\pi} \sqrt{\frac{g_m}{C_{be}^2 R_b}} = \sqrt{f_t f_{pole}},\tag{6}$$

where  $f_t$  is the transit frequency of the technology.

3. Taking the effect of  $C_{be}$ ,  $R_b$ , and  $C_{bc}$  into account, the expressions for  $G_{neg}$  and  $f_{pole}$  are further modified as

$$G_{neg} = \frac{1}{2} \left( \frac{-g_m + \left( (C_{tot} + C_{bc})^2 + C_{bc} C_{tot} g_m R_b \right) R_b \omega^2}{1 + \omega^2 R_b^2 C_{tot}^2} \right),$$
(7)

$$f_{pole} = \frac{1}{2 \, \pi R_b C_{tot}},\tag{8}$$

where  $C_{tot} = C_{be} + C_{bc}$ . From (7), an estimation of the upper limit of the negative conductance can be derived as a function of the technology parameters. The designer can use such an upper limit to have a first judgment about the feasibility of using the cross-coupled oscillator for the target frequency, by comparing the tank losses



Fig. 2. Cross-coupled VCO small signal model with Cbc compensation.

$$G_{neg} = -\frac{1}{2} \left( \frac{g_m}{1 + \omega^2 R_b^2 C_{tot}^2} \right) \tag{9}$$

and the  $G_{neg}$  crosses zero at a frequency of

$$f_{zero,neg} = \frac{1}{2\pi} \sqrt{\frac{g_m}{((C_{tot} + C_{bc})^2 + C_{bc}C_{tot}g_m R_b)R_b}}.$$
 (10)

4. The effect of feedback capacitance on the  $G_{neg}$  of the oscillator: in order to boost the ability of a cross-coupled oscillator to give negative conductance at higher frequencies and to decouple the bias from the outputs, a capacitor  $C_m$  is commonly inserted in the feedback path to eliminate the effect of  $C_{bc}$ , as shown in Fig. 1. Circuit analysis shows that the pole-frequency shifts to a frequency of

$$f_{pole} = \frac{1}{2\pi R_b \left( C_{tot} C_m / (C_{tot} + C_m) \right)}.$$
 (11)

The upper limit of negative conductance is

$$G_{neg} = -\frac{1}{2} \frac{g_m (C_m - C_{bc}) / (C_{tot} + C_m)}{1 + \omega^2 R_b^2 \left(\frac{C_m C_{tot}}{C_m + C_{lot}}\right)^2}, \quad C_m > C_{bc} \quad (12)$$

and the  $G_{neg}$  crosses zero at a frequency of

$$f_{zero,neg} = \frac{1}{2\pi} \sqrt{\frac{g_m (C_m - C_{bc})(C_m + C_{tot})}{(C_{tot} + C_{bc})C_m^2 R_b (C_{tot} + C_{bc} + g_m R_b)}}{\times (C_{tot} C_{bc} / (C_{tot} + C_{bc})))}}.$$
(13)

#### III. SIMULATIONS

In this section, the analytical derivations are validated and illustrated by simulations, which rely on the compact model of the HBT of a commercially available technology. The simulation results correspond to a transistor with the following parameters:  $R_b = 17 \Omega$ ,  $C_{bc} = 18$  fF,  $C_{be} = 195$  fF,  $R_e = 3.2 \Omega$ ,  $R_c = 11.4 \Omega$ , and  $g_m = 257$  mS. However, the analysis results were successfully validated for several other transistor sizes, all available in the same integrated circuit (IC) technology. As can be noticed in Fig. 3,  $f_{zero,neg}$  takes all effects into account at around 65 GHz. When only  $C_{be}$  and  $R_b$  are taken into account (equation (6)) the estimated  $f_{zero,neg}$  is 100 GHz, when  $C_{bc}$  effect is added (equation (10)), the estimated  $f_{zero,neg}$  is 76 GHz.

The effect of  $C_m$  was also evaluated. As can be noticed from Fig. 4, where the conductance of the circuit is plotted versus frequency for different values of  $C_m$ , using  $C_m = 17$  fF  $\cong C_{bc}$  generates positive values for the conductance, as closely predicted by (12). Using larger values for  $C_m$  will finally bring the conductance to the short-circuited case, as in (7).

It can also be deduced that, even though using  $C_m$  improves the magnitude of negative conductance at higher frequency, the magnitude can still be low and insufficient for compensating the tank losses. In addition to negative conductance simulations, transient simulations were performed to support the



Fig. 3. Parasitic effects on the negative conductance of the cross-coupled oscillator curve 1 (simulated all effects), curve 2 (equation (7)), and curve 3 (equation (4)).



Fig. 4. The effect of  $C_m$  on the negative conductance of the cross-coupled oscillator.

conclusions made in this section. An ideal inductor was used to resonate with the capacitance, which corresponds to the imaginary part of  $Y_{in}$ . The inductor was scaled to the minimum value that allows oscillation (corresponding to the highest frequency of oscillation). The following two cases were considered:

- 1. The short circuited  $C_m$ : as can be seen in Fig. 5 the oscillation frequency corresponds to the predictions of the negative conductance.
- 2.  $C_m = 42.5$  fF: (Fig. 6).

# IV. CONCLUSION

The paper brings more insight into the frequency limits of the cross-coupled oscillator at high frequencies than already available. An upper bound of the negative conductance was derived, which helps the designer to check for the suitability of the architecture at the target frequency and enables quick technology evaluation and comparison. A maximum frequency  $f_{zero,neg}$  at which the circuit can provide negative conductance is given in (10) and (13). It should be noted that even though oscillation at frequencies higher than  $f_{zero,neg}$  is not



Fig. 5. Simulated highest frequency of oscillation (64 GHz).



Fig. 6. Simulated highest frequency of oscillation (98 GHz).

possible, the magnitude of the negative conductance might be too low to sustain oscillation at frequency below this upper limit. On the other hand, the presented results provide a simple expression of the negative conductance values available as a function of the target frequency of operation. It was also shown that choosing  $C_m$  smaller than  $C_{bc}$  compromises the ability of the circuit to present negative conductance to the resonant tank and is thus incompatible with oscillation.

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#### APPENDIX A

In this appendix, equations (7)–(10) are derived. Analyzing the circuit in Fig. 2 using nodal analysis in the Laplace domain generates two different equations, one for each node in the circuit (only two nodes are needed because the circuit is symmetric). In this appendix, the capacitor  $C_m$  is replaced by a short circuit and  $R_e$  is neglected.

1.  $\underline{V}_1$  node:

$$\underline{I}_{in} = \frac{\underline{V}_1 - \underline{V}_{be}}{r_b} - g_m \underline{V}_{be} + sC_{bc} (\underline{V}_1 + \underline{V}_{be}), \qquad (A.1)$$

which simplifies to

$$\underline{I}_{in} = \left(s(C_{bc} + C_{tot}) - g_m\right)\underline{V}_{be} + 2sC_{bc}\underline{V}_1.$$
(A.2)

2. <u>V</u><sub>be</sub> node:

$$\frac{\underline{V}_{1} - \underline{V}_{be}}{r_{b}} + sC_{be}\underline{V}_{be} + sC_{bc}\underline{V}_{be} + sC_{bc}\underline{V}_{be} = 0, \quad (A.3)$$

which simplifies to

$$\underline{V}_{be} = \frac{1 - r_b s C_{be}}{1 + r_b s C_{tot}} \underline{V}_1. \tag{A.4}$$

By substituting (A.4) into (A.2) and solving for  $\underline{Y}_{in}$ , equation (A.5) is found

$$\underline{Y}_{in}(s) = \frac{1}{2} \begin{pmatrix} -g_m + s \left( C_{bc} + C_{tot} - g_m r_b C_{bc} \right) \\ -r_b C_{bc} (C_{bc} + C_{tot}) s^2 \\ 1 + r_b C_{tot} s \end{pmatrix}$$
(A.5)

By substituting  $j\omega$  instead of *s* in (A.5) and separating the equation into the real and imaginary parts,  $G_{neg}$  can be calculated, as given in (7). By separating (7) into two parts, a negative and a positive part, the upper limit of the negative conductance can be found by using the negative part, which is given in (9). In order to derive (10), equation (7) is set to zero and solved for the frequency,  $f_{zero,neg}$  can then be found.

#### APPENDIX B

In this appendix, equations (11)-(13) are derived. Analyzing the circuit in Fig. 2 by using nodal analysis in the Laplace domain generates two different equations, one for each node in the circuit (only two nodes are needed because the circuit is symmetric). In this appendix, *Cm* is taken into account and  $R_e$  is neglected.

1.  $\underline{V}_1$  node:

$$\underline{I}_{in} = \frac{\underline{V}_1 - \underline{V}_{be}}{\underline{Z}_f} - g_m \underline{V}_{be} + sC_{bc} (\underline{V}_1 + \underline{V}_{be})$$
(B.1)  
ere  $\underline{Z}_f = r_b + \frac{1}{sC_m}$ .

2. <u>V</u><sub>be</sub> node:

wh

$$\frac{\underline{V}_{1} - \underline{V}_{be}}{\underline{Z}_{f}} + sC_{be}\underline{V}_{be} + sC_{bc}\underline{V}_{be} + sC_{bc}\underline{V}_{be} = 0$$
(B.2)

which simplifies to

$$\underline{V}_{be} = \frac{(C_m + C_{bc}) - r_b s C_{be} C_m}{(C_m + C_{tot}) - r_b s C_{tot} C_m} \underline{V}_1$$
(B.3)

By eliminating  $\frac{\underline{V}_1 - \underline{V}_{be}}{\underline{Z}_f}$  using (B.1) and (B.2), equation (B.4) is found

$$\underline{I}_{in} = -g_{in}\underline{V}_{be} + sC_{bc}(\underline{V}_1 + \underline{V}_{be}). \tag{B.4}$$

By substituting (B.3) into (B.4) and solving for  $Y_{in}$ , equation (B.5) is found.

$$\underline{Y}_{in}(s) = \frac{1}{2} \left( -g_m + s(C_{bc} + C_{tot}) \right) \\ \times \left( \frac{(C_m + C_{bc}) - r_b s C_{bc} C_m}{(C_m + C_{tot}) + r_b s C_{tot} C_m} \right) + s C_{bc}.$$
(B.5)

By substituting  $j\omega$  instead of *s* in (B.5) and separating the equation into the real and imaginary parts,  $G_{neg}$  can be calculated, as given in (B.6).

$$G_{neg} = \begin{pmatrix} \frac{-g_m(C_m - C_{bc})}{(C_{tot} + C_m)} \\ + \left( \frac{r_b C_{tot} C_m^{\ 2} (C_{tot} + C_{bc} + C_{bc} r_b g_m)}{(C_{tot} + C_m) r_b C_m^{\ 2} C_{bc}} \omega^2 \right) \\ \frac{1 + \left( \frac{C_{tot} C_m}{C_{tot} + C_m} \right)^2 r_b^{\ 2} \omega^2}{(C_{tot} + C_m)^2} \end{pmatrix}.$$
(B.6)

By separating (B.6) into two parts, a negative and a positive part, the upper limit of the negative conductance can be found by using the negative part, which is given in (12). In order to derive (13), equation (B.6) is set to zero and solved for the frequency,  $f_{zero,neg}$  can then be found.

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Atheer Barghouthi received his B.Sc. in electrical engineering in 2004 from Birzeit University in Palestine with distinction and received his M.Sc. in microelectronics and microsystems from Hamburg University of Technology in 2008 with distinction. Currently, he works for his Ph.D. as a scientific research employee in Dresden University

of technology in the field of RF circuit design.



**Udo Jörges** was born in Steinach (Thuringia), Germany, in 1952. He received the Diploma degree in electrical engineering, Ph.D. degree, and Habilitation degree in electrical engineering from the Dresden University of Technology, Dresden, Germany, in 1975, 1979, and 1985, respectively. Since 1998, he has been an Associate Professor, and since

2006, he is with the Chair for circuit design and network theory at Dresden University of Technology. His main research interests are in the field of circuit theory and the design of analog ICs.



**Corrado Carta** was born in Cagliari, Italy. He received the Master degree in Electrical Engineering from the University of Cagliari, Italy, in 2000 and the Ph.D. degree from the Swiss Federal Institute of Technology (ETH) Zurich, Switzerland, in 2006. From July 2000 to February 2006, he was with the Microwave Electronics Group of ETH

Zurich, where his main research interests were in the field of silicon-based RFIC design for wireless communications. From April 2006 to May 2008, he was with the High-Speed Electronics Group at the Electrical and Computer Engineering Department of the University of California, Santa Barbara, where his research work focused on the design of silicon-based integrated-circuits for very large mm-wave phased arrays. In June 2008, he joined Sonos Inc., where he led the RF and compliance team, involved in the development and characterization of the wireless interface of new and existing products. In March 2010, he joined the Chair for Circuit Design and Network Theory at TU Dresden, where he is currently leading the mm-wave IC design group.



**Frank Ellinger** was born in Friedrichshafen, Germany, in 1972. He graduated in electrical engineering (EE) in 1996 from the University of Ulm, Germany. He received an MBA and a PhD degree in EE from the ETH Zürich (ETHZ), Switzerland, in 2001, and the habilitation degree in high frequency circuit design from the ETHZ in 2004. Since August

2006 he is a full-time professor and head of the Chair for Circuit Design and Network Theory at the Dresden University of Technology, Germany. Currently, he is member of the management board and coordinator of the communications area in the German excellence cluster project Cool Silicon. From 2001 to 2006, he was head of the RFIC design group of the Electronics Laboratory at the ETHZ, and a project leader of the IBM/ETHZ Competence Center for Advanced Silicon Electronics hosted at IBM Research in Rüschlikon. Prof. Ellinger has been coordinator of the EU funded projects RES-OLUTION and MIMAX. He published more than 170 refereed scientific papers, most of them IEEE journal contributions, and three patents. Prof. Ellinger authored the lecture book "Radio Frequency Integrated Circuits and Technologies" published by Springer. He was elected by the IEEE MTT S as IEEE Distinguished Microwave Lecturer for 2009–2011.