RESEARCH PAPER

Pareto optimization of radar receiver low-noise amplifier source impedance for low noise and high gain

CHARLES BAYLIS¹, ROBERT J. MARKS II¹ AND LAWRENCE COHEN²

In radar receivers, the low noise amplifier (LNA) must provide very low noise figure and high gain to successfully receive very low signals reflected off of illuminated targets. Obtaining low noise figure and high gain, unfortunately, is a well-known trade-off that has been carefully negotiated by design engineers for years. This paper presents a fundamental solution method for the source reflection coefficient providing the maximum available gain under a given noise figure constraint, and also for the lowest possible noise figure under a gain constraint. The design approach is based solely on the small-signal S-parameters and noise parameters of the device; no additional measurements or information are required. This method is demonstrated through examples. The results are expected to find application in design of LNAs and in real-time reconfigurable amplifiers for microwave communication and radar receivers.

Keywords: Amplifiers, Circuit design, Circuit theory, Low noise amplifiers, Microwave circuits, Optimization

Received 6 March 2015; Revised 8 October 2015; Accepted 16 October 2015; first published online 20 November 2015

I. INTRODUCTION

Radar receivers must receive very low-power signals and discern them from the noise. Radar received signal strength is related to the transmitted power by the inverse of the distance raised to the fourth power, whereas communication received signal strength is related to transmitted power by the inverse of the distance squared. Low-noise amplifiers (LNA) are finding significant contemporary application in radar receivers, including automotive radar at 75-77 GHz [1, 2], X-band radar transmit-receive modules near 10 GHz [3], and near-space radar applications [4]. Dawood and Narayanan demonstrate that the output of the radar correlation operation (used for signal identification) is related to the signal-to-noise ratio at the input to the correlator [5]. Since noise figure is a key issue in LNA design, this means that the front-end LNA plays a significant role in the back-end detection capability of a radar system.

Both the noise figure and available gain of an LNA are functions of the source reflection coefficient Γ_s [6]. The designer must decide between choosing $\Gamma_s = \Gamma_{opt}$ for minimum noise figure, selecting Γ_s for optimum gain, or finding a compromise. In many cases, a compromise is necessary, as it is desired to find the highest possible value of

²Naval Research Laboratory, DC, District of Columbia, USA Corresponding author:

R.J. Marks

Email: RJMarksII@gmail.com

available gain G_A while insuring that the noise figure F is below the design limitations.

The literature shows work in optimization and estimation of constrained optima, but, to our knowledge, does not present an analytical solution for the optimal design value of Γ_s . Fukui demonstrates the plotting of noise figure and gain circles on the Smith chart for design purposes [6]. Nieuwoldt et al. describe a Pareto optimization approach using Sequential Quadratic Programming and the Normal Boundary Intersection Method; their optimization is applied to objective functions that include noise figure, gain, and power dissipation under constraints of output and input impedance matching networks and also limitations on component values [7]. The same group demonstrates a wideband LNA synthesis approach for multiple performance measures in [8]. Nguyen et al. compare four different LNA optimization techniques used in CMOS designs, including a technique that uses source degeneration to try to make the optimum impedances for noise figure and input voltage standing-wave-ratio similar [9]. For Si and Ge transistors, Fukui provides expressions for noise figure based on a small-signal model representation of the device, and also investigates the optimal current [10]. He also discusses the optimization of gate length in GaAs metal semiconductor field-effect transistors [11]. Hashemi and Hajimiri describe gain and noise figure design for concurrent multiband LNAs based on the equivalent circuit model and noise equivalent circuit for the active device [12].

Niu et al. demonstrate how designing for low noise figure affects the potential to achieve high power gain [13]. Gonzalez describes design issues for noise and gain in his book on linear transistor amplifier design, and demonstrates

¹Department of Electrical and Computer Engineering, Baylor University, Waco, Texas, USA

optimization for different noise and gain requirements by drawing a line from the optimum gain source impedance to the optimum noise figure source impedance in the Smith chart [14] and calculating variations of gain and noise figure along the line. While many have used rule-of-thumb approximations, the present paper shows from the theory that the Pareto trade-off need not be approximated, but can be directly obtained from the S-parameters and noise parameters.

This paper describes how to directly find the source reflection coefficient providing the largest available gain while meeting noise figure constraints, or the source reflection coefficient providing the smallest noise figure while meeting available gain constraints. This is a constrained optimization problem based on two convex sets (gain and noise figure). Pareto optimization involves finding a tradeoff between two conflicting objectives. Miettenen discusses multiple-objective optimization and provides examples from economics, mathematics, and engineering [15]. A similar situation requiring the use of Pareto analysis is the optimization of load reflection coefficient for linearity and efficiency [16-19] in power amplifiers. However, the power amplifier problem is a nonlinear optimization that must be performed sequentially. For source impedance optimization in LNAs, linear behavior is assumed, allowing the Pareto optimum to be found analytically using the S-parameters and noise parameters.

This paper presents theory that can be applied in many various ways in LNA design and optimization. One particularly useful application is reconfigurable LNAs. In a reconfigurable LNA, many of the parameters involved in a design are fixed, such as bias, source inductance, stabilization networks, and other parameters. However, the source impedance may be constructed from reconfigurable elements to allow tuning around the Smith chart in real-time. In such case, the approach presented in this paper is useful in finding the value of Γ_s providing maximum gain under noise figure constraints or providing minimum noise figure under gain constraints. The requirements may change in mobile systems due to different expected signal-to-noise ratio of the input signal and requirements for signal-to-noise ratio of the RF front end.

Section II provides a theoretical description of the gain versus noise-figure tradeoff and derives equations to be solved to find the optimum noise figure given available-gain constraints or optimum available gain given noise-figure constraints. Section III presents examples of how this technique can be used for design in cases where the device is unconditionally stable and potentially unstable. Section IV provides conclusions regarding the work.

GAIN AND NOISE FIGURE

The locus of points providing a constant available gain G_A in a linear amplifier is a circle in the Smith chart [14]. In addition, the locus of points providing a constant specified noise figure N_i is also a circle in the Smith chart [14]. As such, Fig. 1 depicts the situation in which circles of constant G_A and N_i are drawn in the Smith chart. The equations for the center C_a and radius r_a of a given G_A circle are given in terms of the value of the normalized available gain, $g_a = G_A/|S_{21}|^2$, as follows [14]:

$$C_a = \frac{g_a C_1^*}{1 + g_a (|S_{11}|^2 - |\Delta|^2)},\tag{1}$$

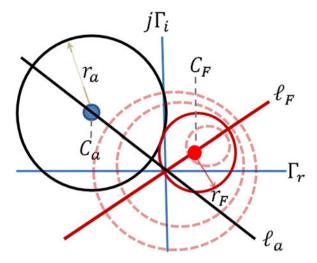


Fig. 1. Circles of constant gain and noise figure on the complex Γ_s plane (the Smith chart).

$$r_{a} = \frac{\left[1 - 2K|S_{12}S_{21}|g_{a} + |S_{12}S_{21}|^{2}g_{a}^{2}\right]^{1/2}}{\left|1 + g_{a}(|S_{11}|^{2} - |\Delta|^{2})\right|},$$
 (2)

where $\Delta=S_{11}S_{22}-S_{12}S_{21},~K=(1-|S_{11}|^2-|S_{22}|^2+|\Delta|^2)/(2|S_{12}S_{21}|),$ and $C_1=S_{11}-\Delta S_{22}^*.$

Additionally, the center and radius of circles of constant noise figure are given in terms of the noise measure

$$N_i = \frac{F_i - F_{min}}{4r_n} \left| 1 + \Gamma_{opt} \right|^2, \tag{3}$$

where F_i is the noise figure and F_{min} , Γ_{opt} , and r_n are the noise parameters of the device representing the minimum noise figure, optimum reflection coefficient for noise figure, and normalized noise resistance, respectively. The equations for the center and radius of the noise figure circles, based on the noise measure, are given as follows [14]:

$$C_F = \frac{\Gamma_{opt}}{1 + N_c},\tag{4}$$

$$C_{F} = \frac{\Gamma_{opt}}{1 + N_{i}},$$

$$r_{F} = \frac{1}{1 + N_{i}} \sqrt{N_{i}^{2} + N_{i} (1 - |\Gamma_{opt}|^{2})}.$$
(5)

Equations (1) and (4) show that both the gain and noise circles are centered along straight lines emanating from the origin of the complex Γ_s plane, as shown in Fig. 1.

A point in the Γ_s plane is on the locus of Pareto optimum points if and only if one of the F_i circles is tangent to one of the G_A circles at that point. For this to be true, the distance between the center of the G_A circle and the center of the F circle must equal the sum of the radii of the circles. The distance between the circle centers, from equations (1) and (4), is given by

$$D = |C_a - C_F| = \left| \frac{g_a C_1^*}{1 + A g_a} - \frac{\Gamma_{opt}}{1 + N_i} \right|, \tag{6}$$

where $A = |S_{11}|^2 - |\Delta|^2$. Optimality occurs when $D = r_a + r_F$,

(1)
$$\left| \frac{g_a C_1^*}{1 + Ag_a} - \frac{\Gamma_{opt}}{1 + N_i} \right| = \frac{\sqrt{1 - ag_a - bg_a^2}}{|1 + Ag_a|} + \frac{\sqrt{N_i^2 + cN_i}}{1 + N_i},$$
 (7)

using equations (2) and (5), where $a = 2K|S_{11}S_{21}|$, $b = -|S_{12}S_{21}|^2$, and $c = 1 - |\Gamma_{opt}|^2$. Multiplying both sides of (7) by $(1 + N_i)|1 + Ag_a|$ and squaring both sides gives

$$|s(1+N_i)g_aC_1^* - |1+Ag_a|\Gamma_{opt}|^2$$

$$= \left[(1+N_i)\sqrt{1-ag_a-bg_a^2} + |1+Ag_a|\sqrt{N_i^2+cN_i} \right]^2,$$
(8)

where $s = \text{sign}(1 + Ag_a) = \pm 1$. Since, for any complex numbers z_1 and z_2 ,

$$|z_1 - z_2|^2 = |z_1|^2 + |z_2|^2 - 2\Re z_1 z_2^*.$$
 (9)

Equation (8) can be written as

$$\begin{aligned} &(1+N_{i})^{2}g_{a}^{2}|C_{1}|^{2}+|1+Ag_{a}|^{2}|\Gamma_{opt}|^{2}\\ &-2sg_{a}|1+Ag_{a}|(1+N_{i})\Re(C_{1}\Gamma_{opt})\\ &=(1+N_{i})^{2}(1-ag_{a}-bg_{a}^{2})+|1+Ag_{a}|^{2}(N_{i}^{2}+cN_{i})\\ &+2(1+N_{i})|1+Ag_{a}|\sqrt{(1-ag_{a}-bg_{a}^{2})(N_{i}^{2}+cN_{i})}. \end{aligned} \tag{10}$$

There are two cases of interest for LNA design: (1) optimization of the noise measure N_i while bounding the gain g_a and (2) optimization of g_a while bounding N_i .

Case 1: bound g_a and optimize N_i .

A lower bound is placed on the normalized available gain g_a , and under this constraint, it is desired to minimize the noise figure F_i and hence the noise measure N_i . With g_a as a fixed value, an expression in terms of N_i can be developed. Equation (10) can be rewritten as a fourth-order polynomial in N_i :

$$A_4 N_i^4 + A_3 N_i^3 + A_2 N_i^2 + A_1 N_i + A_0 = 0, (11)$$

where

$$A_4 = (\alpha + \gamma)^2 + 4\gamma\delta,\tag{12}$$

$$A_3 = 2(\alpha + \gamma)(2\alpha - \beta + c\gamma) + 4\gamma \delta(2 + c), \tag{13}$$

$$A_{2} = 2(\alpha - \beta - \gamma |\Gamma_{opt}|^{2})(\alpha + \gamma) + (2\alpha - \beta + c\gamma)^{2} + 4\gamma \delta(1 + 2c),$$
(14)

$$A_1 = 2(2\alpha - \beta + c\gamma)(\alpha - \beta - \gamma |\Gamma_{opt}|^2) + 4\gamma\delta c, \quad (15)$$

$$A_0 = \left(\alpha - \beta - \gamma |\Gamma_{opt}|^2\right)^2 + 4\gamma\delta,\tag{16}$$

$$\alpha = g_a^2 |C_1|^2 - \delta, \tag{17}$$

$$\beta = 2sg_a |_1 + Ag_a |_{\mathcal{R}} (C_1 \Gamma_{opt}), \tag{18}$$

$$\gamma = -\left|1 + Ag_a\right|^2,\tag{19}$$

$$\delta = 1 - ag_a - bg_a^2. \tag{20}$$

Case 2: bound N_i and optimize g_a .

Because $s^2 = 1$, $|1 + Ag_a| = s(1 + Ag_a)$, if N_i is assigned a fixed, limiting value, equation (10) can be rewritten (using the observation that $s^2 = 1$) as a fourth-order polynomial in g_a :

$$B_4 g_a^4 + B_3 g_a^3 + B_2 g_a^2 + B_1 g_a + B_0 = 0, (21)$$

vhere

$$B_4 = -b\nu A^2 - \lambda^2, \tag{22}$$

$$B_3 = -aA^2v - 2bAv - 2\lambda\varphi,\tag{23}$$

$$B_2 = A^2 v - 2aAv - bv - 2\theta\lambda - \varphi^2, \tag{24}$$

$$B_1 = 2Av - av - 2\theta\varphi, \tag{25}$$

$$B_0 = \nu - \theta^2, \tag{26}$$

$$\nu = 4(1 + N_i)^2 (N_i^2 + cN_i), \tag{27}$$

$$\lambda = (1 + N_i)^2 (|C_1|^2 + b) + A^2 \xi - 2A\eta, \tag{28}$$

$$\varphi = a(1 + N_i)^2 + 2A\xi - 2\eta, \tag{29}$$

$$\theta = \xi - (1 + N_i)^2, \tag{30}$$

$$\xi = \left| \Gamma_{opt} \right|^2 - N_i^2 - cN_i, \tag{31}$$

$$\eta = (1 + N_i)\Re(C_1\Gamma_{opt}). \tag{32}$$

In both Case 1 and Case 2, a fourth-order polynomial can be solved for N_i (Case 1) or g_a (Case 2) by using typical fourth-order polynomial solution techniques. Following the solution to the polynomial, both N_i and g_a will be known. This means that the center and radius of both the available gain circle corresponding to the value of g_a and the noise figure circle corresponding to the value of N_i can be calculated using equations (1), (2), (4), and (5). A complex number of magnitude 1 and phase equal to the angle in the complex plane with the $Re(\Gamma_s)$ axis formed by a straight line from C_F to C_a is given by μ :

$$\mu = \frac{C_a - C_F}{|C_a - C_F|},\tag{33}$$

where C_a and C_F are defined by (4) and (5). μ (in the complex plane) is analogous to a unit vector from vector theory. μ can be used to find the constrained optimum source reflection coefficient $\tilde{\Gamma}_s$:

$$\tilde{\Gamma}_{\rm s} = C_F + r_F \mu. \tag{34}$$

III. LNA PARETO OPTIMIZATION EXAMPLES

Two design optimization examples are provided where a limitation on the noise figure is placed, and it is desired to find the source reflection coefficient Γ_s providing the maximum value of G_A while meeting given noise figure limitations. For brevity we have chosen to focus on this case (Case 2 from Section II), but Case 1 (finding the minimum value of F_i while meeting G_A limitations) follows dual procedures.

Example 1: unconditionally stable device

Consider a device whose S-parameters and noise parameters are given by the following: $S_{11}=0.642e^{-j110^{\circ}}$, $S_{12}=0.02e^{j45^{\circ}}$, $S_{21}=4.54e^{-j126^{\circ}}$, $S_{22}=0.33e^{-j82.1^{\circ}}$, $F_{min}=1.3$ dB, $\Gamma_{opt}=0.43e^{j175^{\circ}}$, and $R_n=9.3$ Ω . It is desired to design a LNA with the available gain as high as possible while possessing a noise figure no greater than 2 dB.

Stability metrics are calculated as follows: $|\Delta| = 0.259$ and K = 3.006. Because K > 1 and $|\Delta| < 1$, this device is unconditionally stable. It is desired to limit the noise figure to $F_i = 2$ dB, giving $N_i = 0.104$. The quartic polynomial in (21) becomes

$$-0.099g_a^4 - 0.36g_a^3 + 0.321g_a^2 + 1.256g_a - 0.81 = 0.$$

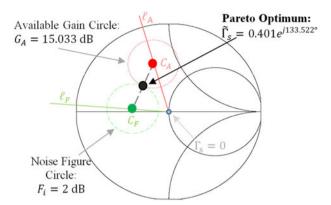


Fig. 2. Pareto optimum source reflection coefficient for Example 1 providing maximum constrained available gain with noise figure less than or equal to 2 dB, plotted with the associated noise figure (2 dB) and available gain circles (15.033 dB).

This polynomial has four roots that can easily be found by a numerical solver: $g_a=-2.898, -2.898, 0.627, 1.546$. Since it is the largest constrained value of g_a that is sought, the root chosen is $g_a=1.546$, resulting in $G_A=15.033\,$ dB. Solving (33) and (34) gives $\mu=1e^{j66.47^\circ}$ and $\tilde{\Gamma}_s=0.401e^{j133.522^\circ}$. At $\tilde{\Gamma}_s$ the maximum constrained value of available gain while maintaining the noise figure less than or equal to 2 dB, $G_A=15.033\,$ dB, is obtained. Figure 2 shows the Pareto optimum value of source reflection coefficient, along with the associated available-gain and noise-figure circles.

The collection of Pareto optimum reflection coefficients for different limiting values of noise figure can be plotted in the Γ_s Smith chart as in Fig. 3. This locus is generated by solving the Pareto optimization problem for multiple limiting values of noise figure spanning from F_{min} until the maximumgain reflection coefficient Γ_{Ms} is reached (in the unconditionally stable case). This collection of points provides optimal trade-off values between gain and noise figure. The same collection of points is obtained by solving the Pareto optimization problem for multiple limiting values of g_a . The Pareto optimum locus extends from Γ_{opt} the reflection coefficient providing optimum noise figure, to Γ_{Ms} , the reflection coefficient providing maximum available gain. While the literature

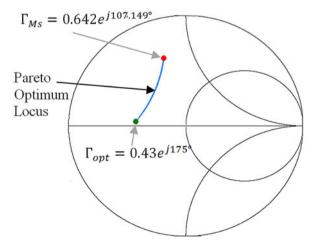


Fig. 3. Pareto optimum locus for Example 1: the collection of Γ_s points providing optimum available gain for different bounded noise figure values. Note that the Pareto locus is *not* a straight line.

often suggests the "rule-of-thumb" method of drawing a straight line between the gain and noise figure optima to perform Pareto designs [14], the Pareto optimum locus is generally *not* a straight line between the points, but rather a curve connecting them.

Figure 4 shows the *Pareto front*. The Pareto front is a plot of the gain-versus-noise tradeoff for the values of Γ_s on the Pareto optimum locus, and it shows the maximum values of G_A that can be obtained under different limiting values of F_i . The plot begins at the minimum noise figure $F_i = F_{min} = 1.3$ dB and ends at the maximum available gain, which occurs at a simultaneous conjugate match for the device [14]:

$$G_{A,max} = \frac{|S_{21}|}{|S_{12}|} \left(K - \sqrt{K^2 - 1} \right) = 15.895 \ dB$$

Example 2: potentially unstable device

Consider a device whose S-parameters and noise parameters are given as follows: $S_{11}=0.6e^{j26^{\circ}}$, $S_{12}=0.07e^{j162^{\circ}}$, $S_{21}=5.14e^{-j34^{\circ}}$, $S_{22}=0.45e^{j72.5^{\circ}}$, $F_{min}=1.8$ dB, $\Gamma_{opt}=0.31e^{j80^{\circ}}$, and $R_n=5.4$ Ω . It is desired to design a LNA with a noise figure less than or equal to 2 dB and the largest available gain under this constraint.

The stability metrics of the device are calculated to be $|\Delta|$ = 0.182 and K = 0.654. Because K < 1, the device is potentially unstable [14]. This does not significantly affect the design procedure, but the stability circle should be drawn and care should be taken that the Pareto optimum is in the stable region on the Smith chart.

For reference in choosing the gain, the maximum stable gain is calculated as in [14]: $G_{MSG} = 18.659$ dB. It is desired to limit the noise figure to 2 dB, resulting in $N_i = 0.199$ from (3). Finding a, b, and c, and then using equations (21) through (32) gives the quartic polynomial of (20) for solution:

$$-0.16g_a^4 - 0.614g_a^3 + 0.617g_a^2 + 2.664g_a - 1.174 = 0$$

Solving this polynomial numerically with a mathematical software package results in four roots: -3.061, -3.06, 0.419, and 1.868. The largest of the roots is the root that will be

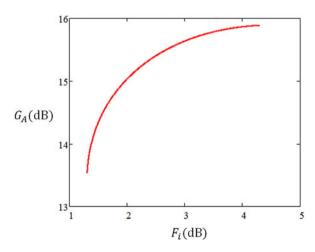


Fig. 4. Pareto front for Example 1: values of available gain G_A for different limiting values of noise figure F_i .

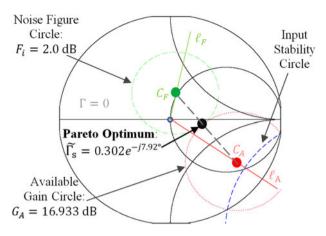


Fig. 5. Pareto optimum source reflection coefficient for Example 2 providing maximum constrained available gain with noise figure less than or equal to 2.0 dB, plotted with the associated noise figure (2.0 dB) and available-gain (16.933 dB) circles. The stability circle is also shown, and it is apparent that the Pareto optimum solution is in the stable region.

used: $g_a = 1.868$, corresponding to $G_A = 16.933$ dB. Using (33) and (34) gives $\mu = 1e^{-j49.4^{\circ}}$ and $\tilde{\Gamma}_s = 0.302e^{-j7.92^{\circ}}$.

This choice of reflection coefficient provides $F_i=2$ dB and $G_A=16.933$ dB. This gain is less than the maximum stable gain, which serves as an informal figure of merit indicating a comfortable level of gain that can be accomplished without too closely approaching instability. Figure 5 shows the Pareto optimum source reflection coefficient along with its associated available gain and noise figure circles. The input stability circle is also shown on this plot. Because $|S_{22}| < 1$, the center of the Smith chart is in the stable region [14], so the identified Pareto optimum source reflection coefficient will provide stable operation. Figure 6 shows the Pareto optimum locus for this device. Because the device is potentially unstable, the Pareto optimum locus approaches the unstable region rather than a stable gain optimum. Figure 7 shows the Pareto front.

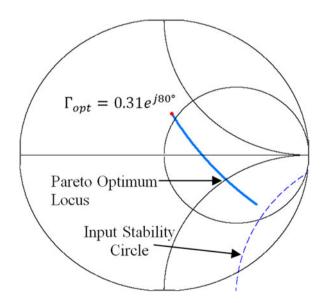


Fig. 6. Pareto optimum locus for Example 2: the Pareto optimum locus goes between the optimum noise figure termination and the stability circle for this potentially unstable device. Note that the Pareto locus is *not* a straight line.

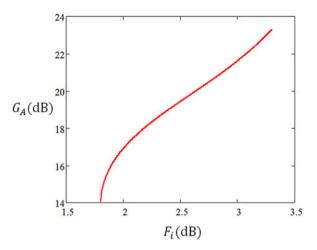


Fig. 7. Pareto front for Example 2: values of available gain G_A for different limiting values of noise figure F_I .

While the constrained optimum $\tilde{\Gamma}_s = 0.302e^{-j7.92^{\circ}}$ is in the stable region of the Γ_s Smith Chart, the design may become unstable if the choice of Γ_L is not made carefully, as in any amplifier design. In some cases, choosing Γ_L to obtain a conjugate match at the output may result in an unstable termination. Thus, care must be taken to accomplish a design where both Γ_s and Γ_L are in the stable regions of their respective Smith Charts.

IV. CONCLUSIONS

An analytical approach has been presented and demonstrated for finding the Pareto optimum source reflection coefficient to dually optimize the available gain and noise figure. This will allow the optimization of radar receivers to meet changing noise and gain requirements based on different scenarios that may be encountered. In most design or optimization situations, a bound is placed on one of the criteria and the other is optimized within this bound. In any case, the exact optimum solution can be found in closed form by solution of a fourth-order polynomial. Examples have been provided in the use of these methods to optimize LNAs for both unconditionally stable and potentially unstable devices. This analytical approach is expected to find application in LNA design and in speeding the real-time optimization of reconfigurable radar receiver amplifiers.

ACKNOWLEDGEMENTS

This work was funded by a Summer Sabbatical from Baylor University and a grant from the National Science Foundation (Grant Number ECCS-1343316).

REFERENCES

 Eye, R.; Allen, D.: 77 GHz low noise amplifier for automotive radar applications, in 25th Gallium Arsenide Integrated Circuit (GaAs IC) Symp. Technical Digest, November 2003, 139–142.

- [2] Hartmann, M.; Wagner, C.; Seemann, K.; Platz, J.; Weigel, R.: A low-power low-noise single-chip receiver front-end for automotive radar at 77 GHz in silicon-germanium bipolar technology, in 2007 Radio Frequency Integrated Circuits Symp., Honolulu, Hawaii, June 2007, 149–152.
- [3] Kuo, W.-M.L.; Liang, Q.; Cressler, J.D.; Mitchell, M.A.: An X-Band SiGe LNA with 1.36 dB mean noise figure for monolithic phased array transmit/receive radar modules, in 2006 Radio Frequency Integrated Circuits Symp., San Francisco, California, June 2006.
- [4] Kuo, W.-M.L. et al.: A low-power, X-Band SiGe HBT low-noise amplifier for near-space applications. IEEE Microw. Wireless Compon. Lett., 16 (9) (2006), 520–522.
- [5] Dawood, M.; Narayanan, R.M.: Receiver operating characteristics for the coherent UWB random noise radar. IEEE Trans. Aerosp. Electron. Syst., 37 (2) (2001), 586–594.
- [6] Fukui, H.: Available power gain, noise figure, and noise measure of two-ports and their graphical representation. IEEE Trans. Circuit Theory, CT-13 (2) (1966), 137-142.
- [7] Nieuwoldt, A.; Ragheb, T.; Massoud, Y.: SOC-LNA: synthesis and optimization for fully integrated narrow-band CMOS low-noise amplifiers, in Proc. of the 43rd Annual Design Automation Conf., 2006, 879–874.
- [8] Nieuwoudt, A.; Ragheb, T.; Massoud, Y.: Hierarchical optimization methodology for wideband low noise amplifiers, in Proc. of the 2007 Asia and South Pacific Design Automation Conf., 2007, 68-73.
- [9] Nguyen, T.-K.; Kim, C.-H.; Ihm, G.-J.; Yang, M.-S.; Lee, S.-G.: CMOS low-noise amplifier design optimization techniques. IEEE Trans. Microw. Theory Tech., 52 (5) (2004), 1433–1441.
- [10] Fukui, H.: The noise performance of microwave transistors. IEEE Trans. Electron Devices, 13 (3) (1966), 329–341.
- [11] Fukui, H. et al.: Optimization of low-noise GaAs MESFETs. IEEE Trans. Electron Devices, **27** (6) (1980), 1034–1037.
- [12] Hashemi, H.; Hajimiri, A.: Concurrent multiband low-noise amplifiers – theory, design, and applications. IEEE Trans. Microw. Theory Tech., 50 (1) (2002), 288–301.
- [13] Niu, G.; Cressler, J.D.; Zhang, S.; Joseph, A.; Harame, D.: Noise-gain tradeoff in RF SiGe HBTs. Solid State Electron., Elsevier Press, 49 (9) (2002), 1445–1451.
- [14] Gonzalez, G.: Microwave Transistor Amplifiers: Analysis and Design, 2nd ed., Prentice-Hall, 1997.
- [15] Miettinen, K.: Nonlinear Multiobjective Optimization, Kluwer Academic Publishers, 1998.
- [16] Baylis, C. et al.: Designing transmitters for spectral conformity: power amplifier design issues and strategies. IET Radar Sonar Nav., 5 (6) (2011), 681–685.
- [17] Ubostad, M.; Olavsbraten, M.: Linearity performance of an RF power amplifier under different bias and load conditions with and without DPD, in 2010 IEEE Radio and Wireless Symp. Digest, 232–235.
- [18] Martin, J.; Baylis, C.; Cohen, L.; de Graaf, J.; Marks, R.J. II: A peak-search algorithm for load-pull optimization of power-added efficiency and adjacent-channel power ratio. IEEE Trans. Microw. Theory Tech., 62 (8) (2014), 1772–1783.
- [19] Fellows, M.; Baylis, C.; Martin, J.; Cohen, L.; Marks, R.J. II: Direct algorithm for the Pareto load-pull optimization of power-added efficiency and adjacent-channel power ratio. IET Radar Sonar Nav., 8 (9) (2014), 1280-1287.



Charles Baylis is an associate professor of electrical and computer engineering at Baylor University, where he directs the Wireless and Microwave Circuits and Systems Program. Dr. Baylis received B.S., M.S., and Ph.D. degrees in electrical engineering from the University of South Florida in 2002, 2004, and 2007, respectively. His research focuses

on spectrum issues in radar and communication systems and has been sponsored by the National Science Foundation and the Naval Research Laboratory. He has focused his work on the application of microwave circuit technology and measurements, combined with intelligent optimization algorithms, to creating reconfigurable transmitters. He serves as the general chair of the annual Texas Symposium on Wireless and Microwave Circuits and Systems, technically cosponsored by the IEEE Microwave Theory and Techniques Society (MTT-S). He also serves as student activities chair of the IEEE MTT-S Dallas chapter.



Robert J. Marks, II is a distinguished professor of electrical and computer engineering at Baylor University, Waco, Texas. When at the University of Washington, he served for 17 years as the faculty advisor to the student chapter of Campus Crusade for Christ. He is a Life Fellow of the IEEE and a Fellow of the Optical Society of America.

His consulting activities include DARPA, Microsoft Corporation, Pacific Gas and Electric, and Boeing Computer Services. His research has been funded by organizations such as the National Science Foundation, General Electric, Southern California Edison, EPRI, the Air Force Office of Scientific Research, the Office of Naval Research, the Whitaker Foundation, Boeing Defense, the National Institutes of Health, The Jet Propulsion Lab, Army Research Office, and NASA. His most recent books are Handbook of Fourier Analysis and Its Applications (Oxford University Press, 2009) and Biological Information—New Perspectives (Singapore: World Scientific, 2013), coedited by M. J. Behe, W. A. Dembski, B. L. Gordon, and J. C. Sanford. He has an Erdos—Bacon number of 5.

Lawrence Cohen received the Bachelor's of Science degree in electrical engineering from The GeorgeWashington University, Washington, DC, USA, in 1975 and the Master's of Science degree in electrical engineering from Virginia Tech, Blacksburg, VA, USA, in 1994. He has been involved in electromagnetic compatibility (EMC) engineering and management, shipboard antenna integration, and radar system design for 32 years. In this capacity, he has worked in the areas of shipboard electromagnetic interference (EMI) problem identification, quantification and resolution, mode-stirred chamber research and radar absorption material (RAM) design, test, and integration. In March 2007, he acted as the Navy's Principal Investigator in the assessment of radar emissions on aWiMAX network.

Additionally, he has acted as the Principal Investigator for various radar programs, including the radar transmitter upgrades. Currently, he is involved with identifying and solving spectrum conflicts between radar and wireless systems as well as research into spectrally cleaner power amplifier designs, tube, and solid state. Mr. Cohen is certified as an EMC Engineer by the National Association of Radio and Telecommunications Engineers

(NARTE). He served as the Technical Program Chairman for the IEEE 2000 International Symposium on EMC and was elected for a 3 year term to the IEEE EMC Society Board of Directors in 1999 and 2009. He is also a member of the IEEE EMC Society Technical Committee 6 (TC-6) for Spectrum Management. For the past 26 years, he has been employed by the Naval Research Laboratory in Washington, DC, USA.