RESEARCH PAPER

Factors reducing the cut-off frequency of resonant tunneling diodes

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The impedance dependence of resonant tunneling diodes (RTDs) based on the GaAs/AlAs heterosystem is investigated in the range of 0.1–40 GHz. The analysis shows that the impedance of about 90% of unbiased RTDs is well described by an equivalent circuit (EC) consisting of parallel-connected resistance and capacitance and an additional resistance connected in series with this parallel combination. When a bias voltage is applied to these RTDs, one needs a "quantum" inductance L_Q to describe the impedance behavior. We find the value of L_Q and calculate the delay time of electrons in the quantum well (QW) of an RTD. The impedance of the rest 10% of the RTDs is well described by an EC that takes into account the recharge of localized electron states at the heterointerfaces of the active layers. Expressions for the cut-off frequencies that take into account the delay of electrons in the QW and the localized electron states at the heterointerfaces are derived. It is shown that the delay of electrons in the QW and localized electron states at the heterointerfaces may significantly reduce the cut-off frequency of RTDs.

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I. INTRODUCTION

At present, a resonant tunneling diode (RTD) is one of the most high-speed semiconductor devices. The detecting properties of RTDs have been demonstrated up to a frequency of 3.9 THz [1]. An RTD-based microwave oscillator described in [2] operates at 1.04 THz and has an output power of 8μ W. In [3], the authors proposed using an RTD as a nonlinear element in subharmonic mixers. These mixers have been widely used in the intensively developing terahertz frequency range as electromagnetic radiation receivers (detectors) [4]. As shown in [5], RTD-based mixers have some advantages over the known mixers of this frequency range. The operation principle of RTDs is described in detail in [6]. A distinctive feature of an RTD is a delay of electrons in its quantum well (QW) when the current flows through the RTD; therefore, the delay time of electrons in the QW is an important parameter determining the high-frequency properties of RTDs.

The high-frequency properties of semiconductor devices are usually described by small-signal equivalent circuit (EC) models. Figure 1(a) shows an EC of a Schottky diode. This circuit consists of parallel-connected barrier resistance R_D , barrier capacitance C_D , and a parasitic resistance r_S connected in series with this parallel combination. Using this circuit, one

¹FIRE RAS, Fryazino, Russian Federation. Phone: +7 903 208 04 63 ²FSUE "S&PE" Pulsar, Moscow, Russian Federation **Corresponding author**: Nikolay Alkeev Email: nikolayalkeev@mail.ru can find the cut-off frequency of a Schottky diode in the detector or frequency converter circuits [8]:

$$f_0 = 1/2\pi\tau_0, \tag{1}$$

where $\tau_0 = r_S C_D$.

Sometimes, the EC shown in Fig. 1(a) is used to describe the high-frequency properties of RTDs; nevertheless, a large number of special ECs have been proposed for RTDs (see, for example, [7, 9-12]). In [7], it was assumed that, under the application of a bias voltage to an RTD, a change in the current flowing through it occurs with some delay; this is equivalent to connecting a so-called "quantum" inductance L_Q in series with R_D (Fig. 1b). According to [7], this inductance is given by

$$L_Q = \tau_{DL}/G_D,\tag{2}$$

where τ_{DL} is the delay time and G_D is the static differential conductance of the RTD. In [9], another EC of an RTD with a "quantum" inductance was obtained directly from the equations describing an RTD in the sequential tunneling approximation [13]. In [10], an EC consisting of two parallel ZC circuits connected in series was suggested in which the impedance Z might depend on frequency. Each ZC circuit simulated one of the barriers of the RTD. This EC was further developed in [11, 12].

Note that it is very convenient to analyze the high-frequency properties of an RTD by the Smith chart. For instance, the impedance curve of the EC shown in Fig. 1(a) is represented by a circular arc on the Smith chart. The impedance curves of the ECs containing a "quantum" inductance are lower than the impedance of the EC shown in Fig. 1(a),



Fig. 1. (a) EC of a Schottky diode and (b) EC of an RTD with "quantum" inductance [7].

while the impedance curves of the EC from [12] and other ECs containing additional capacitances are flatter than the arc of a circle on the Smith chart.

The delay time of electrons in the QWs of RTDs was determined by several methods [1, 14-17]. In [14], the lifetime of electrons in the RTD QW was determined from the decay time of the photoluminescence intensity. The measured decay time ranged from 6×10^{-11} to 8×10^{-10} s for the RTD barrier thickness varying from 2.8 to 6.2 nm (from 10 to 22 monolayers), which is well described by the formula $\tau = \hbar/\Gamma$. Here, Γ is the width of the resonance levels in the QW and \hbar is the Planck's constant. Thus, the electron lifetime in the QW is close to the lifetime τ_D on the resonance level due to the electron tunneling through the emitter and collector barriers. The authors of [14] also pointed out that the mechanism of electron transport (coherent or sequential) through the RTD cannot be determined from the lifetime measurements. In [15], the authors determined the electron lifetime in the OW of RTDs from photoluminescence intensity measurements, but with the use of a correlation technique, which enabled them to measure lifetimes as small as 1.2×10^{-11} s.

In [16], the authors used the following formula to determine the delay time of electrons: $J = en_W/\tau_{DC}$ where *J* is the current density through the RTD, τ_{DC} is the lifetime of electrons in the QW due to tunneling through the collector barrier, and n_W is the electron concentration in the QW, which was determined at liquid helium temperature by two methods. In the first method, n_W was determined from the period of low-frequency oscillations of the RTD capacitance in a quantizing magnetic field. The second method was based on the determination of the linewidth of the recombination radiation from the RTD QW. Both methods yielded approximately the same results. The delay time of electrons in the QW turned out to be rather large (~6 10⁻⁷·s) due to the large thickness of barriers in the RTDs.

In [1], the authors studied the response of an RTD to the radiation of a free electron laser in the frequency range from 120 GHz to 3.9 THz. The delay time of electrons in the QW was estimated from the frequency dependence of the rectified signal and was found to be 0.24×10^{12} s.

In [17], the authors studied an RTD based on the InGaAs/ AlAs heterosystem with a barrier thickness of 3.5 nm and a QW width of 5.8 nm. They analyzed the frequency dependence of the real and imaginary parts of the RTD impedance in a frequency range of up to 10 GHz and found that the charge relaxation time in the QW varied from 80 to 180 ps depending on the bias voltage, whereas the calculated electron lifetime τ_D remained approximately constant (120 ps). The authors explained this behavior by the Coulomb interaction between electrons in the RTD.

Note that such a large scatter in the electron lifetime in the QW of various RTDs is natural because the lifetime depends exponentially on the thickness of the RTD barriers [18].

In the present work, we determine the value of the "quantum" inductance by analyzing the shape of the RTD impedance curve on the Smith chart and calculate the delay time of electrons in the QW by formula (2). We derive an expression for the cut-off frequency of an RTD that takes into account both the capacitance of the RTD and the electron delay in the QW. We show that the cut-off frequency essentially depends on electron delay. The analysis of the frequency dependence of RTDs shows that about 10% of the RTDs studied have localized electron states at the heterointerfaces of the active layers. We suggest that these states are due to defects in heterojunctions and arise during the growth of the active layers of RTDs. The recharge of localized electron states, as well as the delay of electrons in the QW, reduces the high-frequency performance of RTDs.

II. EXPERIMENTAL TECHNIQUE AND DATA PROCESSING

We investigated RTDs based on a GaAs/AlAs heterosystem with 1.7-nm-thick AlAs barriers and a 4-nm-wide GaAs QW. The RTDs were designed to be used in millimeter-wave subharmonic mixers. The current–voltage characteristics and other parameters of RTDs are presented in [5]. A method for measuring the RTD impedance is described in [19]. This method allows one to take into account the impedances of the contact pads and the connecting wires on the RTD chip. We carried out the impedance measurements only in the regions with positive differential conductivity on the I-Vcurve; in the regions with negative differential conductivity, we could not carry out such measurements because of spurious oscillations.

To choose an EC that best describes the experimental curve of an individual RTD, we used the nonlinear regression method, which allowed us not only to determine the most appropriate EC, but also to find the parameters of this EC. The essence of this method is as follows. At all points f_i of the frequency range at which the RTD impedance $Z_m(f_i)$ is measured, we calculate the impedance $Z_c(f_i)$ of the assumed EC. Then, we construct a goal function

$$G = \sum_{i=1}^{n} |Z_m(f_i) - Z_c(f_i)|^2.$$

By varying the parameters of the EC, we find a minimum G_{\min} of the function *G* and the values of the parameters of the EC that minimize the goal function. The EC with the minimum value of *G* among other ECs is taken as the most appropriate EC for a given RTD. This method also allows us to find the variance (or the RMS error) of the EC parameters [19].

III. EXPERIMENTAL RESULTS AND DISCUSSION

The figures below represent the impedance of the active region of an RTD versus frequency on the Smith chart; the impedances of the contact pads and the connecting wires are subtracted from the measured impedance.

Figure 2 shows the experimental impedance of an RTD and the impedance of the EC shown in Fig. 1(a) with optimal values of its parameters. The area of the RTD active region is $10 \times 10 \ \mu\text{m}^2$, and no bias voltage is applied across the diode. One can see that the calculated curve matches well with the experimental one. The parameters of the optimized EC are as follows: $R_D = 2.57 \ \text{k}\Omega$, $C_D = 0.26 \ \text{pF}$, and $r_S =$ $0.756 \ \Omega$. The cut-off frequency of the RTD, calculated by (1), is about 810 GHz.

A) Delay of electrons in the QW

We investigated the frequency dependence of the impedance of about 30 RTD chips. When no bias is applied, the impedance curves of the majority (\sim 90%) of RTDs well correlated with each other and looked like curve 1 in Fig. 2. We found that these impedance curves are slightly lower than circular arcs on the Smith chart when a bias voltage is applied. Moreover, when the voltage across the RTDs is close to zero or corresponds to the minimum of the valley current on the I-V curve, the impedance curves practically coincide with the arc of a circle. Such an impedance behavior indicates that the effect observed is not related to inaccurate calibration of the measurement equipment. Since such a "sagging" of the impedance curve is typical of the ECs that contain a "quantum" inductance, we suggest that this effect is associated with a delay of electrons in the QW as they pass from the emitter to the collector. To explain the absence of sagging when the voltage across the RTD is close to zero or corresponds to the minimum of the valley current on the I-V curve, we have to assume that the main contribution to electron transfer in these cases is made by non-resonant mechanisms rather than by resonant tunneling.

As an example, Fig. 3 shows the experimental frequency dependence of the RTD impedance (curve 1) at a bias of 0.8 V, which corresponds to the center of the resonant tunneling region on the I-V curve. The area of the active region is $10 \times 20 \ \mu\text{m}^2$. Curve 2 in Fig. 3 represents the frequency dependence of the impedance of the EC shown in Fig. 1(a) with optimal parameters. One can see that curve 1 is below curve 2. To evaluate the "quantum" inductance, we used the EC shown in Fig. 1(b) and applied the method of nonlinear regression. We found that the "quantum" inductance is 0.09 nH and the delay time of electrons in the QW, calculated by formula (2), is 1.3 ps. We do not show the curve for the EC of Fig. 1(b) with optimal parameters because it nearly coincides with curve 1 in this figure.

Based on the EC shown in Fig. 1(b), we derive an expression for the cut-off frequency f_{C1} of an RTD that takes into account the delay of electrons in the QW. Suppose that an ac voltage U of frequency f is applied to the leads of the EC shown in Fig. 1(b). It can be shown that the voltage U_1 across the resistor R_D is

$$\frac{U}{1+(r_S/R_D)-(2\pi f)^2\tau_0\tau_D+j2\pi f\tau_0+j2\pi f\tau_0+j2\pi f\tau_D},$$

where τ_{DL} is defined by (2). Note that $R_D = 1/G_R$. When the condition $R_D \gg r_S$ is satisfied, we have

$$\frac{|U_1|}{|U|} = \frac{1}{\sqrt{1 + (2\pi f)^4 (\tau_0 \tau_D)^2 + (2\pi f)^2 (\tau_0^2 + \tau_D^2)}}.$$
 (3)

Curve 1 in Fig. 4 shows the function $|U_1(f)|/|U(f)|$ calculated by formula (3) with $\tau_{DL} = 0$, i.e., for the EC shown in



Fig. 2. RTD impedance versus frequency in the range from 0.1 to 40 GHz: (1) the experimental RTD impedance when no bias is applied; (2) the impedance of the optimized EC shown in Fig. 1 (a).



Fig. 3. RTD impedance versus frequency in the range 0.1-40 GHz: (1) experimental impedance at a bias of 0.8 V; (2) the impedance of the optimized EC shown in Fig. 1(a).

Fig. 1(a). Curve 2 represents the function $|U_1(f)|/|U(f)|$ of the EC with "quantum" inductance shown in Fig. 1(b). The parameters of this EC are the same as those of the EC whose frequency dependence is shown by curve 1 in Fig. 3. One can see that, for the EC with "quantum" inductance, the function $|U_1|/|U|$ decreases with increasing frequency much faster than for the EC in Fig. 1(a). For curve 1, the cut-off frequency (determined from the factor of $\sqrt{2}$ decrease in $|U_1|/|U|$) is equal to 810 GHz, while the cut-off frequency f_{C1} for the EC with an inductance is \sim 125 GHz, which is six and a half times less than the previous one. Thus, taking into account the "quantum" inductance significantly reduces the cut-off frequency of RTDs. Note that f_{C_1} can be found directly from (3). For this purpose, one should equate the radicand (3) to two and solve the quadratic equation for f^2 . However, the expression obtained for f_{C_1} is cumbersome and difficult to analyze.

To assess how the delay time τ_{DL} of electrons in the QW depends on barrier thickness, we take into account that, just as in [14], τ_{DL} coincides with the lifetime τ_{DC} of electrons in QWs due to tunneling through the collector barrier, and apply the following formula from [18]:

$$\tau_{DL} = \frac{2a}{v_1 |T|^2}.$$
 (4)

Here, *a* is the width of the QW, $v_1 = (2\varepsilon_1/m^*)^{1/2}$ is the speed of transverse motion of electrons on the first resonance level with energy ε_1 , m^* is the electron effective mass, and $|T|^2$ is the transparency of the collector barrier, i.e., the probability of electron tunneling through the barrier. In the case of a rectangular barrier [18], we have

$$|T|^{2} = \left[1 + \frac{1}{4} \frac{\sinh^{2}|\kappa|b}{1 + \varepsilon_{1}/V(1 - \varepsilon_{1}/V)}\right]^{-1},$$

where *b* is the barrier thickness, *V* is the barrier height, and $|\kappa| = \frac{1}{\hbar} \sqrt{2m*(V - \varepsilon_1)}$. Note that formula (4) is valid in the case of $|T|^2 \ll 1$. For our RTDs, $V \approx 1.0$ eV, a = 4 nm, and $\varepsilon_1 \approx 0.19$ eV. Table 1 shows the calculated transparency

 $|T|^2$, delay time τ_{DL} , the RTD cut-off frequency f_{C1} , and the frequency $f_{DL} = 1/2 \pi \tau_{DL}$. This frequency coincides with the frequency f_{C1} when $\tau_0 = 0$; i.e., it shows the contribution of the electron delay to the cut-off frequency of RTDs. Calculations were carried out for the barrier thickness of 4, 6, 8, and 10 monolayers.

Table 1 shows that the cut-off frequency f_{C_1} tends to the value $1/2\pi r_S C_D = 810$ GHz as the barrier thickness decreases. Since $r_{\rm S}C_{\rm D}$ is practically independent of the RTD active area, to increase the cut-off frequency of the RTD, one should reduce the specific contact resistance and the specific capacitance of the RTD. In this relation, RTDs based on the InGaAs/AlAs heterosystem are more advantageous. The specific resistance of non-alloyed ohmic contacts to InGaAs is an order of magnitude lower than the specific ohmic resistance of alloyed ohmic contacts to GaAs [20]. Other advantages of RTDs based on the InGaAs/AlAs heterosystem are the larger barrier height, which increases the temperature stability of these RTDs, and the longer mean-free path of electrons in InGaAs as compared with that in GaAs [21]. Table 1 also shows that f_{DL} rapidly decreases with increasing thickness of the barriers. Moreover, we can argue that, for a reliable operation of RTD-based mixers at frequencies above 1 THz, the thickness of the diode barriers should not exceed six monolayers.

Note that the greater value of τ_{DL} in our RTDs in comparison with the calculated value (0.032 ps) is likely to be attributed to the increased thickness of the RTD barrier layers, which exceeds six monolayers. This follows from the fact that the peak current density in our RTDs is seven times lower than that in the RTDs from [22], which have a barrier thickness of six monolayers.

B) Heterointerface defects

About 10% of RTDs investigated in this study had flatter impedance curves than a circular arc. One of these impedance curves (curve 1) is shown in Fig. 5.

The flatter shape of the curves was also observed in the RTD samples from Duisburg University (Germany) [23]. InAlAs RTD barriers were 5 nm thick, the width of the



Fig. 4. $|U_1|/|U|$ versus frequency: (1) for the optimized EC shown in Fig. 1(a); (2) for the optimized EC shown in Fig. 1(b).

Number of monolayers	4	6	8	10
b, nm	1.1	1.7	2.2	2.8
$ T ^2$	0.609	0.249	0.089	0.023
$ au_{DL}$, s	1.32×10^{-14}	3.22×10^{-14}	8.96×10^{-14}	3.52×10^{-13}
f_{C_1} , GHz	809	804	772	549
f_{DL} , GHz	12 060	4940	1780	903

Table 1. Dependence of high-frequency RTD properties on the barriers thickness.

InGaAs QW was also 5 nm, and the area of the active region was 50 μ m². Figure 6 shows a part of the Smith chart. Curve 1 represents the experimental RTD impedance at a bias of 0.3 V, and curve 2 represents the impedance of the EC of Fig. 1(a) with optimal parameters (i.e., for $G = G_{min}$). We can see that curve 1 is flatter than curve 2.

It turned out that the impedance curve that is flatter than a circular arc is well described by the EC proposed in [24] and shown in Fig. 7(a). Curve 3 in Fig. 6 shows the impedance of the EC shown in Fig. 7(a) for $G = G_{\min}$. We can see that curve 3 fits the experimental curve 1 much better than curve 2. Quantitatively, G_{\min} for the EC shown in Fig. 7(a) is more than an order of magnitude smaller than G_{\min} for the EC shown in Fig. 1(a).

The $C_W R_W$ circuit in Fig. 7(a) is similar to an analogous circuit of a MOS structure shown in Fig. 7(b) [6], which describes the recharge of the surface states of the structure by a high-frequency electric field. Here, C_i and C_D are the capacitances of the insulator and the semiconductor depletion layer, respectively. The value $C_S R_S$ determines the relaxation time of the surface states. By analogy with the circuit shown in Fig. 7(b), we assume that the $C_W R_W$ circuit in Fig. 7(a) describes the recharge of localized electron states at the RTD heterointerfaces. For the RTDs from Duisburg University, the values of C_W and R_W at the minimum of the goal function were equal to 0.042 pF and 350 Ω , respectively, and the recharge time of surface states was found to be 14.7 ps.



Fig. 5. (1) The experimental impedance of an RTD and (2) the impedance of the optimized EC shown in Fig. 7(a), in the frequency range 0.1–40 GHz.

For the RTD whose impedance is shown by curve 1 in Fig. 5, we determined the parameters of the EC shown in Fig. 7(a) by the nonlinear regression method. The impedance of this EC for $G = G_{\min}$ is shown by curve 2 in Fig. 5. The values of C_W , R_W , and r_S were equal to 0.12 pF, 217 Ω , and 10.9 Ω , respectively, and the recharge time of the surface states was found to be 26 ps.

Thus, we can assume that the flatter shape of the impedance curve for 10% of RTDs is associated with damaging the heterointerface structure during the growth of the active layers of RTDs and the appearance of localized electron states at the heterointerfaces. These states, similar to the surface states on the SiO_2-Si interface, are recharged in an ac electric field.

To assess the influence of defects at the heterointerfaces on the high-frequency properties of RTDs, we determine the cut-off frequency f_{C_2} for the circuit shown in Fig. 7(a). We will do this in the same way as the formula for the cut-off frequency f_{C_0} was obtained in [8].

Suppose that a high-frequency voltage U is applied to the leads of the EC in Fig. 7(a). Let us calculate the voltage U_1 across the resistor R_D : $U_1 = UZ_2/(r_S + Z_2)$, where Z_2 is the impedance of the circuit consisting of parallel-connected R_D and C_D and the $R_W C_W$ circuit connected in series with them. The analysis has shown that if R_D , $R_W \gg r_S$, then $|U_1|/|U|$ weakly depends on C_W , and its frequency dependence is the same as that of the EC shown in Fig. 1(a): $|U_1|/|U| = \sqrt{1 + (2\pi)^2 f^2 \tau_0}$; therefore, $f_{C_2} = f_{C_0}$.

The fact that the $R_W C_W$ circuit produces no effect on the f_{C_2} of these RTDs can be explained as follows. The recharge time τ_W of the circuit $R_W C_W$ is rather large due to the large value of R_W ; therefore, the capacitance C_W cannot be recharged in a high-frequency electric field. As τ_W decreases



Fig. 6. Impedance versus frequency in the range 0.1-50 GHz: (1) the experimental RTD impedance at a bias of 0.3 V; (2) the impedance of the optimized EC shown in Fig. 1(a); and (3) the impedance of the optimized EC shown in Fig. 7(a).



Fig. 7. (a) EC of an RTD from [24] and (b) EC of a MOS structure from [6].

when C_W tends to zero, the circuit $R_W C_W$ ceases to influence f_{C_2} due to the smallness of C_W . Thus, the cut-off frequency of the RTDs with flat frequency dependence of the impedance does not depend on the recharge time of localized electron states at the RTD heterointerfaces. However, one should pay attention to the following two features of these RTDs: a large value of R_D (Fig. 5) and an enhanced parasitic series resistance r_S of up to $\sim 10-20 \Omega$, which, according to formula (1), leads to a decrease in the cut-off frequency.

Note that the method based on the analysis of the frequency dependence of the impedance is highly informative. For instance, the shape of the *I*–*V* curve of the RTDs whose impedance versus frequency is close to an arc of a circle on the Smith chart nearly coincides with the shape of the *I*–*V* curve of the RTDs whose impedance exhibits flatter frequency dependence than the arc of a circle; i.e., the shape of the *I* – *V* curves does not allow one to find out if there are localized electron states at the heterointerfaces of RTDs.

IV. CONCLUSIONS

We have investigated the frequency dependence of the impedance of RTDs based on the GaAs/AlAs heterosystem with 1.7 nm barriers and 4 nm QWs that are intended for use in millimeter-wave subharmonic mixers operating in the range from 0.1 to 40 GHz. The measured results have been compared with the frequency dependence of the impedance of various ECs of RTDs on the Smith chart. The analysis has shown that, at zero dc bias, the high-frequency properties of the majority (\sim 90%) of RTDs are well described by an EC consisting of parallel-connected resistance and capacitance and an additional resistance connected in series with this parallel circuit. The frequency dependence of the impedance in this case is represented by an arc of a circle on the Smith chart. When a bias voltage is applied to the diodes, the curves of their impedances lie below the arc of a circle on the Smith chart. Such a "sagging" of curves was observed only in the sections of the I-V curve where the resonant tunneling of electrons occurs. This effect is well described by the EC containing a "quantum" inductance L_Q . The value of L_Q has allowed us to calculate the delay time of electrons in the QWs of RTDs.

We have obtained an expression for estimating the RTD cut-off frequency that takes into account the delay of electrons in the QW. Our evaluation has shown that the cut-off frequency of our RTDs is 125 GHz, which is six and a half times lower than the cut-off frequency of RTDs when the delay of electrons in the QW is neglected.

We have calculated the electron delay time in the QW as a function of the RTD barrier thickness. These calculations

show that the RTD cut-off frequency is greater than 1 THz provided that the barrier thickness of the diodes does not exceed six monolayers.

To describe the high-frequency properties of the remaining \sim 10% of RTDs, it is necessary to use an EC that takes into account the recharge of localized electron states at the RTD heterointerfaces. These states can be attributed to the destruction of the heterojunction structure during the growth of the active layers of RTDs and degrade the performance of RTDs by increasing the parasitic series resistance.

Note that the method based on the analysis of the frequency dependence of RTD impedance is highly informative. For instance, our investigations have shown that the I-Vcurves of RTDs with localized electron states on the heterointerfaces nearly coincide with the I-V curves of ordinary RTDs.

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