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Effective resistivity extraction of low-loss silicon substrates at millimeter-wave frequencies

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Abstract

The effective resistivity (ρ_{eff}) is a figure of merit commonly used to assess the radio-frequency performance of a substrate from the measurements of coplanar waveguide lines. For highly resistive substrates, such as the trap-rich (TR) substrate, the extracted ρ_{eff} decreases by several orders of magnitude at millimeter-wave frequencies. The explanation for this decay is twofold. First, the imaginary part of the characteristic impedance ($\Im(Z_c)$) is not well extracted, which leads to an incorrect separation of the total losses among the metal and substrate losses. Second, the original expression of ρ_{eff} does not include dielectric losses, which might become non-negligible at millimeter-wave frequencies. This paper solves both issues by presenting a new procedure to extract ρ_{eff} and the dielectric losses simultaneously, and by introducing a novel method to correct $\Im(Z_c)$. Furthermore, it is shown that this extraction method enables the correct extraction of substrate parameters up to 220 GHz of TR and high-resistivity silicon substrates. Finally, the origin of the large extracted value of dielectric loss is discussed in the potential presence of surface roughness and surface wave radiation. Both phenomena are discounted thanks to measurements of an additional reflective structure and a standard impedance substrate.

Introduction

The simultaneous downscaling of silicon-based transistors and their integration on high-performance radio-frequency (RF) Silicon-on-Insulator (SOI) substrates have enabled the development of competitive System-on-Chips (SoCs) with integrated front-end modules working at microwave and millimeter-wave (mm-wave) frequencies. Among those SOI substrates, the trap-rich (TR) substrate answers the demands in terms of very low propagation loss and harmonic distortion at microwave frequencies, thanks to its high effective resistivity.

The effective resistivity was first introduced as a figure of merit (FoM) to characterize the RF losses of a substrate, based on a coplanar waveguide (CPW) line analysis, independently from its conductor losses [1]. Then, it has been used to characterize the impact of the substrate on the crosstalk between two structures [2], the quality factor of inductors [3], the harmonic distortion in passive structures [4], and in RF switch modules [5].

In general, a transmission line is represented as a two-wire line (signal and ground, cf. Fig. 1 (a)), described by two complex parameters, i.e. its characteristic impedance (Z_c) and its propagation constant (γ). A piece of transmission line of infinitesimal length (dz) can be modeled as a lumped-element circuit, as shown in Fig. 1(b), described by R, L, G, and C. They are defined as

R is the series resistance per unit length, for the signal and ground conductors, in Ω/m .

L is the series inductance per unit length, for the signal and ground conductors, in H/m

G is the shunt conductance per unit length, in S/m.

C is the shunt capacitance per unit length, in F/m.

The well-known expressions to move from one representation (γ, Z_c) to the other (RLGC) are [6, 7]

$$Z_c = \sqrt{(j\omega L + R)/(j\omega C + G)}, \qquad (1)$$

$$\gamma = \sqrt{(j\omega L + R)(j\omega C + G)},\tag{2}$$

$$R = \Re(\gamma Z_c),\tag{3}$$

$$L = \Im(\gamma Z_c)/\omega,\tag{4}$$

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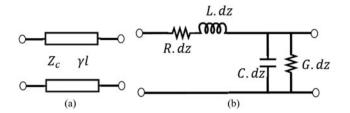


Fig. 1. Two-wire (signal and ground) transmission line model (a). Lumped-element equivalent circuit (b).

$$G = \Re(\gamma/Z_c),\tag{5}$$

$$C = \Im(\gamma/Z_c)/\omega. \tag{6}$$

The *RLGC* line elements representation is more convenient to our case as it gives more physical insight. Indeed, the *G* and *C* terms describe the admittance of the transmission line cross-section, which is of particular interest for substrate characterization.

Figure 2(a) represents a general non-homogeneous structure located below the CPW metal line. In integrated circuits, the substrate is usually made up of a dielectric layer (oftentimes silicon dioxide) below which the semiconductor bulk material (usually silicon) is located. Furthermore, the semiconductor region is in general characterized by a space-dependent resistivity ($\rho(x, y)$). To characterize the overall performance of this material stack with regards to the performance of overlying RF and mm-wave passives, it is convenient to equate this material stack to an equivalent effective substrate (Fig. 2(b)) of uniform effective relative permittivity ($\epsilon_{r,eff}$) and uniform effective resistivity (ρ_{eff}).

In this equivalent effective structure, the line shunt capacitance C is the sum of a top contribution (C_{top}), including the metallization thickness, and a bottom contribution (C_{bot}) from the effective substrate. They are expressed as the product of the medium permittivity and a geometry factor (F_{top} , F_{bot}) that depends on the CPW cross-section dimensions, such that

$$C_{top} = \epsilon_0 \epsilon_{r,top} F_{top}, \tag{7}$$

$$C_{bot} = \Im(Y_{bot})/\omega = \epsilon_0 \epsilon_{r,eff} F_{bot}, \tag{8}$$

where $\epsilon_{r,top}$ is the relative permittivity of the top medium and Y_{bot} is the equivalent admittance of the bottom structure. The geometry factor accounts for the field distribution among the top and bottom parts and is computed by accurate closed-forms analytical expressions formulated in [8]. Assuming no losses in the top medium (air in this case), the shunt conductance is given by

$$G = \Re(Y_{bot}) = \frac{F_{bot}}{\rho_{eff}},\tag{9a}$$

$$\rho_{eff} \equiv \frac{F_{bot}}{G}.$$
 (9b)

This expression for ρ_{eff} is equivalent to the original definition of the effective resistivity given in [1], which is

$$\rho_{eff} \equiv \frac{qC_0}{\epsilon_0 G},\tag{10}$$

and can be derived from (9b) by defining q and C_0 , as the filling factor and the air-filled capacitance, respectively. They are related to the top and bottom geometry factor by

$$C_0 \equiv C|_{\epsilon_{r,top} = \epsilon_{r,eff} = 1} = \epsilon_0 (F_{top} + F_{bot}), \tag{11}$$

$$q \equiv \frac{F_{bot}}{F_{bot} + F_{top}}. (12)$$

The effective resistivity computed from (10) accounts for all shunt RF losses (not series metallic losses) present in the non-uniform structure, originating from some real component of the shunt admittance $\Re(Y_{bot})$. The term resistivity is used, because these RF losses are interpreted as conductive losses in the substrate. Keeping in mind that interpretation, the effective uniform resistivity and permittivity strongly decay with increasing frequency in the low frequency range, because the slow-wave mode is still dominant. Then, they are expected to reach a plateau and remain constant at higher frequencies. However, this is not necessarily observed in practice, mainly for substrates having a high effective resistivity. Indeed, in the substrate samples presented in this paper, a significant decrease of ρ_{eff} with frequency is observed above 5 to 10 GHz.

As it will be explained in the following sections, two reasons for this unexpected decay of effective resistivity are identified: (i) a misestimation of the characteristic impedance (Z_c) of the transmission lines and (ii) the presence of additional nonconductive losses in our samples. Section "Description of the new extraction procedure" describes a correction method for the correct evaluation of Z_c as well as a new extraction procedure of the effective substrate parameters, including dielectric losses. Section "Detailed study of effective resistivity extraction on a trap-rich substrate" shows the characterization of a TR substrate applying this new methodology. Section "Sensitivity of the R-based method to input parameters" discusses the limitations of this characterization procedure. Section "Cutoff frequency" presents the cutoff frequency above which the effective dielectric losses become the dominant loss mechanism. Then, section "Validation up to 220 GHz" confirms the correctness of the new extraction procedure up to 220 GHz with CPW lines measurements on three different substrates. Finally, the origin of the large value of dielectric loss that is extracted is discussed in section "Discussion about dielectric loss". An earlier version of this paper was presented at the 2019 14th European Microwave Integrated Circuits Conference and was published in its Proceedings [9].

Description of the new extraction procedure

This section describes a new extraction procedure of effective substrate parameters, yielding physically meaningful substrate loss parameters at mm-wave frequencies. In section "Sensitivity analysis of the extracted characteristic impedance", it is shown how a misestimation of the imaginary part of $Z_c(\Im(Z_c))$ leads to a wrong evaluation of metallic and substrate losses. A method is

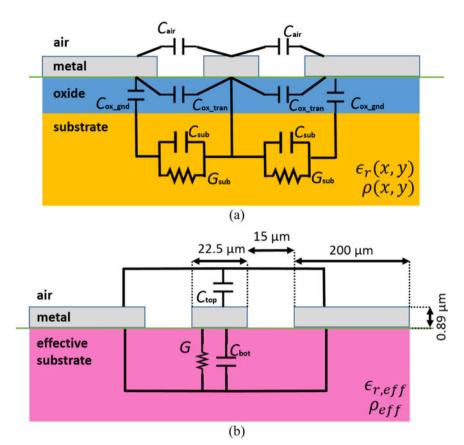


Fig. 2. Cross-section of a CPW line on top of a general multilayer structure (a) and of an effective uniform substrate (b), along with the dimensions of the measured CPW lines.

proposed to correct it in section "R-based method". In section "Inclusion of dielectric losses", the uniform effective substrate model is extended to include dielectric losses. A new procedure to extract simultaneously conductive and dielectric losses is also proposed.

Sensitivity analysis of the extracted characteristic impedance

As stated in section "Introduction", a transmission line is fully described by two complex parameters, i.e. its characteristic impedance and its propagation constant. While the multiline Thru-Reflect-Line (mTRL) algorithm [10] gives the value of γ in a straightforward and accurate manner, the estimation of Z_c is more complex and several methods exist to evaluate it. Aiming at extracting the substrate properties, it is necessary to express γ and Z_c in terms of the *RLGC* line elements (cf. Fig. 1(b)).

For low-loss transmission lines $(R \ll j\omega L \text{ and } G \ll j\omega C)$, (1) and (2) become

$$Z_c \approx \sqrt{\frac{L}{C}} + j \frac{1}{\omega C} \left(\sqrt{\frac{L}{C}} \cdot \frac{G}{2} - \sqrt{\frac{C}{L}} \cdot \frac{R}{2} \right),$$
 (13)

and $(\gamma = \alpha + i\beta)$

$$\alpha \approx \frac{\Re(Z_c)G}{2} + \frac{R}{2\Re(Z_c)},$$
 (14)

$$\beta \approx \omega \sqrt{LC}$$
. (15)

These equations can be turned around in order to evaluate G and R:

$$G \approx \frac{1}{\Re(Z_c)} \cdot (\alpha + \omega C \cdot \Im(Z_c)),$$
 (16)

$$R \approx \Re(Z_c) \cdot (\alpha - \omega C \cdot \Im(Z_c)).$$
 (17)

Here, the second term of the products shows plainly that an (under-) over-estimation of $\Im(Z_c)$ will have the effect of (over-) under-estimating R and (under-) over-estimating G. Indeed, as stated above, the total loss α is accurately evaluated by the mTRL approach. Hence, an error in the extracted $\Im(Z_c)$ induces an error in the repartition of this total loss to the series metallic R term and to the shunt substrate G term.

The extraction of Z_c relies on multiple assumptions and, therefore, there exist multiple methods for its extraction. The methods suited to silicon substrates are based on a two-tier calibration. First, a calibration is done on an impedance standard substrate (ISS) calkit in order to move the reference plane to the probe tips, defined with respect to a 50 Ω reference impedance. Then, lines of different lengths and a reflect structure are measured. Each method relies on a particular modeling of the transition between the probe tips and the middle of the shortest line (thru).

CPW lines fabricated on top of a TR SOI wafer are measured from 10 MHz to 67 GHz using an Agilent 2-port PNA-X vector network analyzer and a pair of GSG Infinity probes from Cascade Microtech. The CPWs are fabricated using aluminum, and their dimensions, which were measured using a precision profiler, are described in Fig. 2(b). An open structure, a thru

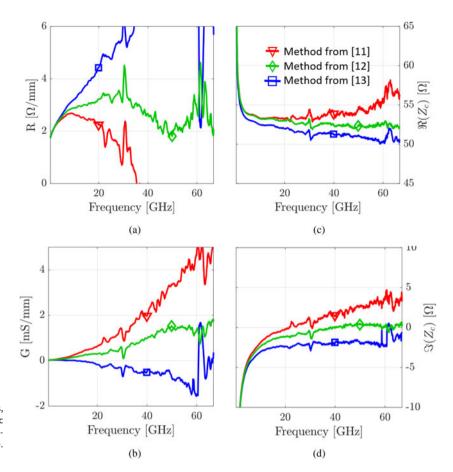


Fig. 3. Distributed R (a) and G (b) line elements of the CPW lines, as well as the real (c) and imaginary (d) parts of its characteristic impedance versus frequency using several Z_c extraction techniques: [11] (red, "v"), [12] (green, "d"), and [13] (blue, "s"). R and G are computed from (3) and (5).

(102.8 µm-long) and two lines of different lengths (2102.8 and 8102.8 µm) are measured. A Short-Open-Load-Thru (SOLT) calibration is performed on an ISS calkit. Then, CPW lines of different lengths are measured and the mTRL algorithm is applied to extract the propagation constant. Figure 3 shows the characteristic impedance as well as the series metallic loss (R) and shunt substrate loss (G), using several methods of Z_c extraction [11–13]. For each method, the RLGC elements are obtained from (3)–(6). The glitches at 30 and 60 GHz are a consequence of the fact that the mTRL algorithm is ill-conditioned when the line length differences are a multiple of a half wavelength.

At low frequencies (below 5 GHz), all the different Z_c extraction techniques agree well, giving confidence in the accuracy of the extracted parameters. However, as the frequency increases, an increasingly stronger discrepancy is observed in each parameter.

Figure 3(c) shows that $\Re(Z_c)$ is extracted with a small relative variation among the different extraction methods. In contrast, from one Z_c extraction technique to another, there is a very large relative variation in the estimated value of $\Im(Z_c)$ (cf. Fig. 3 (d)), which leads to unphysical R and G at high frequencies. An accurate evaluation of $\Im(Z_c)$ is crucial to get realistic values of R and G. This statement is further confirmed with a sensitivity analysis.

Using the derivation provided in Appendix A, the sensitivity of R and G is developed from (16) and (17) as

$$\left|\frac{\delta R}{R}\right| \le B_R = T_R \cdot \left[\left|\frac{\delta C}{C}\right| + \left|\frac{\delta \Im(Z_c)}{\Im(Z_c)}\right|\right] + \left|\frac{\delta \Re(Z_c)}{\Re(Z_c)}\right|,\tag{18}$$

$$\left|\frac{\delta G}{G}\right| \le B_G = T_G \cdot \left[\left|\frac{\delta C}{C}\right| + \left|\frac{\delta \Im(Z_c)}{\Im(Z_c)}\right|\right] + \left|\frac{\delta \Re(Z_c)}{\Re(Z_c)}\right|,\tag{19}$$

where B_R and B_G define the maximum boundary values of relative variations, and T_G and T_R are detailed in Appendix A.

Each term $|\delta x/x|$, where x is G, R, C, $\Im(Z_c)$, or $\Re(Z_c)$, is observed from measurements based on different extraction techniques. Three extraction methods are shown in Fig. 3 that give different values of the parameters upon which the sensitivity analysis is carried out $(C, \Im(Z_c))$, or $\Re(Z_c)$). The denominator, x, uses the value of the parameter extracted using [12], while the numerator, δx , is computed as the difference between the value of the parameter extracted using [12] and of that using [11]. The term $|\delta x/x|$ therefore represents the relative uncertainty of variable x, and this uncertainty is given for each term in Table 1, along with the evaluations of the boundaries B_R and B_G at 10, 20, and 50 GHz.

First, it is clear that the observed variations in $\delta G/G$ and in $\delta R/R$ are well bounded by the theoretical boundaries computed by B_G and B_R . Secondly, it is shown that the observed variations in C and $\Re(Z_c)$ account for <1% variation each in the extracted R and G up to 20 GHz and at most 8% at 50 GHz. We therefore confirm as expected, that the relative uncertainty on the imaginary part of the characteristic impedance is the main contribution to the misestimation of both R and G. An accurate evaluation of $\Re(Z_c)$ is therefore crucial to be able to extract the substrate losses. Furthermore, the fact that $\delta G/G$ (31) and $\delta R/R$ (33) are close to

Table 1. Sensitivity analysis of R and G to an uncertainty in Z_c and C, based on (18) and (19)

() ()			
f (GHz)	10 (%)	20 (%)	50 (%)
Equation (19)			
Observed relative variation $\delta G/G$	61	116	121
Calculated max boundary B_G relative variation	62	123	142
Observed contributions to B_G			
$T_G \cdot \left \frac{\&c}{c} \right $	0.3	0.7	1.2
$T_G \cdot \left \frac{\delta \Im(Z_c)}{\Im(Z_c)} \right $	62	122	136
$\left \frac{\delta\Re(Z_c)}{\Re(Z_c)}\right $	0.1	0.5	4.4
Equation (18)			
Observed relative variation $\delta R/R$	9	30	301
Calculated max boundary B_R relative variation	9	32	334
Observed contributions to B_R			
$T_R \cdot \left \frac{\delta C}{C} \right $	0.04	0.2	3
$T_R \cdot \left \frac{\delta \Im(Z_c)}{\Im(Z_c)} \right $	8.8	31.7	327
$\frac{\delta \Re(Z_c)}{\Re(Z_c)}$	0.1	0.5	4.4

More details about the computation of these terms are given in Appendix A.

their respective boundaries confirm that the main uncertainty comes from $\Im(Z_c)$ as stated above.

R-based method

A new procedure to *correct* the imaginary part of Z_c is proposed here. First, an existing Z_c extraction method is used to compute the terms L and C (from (4) and (6)), which only depend on β and the real part of Z_c in the low-loss transmission line approximation. From this first extraction, that we shall label as "1", trusted values for C_1 and L_1 are obtained. Ultimately, we are interested in extracting a trusted value of G toward substrate electrical characterization. But, as we have seen, it is difficult to evaluate $\Im(Z_c)$ with sufficient precision for series losses R_1 and shunt losses G_1 to be well discriminated, and this is especially true for high-quality substrates (low G), and at high frequencies. We therefore propose the R-based method, by which we will suppose that R is perfectly known over the entire frequency range. In practice, R_{forced} is set by the closed-form analytical expressions given in [8], and G_{corr} is computed using

$$G_{corr} = -\omega C_1 + \frac{\gamma^2}{R_{forced} + j\omega L_1}.$$
 (20)

It is important to note that this method is no longer a pure extraction technique, as R_{forced} is now an input to the extraction, and must be accurately known for meaningful results. In general, we found that knowing the metal dimensions along with its conductivity permits a fairly accurate estimation of R_{forced} . Nevertheless, the value of R_1 from the initial extraction is trusted up to at least 5 GHz, as it was observed in section "Sensitivity analysis of the extracted characteristic impedance". Therefore, if an

uncertainty in the metal dimensions or conductivity still persists, a fine tuning of one of these parameters can be performed to ensure a good matching of R_{forced} with R_1 at low frequency.

In practice, only the real part of (20) is taken just to ensure that we have a real value for G, the imaginary part of the right-hand side of (20) is negligible (at least 2 orders of magnitude lower than the real part). The method from [12] provides a valid extraction of $\Re(Z_c)$, because it leads to meaningful values of L, i.e. decreasing smoothly with frequency due to the skin effect, and C, i.e. constant with frequency. It is therefore chosen for the computation of L and C in the present resistance-based method.

The correction applied to $\Im(Z_c)$ is significant. Figure 4 shows the imaginary part of Z_c as well as the series metallic loss (R) and shunt substrate loss (G) using solely [12] (first extraction method, in green) and applying the R-based method to correct $\Im(Z_c)$ (in brown). Once the substrate-related loss parameter (G_{corr}) is well extracted, it still has to be associated to physical loss mechanisms. This is done in the next part.

Inclusion of dielectric losses

The substrate loss term G has been corrected; however, the loss-mechanisms behind it still need to be identified. Figure 4(b) shows that G_{corr} increases with increasing frequency. This behavior is uncharacteristic of a purely conductive substrate and warrants the investigation into frequency-dependent losses, the first of which to be discussed are dielectric losses.

If the model presented in Fig. 2(b) is extended to include dielectric losses, then the shunt admittance of the transmission line can be written as follows:

$$G + j\omega C = \frac{F_{bot}}{\rho_{eff}} + \omega \epsilon_0 [F_{bot} \epsilon_{r,eff} + F_{top} \epsilon_{r,top}], \qquad (21)$$

and G and C become

$$G = \frac{F_{bot}}{\rho_{eff}} + \omega \epsilon_0 [F_{bot} \epsilon''_{r,eff} + F_{top} \epsilon''_{r,top}] , \qquad (22)$$

$$C = \underbrace{\epsilon_{0} \epsilon'_{r,eff} F_{bot}}_{=C_{bot}} + \underbrace{\epsilon_{0} \epsilon'_{r,top} F_{top}}_{=C_{top}}, \tag{23}$$

where complex effective relative permittivities are defined for the top and bottom materials: $\epsilon_{r,eff} = \epsilon'_{r,eff} - j\epsilon''_{r,eff}$ and $\epsilon_{r,top} = \epsilon'_{r,top} - j\epsilon''_{r,top}$.

Due to the frequency linear term of (22), if dielectric loss is non-negligible and (10) is used to evaluate the effective resistivity, then it would yield a ρ_{eff} that is decreasing with frequency. In the present study, the material on top of the CPW metal lines is assumed to be air, corresponding to $\epsilon_{r,top}^{"}=0$. The discussion could easily be extended to a general material for which $\epsilon_{r,top}^{"}\neq 0$. To include conductive and dielectric loss mechanisms, the effective loss tangent is defined as

$$\tan \delta_{eff}(f) \equiv \frac{G(f)}{\omega \epsilon_0 \epsilon_{r,eff}^{'}(f) F_{bot}} = \frac{1}{\rho_{eff} \omega \epsilon_0 \epsilon_{r,eff}^{'}} + \tan \delta_{d_{eff}}, \quad (24)$$

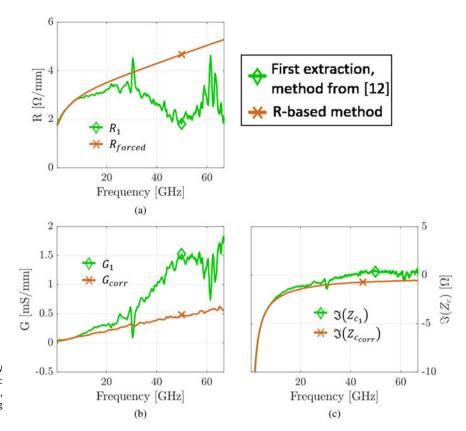


Fig. 4. Distributed R (a) and G (b) line elements of the CPW lines, as well as the imaginary (c) part of its characteristic impedance versus frequency: (i) using only [12] ("d", green), computed from (3) and (5), and (ii) by applying the R-based method ("x", brown), i.e. with (20).

with

$$\tan \delta_{d_{eff}} \equiv \frac{\epsilon''_{r,eff}}{\epsilon'_{r,eff}}, \tag{25}$$

where (25) is the definition of the effective dielectric loss tangent. Then, two frequency-independent fitting parameters $\tilde{\rho}_{eff}$ and $\tan \tilde{\delta}_{d_{eff}}$ are extracted by the least-squares method applied to (24) over an intermediate frequency range. Once those two parameters are extracted, one can assess their consistency by computing two equivalent frequency-dependent material parameter FoMs, i.e.

$$\rho_{eff}(f) = [(\tan \delta_{eff}(f) - \tan \tilde{\delta}_{d_{eff}})\omega \epsilon_{0} \epsilon_{r,eff}(f)]^{-1}, \qquad (26)$$

$$\tan \delta_{d_{eff}}(f) = \tan \delta_{eff}(f) - \frac{1}{\tilde{\rho}_{eff} \omega \epsilon_0 \epsilon_{r,eff}(f)}.$$
 (27)

Detailed study of effective resistivity extraction on a trap-rich substrate

This section shows the effective resistivity extracted in different ways. Section "Description of the new extraction procedure" showed that the substrate shunt loss G is very sensitive to the accurate evaluation of the imaginary part of Z_c . A method to correct it was proposed. Finally, the definition of the effective resistivity is extended to separate the contribution of dielectric and conductive losses to the total substrate losses. The present section's purpose is to illustrate the relevance of each step from the

previous sections on the correct extraction of the effective substrate loss parameters. To that end, the effective resistivity and/or dielectric loss tangent are extracted in many different ways detailed in the following. Note that the propagation constant is the same in all the cases and is extracted from the mTRL algorithm.

- (a) The method from [11] is used to extract Z_c . From γ and Z_c the term G is computed with (5). Equation (10) is used to evaluate the effective resistivity.
- (b) The method from [12] is used to extract Z_c . From γ and Z_c , the term G is computed with (5). Equation (10) is used to evaluate the effective resistivity.
- (c) The method from [12] is used to extract Z_c . The R-based method is applied to correct $\Im(Z_c)$ and (20) is used to compute G. Then, the effective resistivity is evaluated from (10).
- (d) The method from [12] is used to extract Z_c . From γ and Z_c , the term G is computed with (5). Fitting values of $\tilde{\rho}_{eff}$ and $\tan \tilde{\delta}_{d_{eff}}$ are extracted from the fitting of (24). Then, the effective resistivity is evaluated from (26).
- (e) The method from [12] is used to extract Z_c . The R-based method is applied to correct $\Im(Z_c)$ and (20) is used to compute G. Fitting values of $\tilde{\rho}_{eff}$ and $\tan \tilde{\delta}_{d_{eff}}$ are extracted from the fitting of (24). Then, the effective resistivity is evaluated from (26).

Before commenting on the results, let us note that we expect frequency dependency in the ρ_{eff} for cases (a), (b), and (c), because (10) includes all losses that contribute to $\Re(Y_{bot})$ into the term ρ_{eff} .

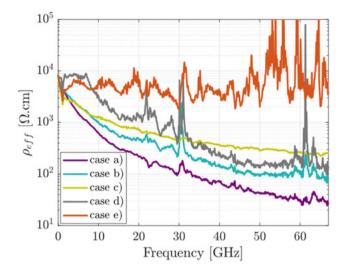


Fig. 5. Effective resistivity versus frequency using different extractions. For cases (a) to (c), (10) is used to evaluate the effective resistivity. For cases (d) and (e), (26) is used to compute the effective resistivity.

We expect a flatter $\rho_{\it eff}$ for cases (d) and (e), as (26) is used, that basically removes linear frequency-dependent losses from the term $\rho_{\it eff}$, and associates them instead to a term $\tan \tilde{\delta}_{d_{\it eff}}$.

Figure 5 shows the extracted effective resistivity in each case from 10 MHz to 67 GHz.

The first case (a) (purple) represents an extraction of the effective resistivity with a commonly used extraction method of Z_c . It is seen that ρ_{eff} decreases significantly from $8 \text{ k}\Omega$.cm to 700Ω .cm up to 10 GHz.

It never reaches a plateau, even though the slow-wave mode is suppressed above a few tens of MHz.

The second case (b) (light blue) uses a more advanced technique to extract Z_c .

Case (c) (yellow) applies the same Z_c extraction technique as case (b), but then the R-based method from section "Description of the new extraction procedure" is applied for a better separation of series (metallic) losses R and shunt substrate losses G at high frequencies (above 10 GHz). Please note that the effective resistivities for cases (b) and (c) are close to each other up to 9 GHz, because G_1 (used in case (b) to extract ρ_{eff} and G_{corr} (used in case c) to extract ρ_{eff} are very close, as was shown and mentioned in Fig. 4(b).

For all the three first cases (a) to (c), the evaluated effective resistivity, using (10), decays for increasing frequencies, which indicates that conductive loss in the substrate is not sufficient to model the term G.

Case (d) (grey) uses an advanced technique to extract Z_c , but does not apply the R-based correction method. Therefore, G is well evaluated up to only 9 GHz (cf. Fig. 4(b)). By including dielectric loss in the extraction using (26), an overall flat (except for the glitch at 1 GHz) effective resistivity is obtained up to 7 GHz. However, the misestimation of $\Im(Z_c)$ prevents an accurate extraction of substrate loss parameters in the mm-wave spectrum above this frequency.

Case (e) (brown) applies the whole procedure explained in section "Description of the new extraction procedure". First, the R-based method corrects the G parameter. Then, ρ_{eff} is obtained from G_{corr} by including dielectric loss in the extraction using (26). In this case, it is shown that ρ_{eff} is flat across the whole

measured frequency range, at a mean of around 5 k Ω .cm, lending physical meaning to this extraction. Indeed it was necessary to remove the dielectric loss contribution (extracted from the curve fitting of (24), yielding tan $\tilde{\delta}_{de\!f\!f}=9.86\times 10^{-3}$) to the total substrate loss. It should be noted that the fluctuations in $\rho_{e\!f\!f}$ are expected, as we are dealing with a highly resistive low-loss substrate, and the G parameter is low.

This case study highlights the need to (i) accurately estimate the imaginary part of the characteristic impedance and (ii) to extend the substrate loss extraction model by the inclusion of dielectric loss, in order to extract meaningful substrate properties. In order to achieve this for low-loss substrates up to mm-wave frequencies, it was required that the metallic loss be perfectly known so that the *R*-based method can be applied. The next section discusses the sensitivity of the extraction technique to the assumed knowledge of *R*.

Sensitivity of the R-based method to input parameters

This new R-based method is no longer a pure extraction method, as it relies on the exact knowledge of the R term of the transmission lines. Because of this, it is pertinent to perform a short sensitivity analysis on the input parameters used to calculate R. The expression in [8] used to compute R takes the CPW dimensions and the metal conductivity as input variables. Table 2 summarizes the extracted $\tilde{
ho}_{eff}$ and $an \delta_{d_{eff}}$ for two different inputs. The first set of input represents the CPW dimensions from the layout design and the metal conductivity expected from the fabrication process. The CPW dimensions of the second set correspond to measurements of the physical lines with a profiler. The metal conductivity (σ_{metal}) of 3.25×10^7 S/m is chosen to ensure a good match with the R extracted from other Z_c extraction methods up to 5 GHz. Indeed, as observed in the previous section, Fig. 3(a) shows that in the lower part of the frequency range, all extraction methods agree on the value and trend (skin effect) of the R(f) curve. The CPW lines were fabricated in our laboratory using aluminum that has a nominal conductivity of 3.82×10^7 S/m [14], but that has been measured to be as low as 2.80×10^7 S/m during our process monitoring. Furthermore, the resolution of the photolithography and wet metal etching steps is of the order of a few microns. This explains the difference between the expected and actual CPW dimensions.

Figure 6 shows R(f), $\rho_{eff}(f)$, and $\tan \delta_{d_{eff}}(f)$ for those inputs. It is important to highlight that even a small variation in the estimation of R(f) leads to significant changes in $\rho_{eff}(f)$, hence the need for the accurate knowledge of R(f). As described above, the effective resistivity, which is directly correlated to the substrate losses, is extracted from the measured insertion loss of a CPW

Table 2. Extracted effective parameters and CPW dimensions used as input parameters for the *R*-based method described in section "Description of the new extraction procedure"

#	Input parameters	$ ilde{ ho}_{ ext{eff}}$ (k Ω .cm)	tan $ ilde{\delta}_{d_{\it eff}}$
1	$W = 26 \mu \text{m}, S = 12 \mu \text{m}, t = 0.89 \mu \text{m},$ $\sigma_{metal} = 2.9 \times 10^7 \text{ S/m}$	5.07	0.00586
2	$W = 22.5 \mu\text{m}, S = 15 \mu\text{m}, t = 0.89 \mu\text{m},$ $\sigma_{metal} = 3.25 \times 10^7 \text{S/m}$	4.78	0.00986

W is the central signal line conductor width, S is the spacing between the conductor line and the ground lines, and t is the metal thickness.

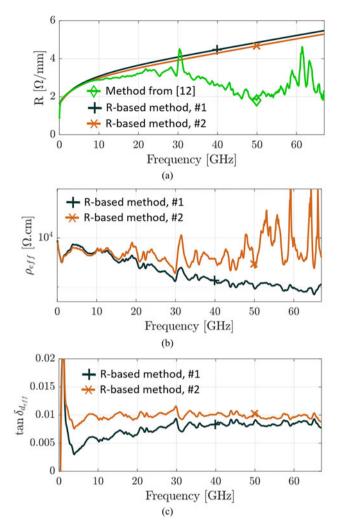


Fig. 6. Series metallic loss R(f) (a) versus frequency. $\rho_{erf}(f)$ (b) computed from (26) and $\delta_{d_{erf}}(f)$ (c) computed from (27) versus frequency. Comparison of the new extraction procedure with two sets of input: #1 ("+", black) and #2 ("x", brown). The R(f) computed from (3) using method [12] to extract Z_c is repeated here ("d", green) to illustrate the good fitting with the other curves at low frequency.

line after withdrawing the conductive losses along the metallic line. When the insertion losses of the line is dominated by the metallic losses, the effective resistivity is very high and quite sensitive to any measurement noise which leads to a pretty noisy corrected $\rho_{eff}(f)$ as observed in Figs 5 and 6(b) in solid brown lines. It is difficult to obtain a smoother curve via measurements, because a variation of the effective resistivity from 5 to 20 k Ω .cm represents a difference of only 5×10^{-3} dB/mm in α .

A small variation or uncertainty in the process can lead to significant differences in the extracted $\tilde{\rho}_{eff}$ and $\tan \tilde{\delta}_{d_{eff}}$. Nevertheless, though the dimensions from #1 give a good low-frequency fitting of R(f), we see that the overall extraction gives frequency-dependent terms for both ρ_{eff} and $\tan \delta_{d_{eff}}$. This suggests that the input $R_{forced}(f)$ for #1, based on σ_{metab} and CPW dimensions, is erroneous. So, in practice, if there is an uncertainty on σ_{metal} and/or the CPW dimensions, various extraction iterations using different pairs of values $[\sigma_{metab}$ dimensions] (close to the expected values) that fit $R_{forced}(f)$ to $R_1(f)$ in the low-frequency range may be carried out, until frequency-independent ρ_{eff} and $\tan \delta_{d_{eff}}$ are obtained.

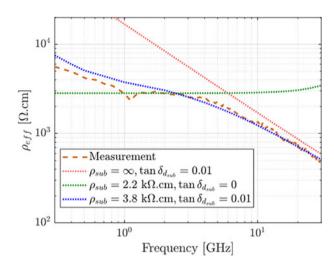


Fig. 7. Effective resistivity computed with (10) from CPW lines measurements (dashed line) and ADS Momentum simulations (dotted lines) on several substrates: only dielectric loss (red), only conductive loss (green), both (blue).

This method provides an accurate extraction of the substrate parameters over a wide frequency range. However, it relies on too strong assumptions to be used as a general method. First, the physical dimensions of the lines must be fairly accurately known. Second, the expressions used for *R* assume a single nonmeshed metal layer, without dummy fills, or layer stacking with via arrays. Indeed, the presence of dummy fills is acknowledged to increase *R* by several percent with frequency [15]. An alternative method potentially applicable in a more general case could be the estimation of *R* with very precise 3-D electromagnetic (EM) simulations, similarly to what was done in [16]. In that case, a careful layout is needed to simplify the simulation complexity.

Cutoff frequency

It is possible to define a cutoff frequency (f_c) that separates the region where the conductive losses in the substrate dominate over the dielectric losses:

$$f_c \equiv \frac{1}{2\pi\epsilon_0 \tilde{\rho}_{eff} \epsilon'_{r eff} \tan \tilde{\delta}_{d_{eff}}} \ . \tag{28}$$

The cutoff frequency depends on the value of the effective resistivity and dielectric losses. For instance, if $\tilde{\rho}_{eff}=4.78$, $\tan \tilde{\delta}_d=0.00986$, and $\epsilon_{r,eff}'=11.7$, then $f_c=3.26$. Common EM simulators (e.g. ADS Momentum, HFSS) do not allow to define the loss tangent along with the DC resistivity for the same material. Figure 7 shows $\rho_{eff}(f)$ computed from (10) based on ADS Momentum simulations of a CPW line mounted on different types of substrate. Although using (10) to evaluate $\rho_{eff}(f)$ is not physically meaningful as discussed in the previous sections, it is used in this section and the further sections, because it includes all substrate-related losses and therefore information about the substrate loss parameters. The term G(f) or $\tan \delta_{eff}(f)$, with (24), could have been equivalently used, but the $\rho_{eff}(f)$ is chosen, because it is more often used to describe the RF quality of silicon substrates.

The dashed brown line corresponds to measurements extracted with the *R*-based method to correct $\Im(Z_c)$ based on the second set of inputs, i.e. using the measured CPW

dimensions. The red and green dotted lines correspond to simulations of a substrate with only dielectric and conductive losses, respectively. The dotted blue line represents a substrate consisting of two materials, each one of them including one type of substrate loss. As one could expect, specifying only dielectric losses instead of the substrate resistivity is much more accurate above 4 GHz. For highly resistive substrates with relatively large dielectric losses, it is more accurate to model the substrate by its dielectric losses rather than its conductive losses. Moreover, by considering only dielectric losses in our case, the total losses are underestimated by <0.007 dB over the whole frequency band, compared with the case when both losses are accounted for. Instead, the substrate without dielectric losses underestimates by 0.1 dB the total losses at 50 GHz and this discrepancy increases with frequency. Although our sample does not necessarily represent substrates used in commercial CMOS SOI technologies, care should be taken in circuit design to verify that dielectric losses are not dominating over conductive losses in low-loss substrates.

Validation up to 220 GHz

This section validates the new extraction procedure by comparing the extrapolated model developed in the 0.01–67 GHz range with measurements up to 220 GHz. The first part shows measurements of CPW lines on the same TR substrate that has been used so far. The second part applies the same extraction procedure and comparison to measurements of CPW lines on two different high resistivity (HR) silicon substrates.

Extended frequency range

The CPW lines are measured in a second setup from 140 to 220 GHz using an Agilent 4-port PNA-X vector network analyzer and a pair of GSG Infinity probes from Cascade Microtech. A Line-Reflect-Reflect-Match (LRRM) calibration is performed on an ISS calkit and the mTRL algorithm is applied to extract the propagation constant.

Figures 8(a) and 8(b) show the real and imaginary parts of the characteristic impedance extracted with the method from [12] (green) and the *R*-based method to correct $\Im(Z_c)$ (brown). The measurements in the 140-220 GHz range are noisier, which is expected, and may also be due to the excitation of higher-order CPW mode. Please observe the very strong correction obtained in $\Im(Z_c)$ above 140 GHz thanks to the *R*-based method. Figure 8 confirms that the new method works well at least as far as 220 GHz. The $\rho_{eff}(f)$ computed with (10) is also extracted from the R-based method and is in very good agreement with the lumped-element equivalent circuit from Fig. 1(b) in dotted line (cf. Fig. 8(c)). This equivalent circuit uses [8] to compute R and L from the CPW line dimensions, (22) and (23) to compute G and C with the constant substrate parameters ($\epsilon_{r,eff}^{'},\,\tilde{\rho}_{eff},$ and $\tan \delta_{d_{eff}}$) extracted from the 0.01-67 GHz measurements and extrapolates G with (22) up to 220 GHz, with excellent agreement.

High resistivity samples

Two other wafers were manufactured in our facilities. They consist of a 500 μ m-thick bulk HR substrate of 5 k Ω .cm nominal resistivity with a 200 nm-thick dielectric layer on top. On one of the wafers, an SiO₂ oxide was thermally grown (the sample is called here HRSiO₂), and on the other, an Al₂O₃ layer is deposited by atomic layer deposition (the sample is called here HRAl₂O₃).

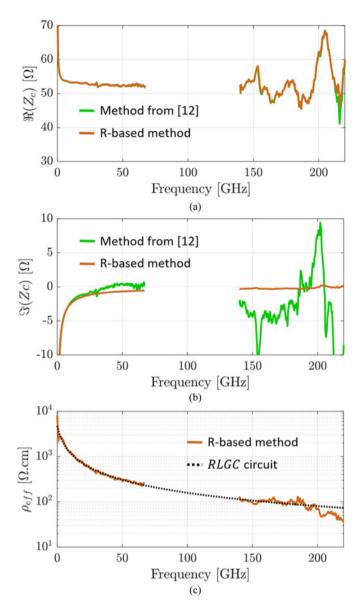


Fig. 8. Real (a) and imaginary (b) parts of the characteristic impedance and effective resistivity computed from (10) versus frequency, from CPW lines measurements of the trap-rich silicon substrate. In green, Z_c extracted using the method from [12]. In brown, measurements extraction corrected with the R-based method. In dotted black line, reconstructed and extrapolated lumped-element equivalent circuit.

Finally, the same CPW lines as the TR sample are patterned on top of the dielectric. All wafers are measured in the two (low and high frequency) setups.

Figures 9 and 10 present the extracted characteristic impedance and effective resistivity using the method from [12] and the one described in section "Description of the new extraction procedure" that applies the *R*-based method. The measured 140–220 GHz effective resistivity is again in very good agreement with the extrapolated data from the lumped-element equivalent circuit extracted from the lower frequency range (0.01–67 GHz) with the associated substrate parameters displayed in Table 3.

The value of $\tilde{\rho}_{eff}$ of the two HR samples is much lower than the nominal resistivity of the bulk material. It is the consequence of unavoidable fixed charges in the dielectric layer that appear during the manufacturing process and induce a highly conductive

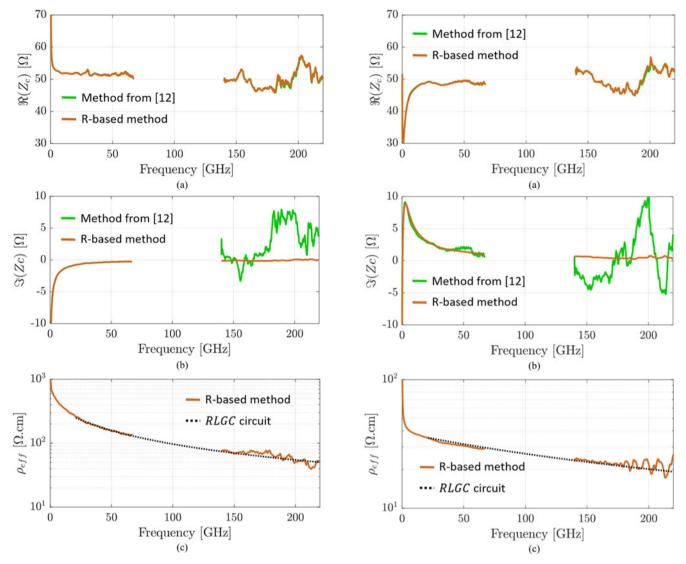


Fig. 9. Real (a) and imaginary (b) parts of the characteristic impedance and effective resistivity computed from (10) versus frequency, from CPW lines measurements of the $HRSiO_2$ silicon substrate. In green, Z_c extracted using the method from [12]. In brown, measurements extraction corrected with the R-based method. In dotted black line, reconstructed and extrapolated lumped-element equivalent circuit.

Fig. 10. Real (a) and imaginary (b) parts of the characteristic impedance and effective resistivity computed from (10) versus frequency, from CPW lines measurements of the ${\rm HRAl_2O_3}$ silicon substrate. In green, Z_c extracted using the method from [12]. In brown, measurements extraction corrected with the *R*-based method. In dotted black line, reconstructed and extrapolated lumped-element equivalent circuit.

sheet in the silicon at the interface of the dielectric. It is a well-known phenomenon also called parasitic surface conduction (PSC) [17]. The tan $\tilde{\delta}_{d_{\rm eff}}$ is larger for the HRAl₂O₃ sample than for the other two, both consisting of a thermal growth of SiO₂. It is consistent with the fact that bulk Al₂O₃ dielectric has a larger loss tangent than bulk SiO₂. However, the values of dielectric loss tangent extracted here from our samples HRAl₂O₃ and HRSiO₂ are approximately two orders of magnitude larger than the values of dielectric loss tangent of bulk Al₂O₃ and SiO₂ reported in the literature [18]. This discrepancy is discussed in the next section.

Discussion about dielectric loss

The values of dielectric loss extracted in the previous section are quite large compared to the dielectric loss of bulk silicon, silicon dioxide, and aluminum oxide, which are in the order of 10^{-4} [18]. The value of effective resistivity is consistent with the measured

samples and could be predicted. The 4.7 k Ω .cm of the TR wafer is close to the 5 k Ω .cm of the nominal resistivity of the bulk material, meaning that the PSC is effectively eliminated. However, the measurements indicate the presence of additional losses. This can be understood from Fig. 11. This figure displays the total propagation losses (α), in green, extracted using the mTRL algorithm from CPW lines measurements on the TR sample. Most of the losses originate from conductive metal loss due to

Table 3. Extracted effective substrate loss parameters from all wafers

Wafer	$ ilde{ ho}_{\it eff}$ ($\Omega.cm$)	$ an ilde{\delta}_{d_{\mathit{eff}}}$
TR	4780	0.00986
HRSiO ₂	426	0.013
HRAl ₂ O ₃	38.8	0.018

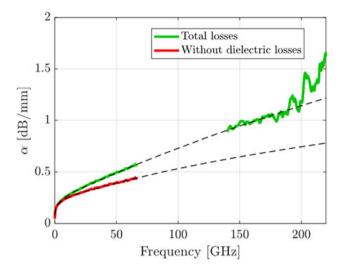


Fig. 11. Total propagation losses in solid green lines from CPW lines measurements on the TR wafer. In solid red line, the total propagation losses from which the dielectric loss has been subtracted. The dotted lines correspond to the reconstructed *RLGC* lumped-element equivalent circuit in each case.

the skin effect. When we subtract the dielectric loss from the total losses, we obtain the red curve, which is significantly lower than the green curve. This red curve includes all losses from metallic loss to conductive loss in the substrate, within the assumptions made by the model. By mapping the measurements to the model from (22), we associated the additional losses (the difference between the green and red lines) to dielectric losses. These additional losses increase with frequency, in a monotonic fashion and linearly proportional to the frequency. Dielectric loss fits this trend; however, we must remain wary when confronted by the quite large values of $\tan\tilde{\delta}_{deff}$, higher than expected for the materials in the substrate. Indeed, another possibility is that these additional losses (at least partially) could have another origin that is not accounted for in the current model, i.e. (i) surface roughness and (ii) surface wave radiation.

One-port reflect structure

An additional structure is measured in the low-frequency range. It is a one-port reflective structure with the layout displayed in Fig. 12. The cross-section is similar to a CPW line and we can subsequently map the measured admittance Y into a conductive

and a capacitive component similarly to the G and C distributed elements of a transmission line. By doing so, and applying (10), one can compare the substrate-related losses from this reflective structure (solid blue line) with the ones coming from the CPW line (solid brown line), cf. Figure 13. The losses are also shown to increase with frequency, meaning that surface roughness is not the cause of the additional observed losses, as there is no propagation in this structure.

Furthermore, ADS Momentum simulations are performed with this one-port structure in a similar fashion to section "Cutoff frequency". Different substrate definitions are compared (dotted lines) in Fig. 13. The one that includes both conductive and dielectric losses in the substrate fits more accurately to the measurements. Moreover, additional simulations where the boundary condition is changed from a semi-infinite silicon substrate to a floating finite metallic plane (emulating the chuck in on-wafer RF measurement conditions) are carried out. The first boundary condition is free of the parallel-plate mode, which is the first higher order mode stimulated in CPW lines with a metallic back plane, whereas the second boundary condition includes such modes. The difference in both cases is so small that it is not shown here.

Finally, the ISS was also measured with the same calibration from 0.01 to 67 GHz. The same *R*-based method is applied to the CPW lines measurements. Figure 14 shows the effective loss tangent computed with (24) for the studied TR (brown) silicon substrate and the ISS (blue) using the same methodology presented in this paper. It is observed that the dielectric loss cannot be extracted from the measurement of the ISS, because it is within the noise floor of the setup, contrarily to the TR wafer.

The application of the whole extraction method to CPW lines of the ISS calkit confirms the existence of those non-negligible additional losses highlighted above. The frequency-dependent substrate loss observed for the one-port reflect structure discards the possibility for surface roughness to be at the origin of the observed additional losses highlighted above. The increasing difference between the red and green curves of Fig. 11 starting below 50 GHz suggests that surface wave radiation is not causing these additional losses either. We can therefore confidently identify them as being dielectric losses, although they are significantly larger than what is expected from the bulk materials constituting the samples. The origin might come from the manufacturing process of the wafers that was performed in our academic facilities. However, identifying and correcting the source of the high dielectric losses is an investigation that is still ongoing.

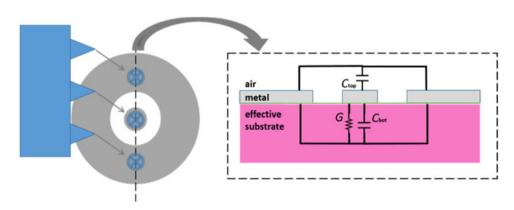


Fig. 12. Top view (left in grey) of the one-port reflective structure and its cross-section (right).

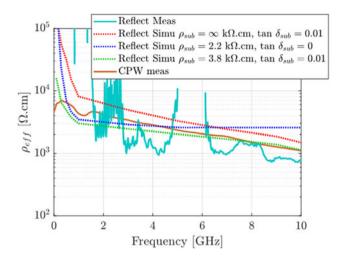


Fig. 13. Effective resistivity computed with (10) from the one-port reflective structure (solid blue line) and CPW measurements (solid brown line) of the TR wafer, along with ADS Momentum simulations of the reflect (dotted lines) for different substrate definitions: only dielectric loss (red), only conductive loss (blue), both (green).

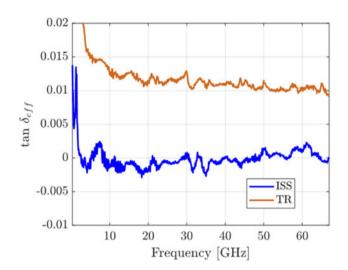


Fig. 14. Effective loss tangent versus frequency, computed with (24) using the *R*-based method from measurements of CPW lines on two different substrates: the ISS (blue) and the TR silicon substrate (brown).

Conclusion

A wide frequency band analysis of the ρ_{eff} is presented. It is demonstrated that an accurate extraction of the imaginary part of the characteristic impedance is critical to the estimation of ρ_{eff} especially for low-loss substrates for mm-wave applications. To that end, a new method correcting $\Im(Z_c)$ is introduced. It computes the metallic losses of a CPW line and uses it to correct the extracted substrate G term. It gives a correct extraction of the substrate parameters over a wide frequency range. However, it is not well suited as a general method, because it relies on too strong assumptions, i.e. on the exact knowledge of the series metallic losses R of the line. For highly resistive substrates, the dielectric loss may become non-negligible above a few GHz yielding the original definition of ρ_{eff} (10) obsolete. A new formulation is proposed in (26) and (27). It separates conductive and dielectric

Table 4. Sensitivity analysis of R and G to a variation in C and Z_c , based on (31) and (33)

Equation (31) Observed relative variation $\delta G/G$ 61 116 122 Calculated max boundary B_G relative variation T_G 3.02 1.42 0.29 $\left \frac{BC}{C}\right $ 0.1 0.5 4.3 $\left \frac{\partial S(Z_f)}{\Im(Z_f)}\right $ 20.5 85.7 467 Observed contributions to B_G $T_G \cdot \left \frac{BC}{C}\right $ 0.3 0.7 1.2 $T_G \cdot \left \frac{BC}{C}\right $ 0.3 0.7 1.2		10	20	50
Observed relative variation $\delta G/G$ 61 116 123 Calculated max boundary B_G relative variation 62 123 142 T_G 3.02 1.42 0.29 $\frac{ \delta C }{ C }$ 0.1 0.5 4.3 $\frac{ \delta S (Z_c) }{ S(Z_c) }$ 20.5 85.7 46 Observed contributions to B_G $T_G \cdot \frac{ \delta C }{ S(Z_c) }$ 0.3 0.7 1.2 $T_G \cdot \frac{ \delta S (Z_c) }{ S(Z_c) }$ 62 122 136 $\frac{ \delta S (Z_c) }{ S(Z_c) }$ 0.1 0.5 4.4	f (GHz)	(%)	(%)	(%)
Calculated max boundary B_G relative variation 62 123 142 T_G 3.02 1.42 0.29 $\left \frac{\mathcal{B}C}{\mathcal{C}}\right $ 0.1 0.5 4.3 $\left \frac{\mathcal{B}S(Z_c)}{\mathcal{S}(Z_c)}\right $ 20.5 85.7 46 Observed contributions to B_G 0.3 0.7 1.2 $T_G \cdot \left \frac{\mathcal{B}C}{\mathcal{B}(Z_c)}\right $ 62 122 136 $\left \frac{\mathcal{B}R(Z_c)}{\mathcal{R}(Z_c)}\right $ 0.1 0.5 4.4	Equation (31)			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Observed relative variation $\delta G/G$	61	116	121
$ \begin{vmatrix} \frac{\delta C}{C} \\ \frac{\delta C}{C} \end{vmatrix} $		62	123	142
$ \begin{vmatrix} \frac{\delta \Im(Z_G)}{\Im(Z_G)} \end{vmatrix} \qquad 20.5 \qquad 85.7 \qquad 460 $ Observed contributions to B_G $ T_G \cdot \frac{ \delta G }{ C } \qquad 0.3 \qquad 0.7 \qquad 1.2 $ $ T_G \cdot \frac{ \delta \Im(Z_G) }{\Im(Z_G)} \qquad 62 \qquad 122 \qquad 136 $ $ \frac{ \delta \Re(Z_G) }{\Re(Z_G)} \qquad 0.1 \qquad 0.5 \qquad 4.4 $	T_G	3.02	1.42	0.291
Observed contributions to B_G $T_G \cdot \frac{ \delta C }{ C } \qquad \qquad 0.3 \qquad 0.7 \qquad 1.2$ $T_G \cdot \frac{ \delta D(Z_c) }{ N(Z_c) } \qquad \qquad 62 \qquad 122 \qquad 136$ $\frac{ \delta B(Z_c) }{ R(Z_c) } \qquad \qquad 0.1 \qquad 0.5 \qquad 4.4$	<u>&C</u>	0.1	0.5	4.3
$ T_G \cdot \frac{ \delta C }{C} $ $ T_G \cdot \frac{ \delta N(Z) }{N(Z_C)} $ $ 62 $ $ 122 $ $ \frac{ \delta N(Z_C) }{R(Z_C)} $ $ 0.1 $ $ 0.5 $ $ 4.4 $	$\frac{\delta \Im(Z_c)}{\Im(Z_c)}$	20.5	85.7	467
$T_G \cdot \frac{ \delta \Im(Z_c) }{ \Im(Z_c) }$ 62 122 136 $\frac{ \delta \Re(Z_c) }{ \Re(Z_c) }$ 0.1 0.5 4.4	Observed contributions to B_G			
$\left \frac{\delta R(Z_i)}{R(Z_i)}\right $ 0.1 0.5 4.4	$T_G \cdot \left \frac{\delta C}{C} \right $	0.3	0.7	1.2
	$T_G \cdot \left \frac{\delta \Im(Z_c)}{\Im(Z_c)} \right $	62	122	136
Equation (33)	$\frac{\left \delta \Re(Z_c)\right }{\Re(Z_c)}$	0.1	0.5	4.4
	Equation (33)			
Observed relative variation $\delta R/R$ 9 30 30	Observed relative variation $\delta R/R$	9	30	301
Calculated max boundary B_R relative 9 32 334 variation		9	32	334
T _R 0.43 0.37 0.7	T_R	0.43	0.37	0.7
$\left \frac{\delta C}{C}\right $ 0.1 0.5 4.3	$\left \frac{\delta C}{C}\right $	0.1	0.5	4.3
$\frac{\left \frac{\partial \mathbb{N}(Z_{c})}{\mathbb{N}(Z_{c})}\right }{\left \frac{\partial \mathbb{N}(Z_{c})}{\mathbb{N}(Z_{c})}\right }$ 20.5 85.7 46	$\frac{\delta \Im(Z_c)}{\Im(Z_c)}$	20.5	85.7	467
Observed contributions to B_R	Observed contributions to B_R			
$T_R \cdot \left \frac{\delta C}{C} \right $ 0.04 0.2 3	$T_R \cdot \left \frac{\delta C}{C} \right $	0.04	0.2	3
$T_R \cdot \left \frac{\delta \Im(Z_c)}{\Im(Z_c)} \right $ 8.8 31.7 32	$T_R \cdot \left \frac{\delta \Im(Z_c)}{\Im(Z_c)} \right $	8.8	31.7	327
$\left \frac{\delta \Re(Z_t)}{\Re(Z_t)}\right $ 0.1 0.5 4.4	$\frac{\left \delta \Re(Z_c)\right }{\Re(Z_c)}$	0.1	0.5	4.4

The value of each observed term as well as its contribution to the relative variation in *R* and *G* is reported here.

losses in two distinct FoMs. Both are important to characterize the total losses of a substrate over a wide frequency range. For chip design on this TR substrate at mm-wave frequencies, it is therefore more accurate to model the substrate losses by only dielectric losses than only conductive losses. On the other hand, RF designs in the sub-6 GHz range may prefer a conductive substrate model, especially for lower performance substrates. The validity of the $\Im(Z_c)$ correction is confirmed up to 220 GHz. The model including dielectric and conductive losses from (22) is also validated up to 220 GHz on various substrates. The origin of the loss contribution that is not metallic, nor conductive, is discussed. With measurements of an additional oneport reflective structure and CPW lines on an ISS calkit, this loss contribution is confidently identified as dielectric losses, justifying the large value ($\approx 10^{-2}$) of loss tangent that is extracted. The results obtained show that the method proposed in this paper can be applied to different flavors of substrates in order to extract accurately two FoMs, ρ_{eff} and $\tan \delta_{d_{eff}}$, representing their total RF losses at microwave and mm-wave frequencies.

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References

- Lederer D and Raskin J-P (2005) Effective resistivity of fully-processed SOI substrates. Solid-State Electronics 49, 491–496.
- Ben Ali K, Roda Neve C, Gharsallah A and Raskin J (2011) Ultrawide frequency range crosstalk into standard and trap-rich high resistivity silicon substrates. IEEE Transactions on Electron Devices 58, 4258–4264.
- Liu S, Zhu L, Allibert F, Radu I, Zhu X and Lu Y (2017) Physical models
 of planar spiral inductor integrated on the high-resistivity and trap-rich
 silicon-on-insulator substrates. *IEEE Transactions on Electron Devices*64, 2775–2781.
- Roda Neve C and Raskin J (2012) RF harmonic distortion of CPW lines on HR-Si and trap-rich HR-Si substrates. *IEEE Transactions on Electron Devices* 59, 924–932.
- Kazemi Esfeh B, Rack M, Makovejev S, Allibert F and Raskin J (2018) A SPDT RF switch small- and large-signal characteristics on TR-HR SOI substrates. IEEE Journal of the Electron Devices Society 6, 543–550.
- Pozar DM (2011) Microwave Engineering, 4th Edn. Hoboken, New Jersey: Wiley.
- Marks RB and Williams DF (1992) A general waveguide circuit theory. Journal of Research of the National Institute of Standards and Technology 97, 553–562.
- Heinrich W (1993) Quasi-TEM description of MMIC coplanar lines including conductor-loss effects. *IEEE Transactions on Microwave Theory and Techniques* 41, 45–52.
- Nyssens L, Rack M and Raskin J (2019) Effective resistivity extraction of low-loss silicon substrate at millimeter-wave frequencies. 2019 14th European Microwave Integrated Circuits Conference (EuMIC), Paris, France, pp. 1–4.
- Marks RB (1991) A multiline method of network analyzer calibration. IEEE Transactions on Microwave Theory and Techniques 39, 1205–1215.
- 11. **Dehan M** (2003) Characterization and Modeling of SOI RF Integrated Components (Ph.D. dissertation) Université catholique de Louvain.
- Nyssens L, Rack M and Raskin J-P (2019) New method for accurate transmission line characterization on low-loss silicon substrate at millimeter-wave frequencies. 2019 Microwave Technology and Techniques Workshop ESA-ESTEC, April 2019.
- Williams DF, Arz U and Grabinski H (1998) Accurate characteristic impedance measurement on silicon. 1998 IEEE MTT-S International Microwave Symposium Digest (Cat. No.98CH36192), vol. 3, Baltimore, MD, USA, pp. 1917–1920.
- Hayt WH and Buck JA (2011) Engineering Electromagnetics, 8th Edn. Singapore: McGraw-Hill.
- Tsuchiya A and Onodera H (2007) Measurement of interconnect loss due to dummy fills. 2007 IEEE Workshop on Signal Propagation on Interconnects, Genova, pp. 241–244.
- Galatro L and Spirito M (2017) Millimeter-wave on-wafer TRL calibration employing 3-D EM simulation-based characteristic impedance extraction. IEEE Transactions on Microwave Theory and Techniques 65, 1315–1323
- 17. Yunhong Wu S, Gamble Armstrong BM, Fusco VF and Stewart JAC (1999) SiO/sub2/interface layer effects on microwave loss of high-resistivity CPW line. IEEE Microwave and Guided Wave Letters 9, 10–12.
- 18. **Geyer RG** (1990) NIST Technical Note 1338: Dielectric Characterization and Reference Materials. NIST, Technical Report.

Appendix

Appendix A: Sensitivity analysis

From the expressions (16) and (17), we would like to evaluate the sensitivity of R and G to a misestimation of the characteristic impedance. Considering a small perturbation, the variation in G is computed as

$$\delta G = \frac{\omega \Im(Z_c)}{\Re(Z_c)} \delta C + \frac{\omega C}{\Re(Z_c)} \delta \Im(Z_c) - \frac{\alpha + \omega C \Im(Z_c)}{\Re(Z_c)} \delta \Re(Z_c). \tag{A.1}$$

Since we are only interested in the impact of the Z_c extraction technique and that an accurate estimation of γ is used, the term $\delta\alpha$ was neglected. With some manipulations, (A.1) can be expressed as

$$\frac{\delta G}{G} = \frac{\omega C \Im(Z_c)}{\alpha + \omega C \Im(Z_c)} \left(\frac{\delta C}{C} + \frac{\delta \Im(Z_c)}{\Im(Z_c)} \right) - \frac{\delta \Re(Z_c)}{\Re(Z_c)}. \tag{A.2}$$

By taking the absolute value, (A.2) is bounded by

$$\left|\frac{\delta G}{G}\right| \le B_G = T_G \cdot \left[\left|\frac{\delta C}{C}\right| + \left|\frac{\delta \Im(Z_c)}{\Im(Z_c)}\right|\right] + \left|\frac{\delta \Re(Z_c)}{\Re(Z_c)}\right|,\tag{A.3}$$

with

$$T_G \equiv \left| \frac{\omega C\Im(Z_c)}{\alpha + \omega C\Im(Z_c)} \right|. \tag{A.4}$$

A similar reasoning for R yields

$$\left|\frac{\delta R}{R}\right| \le B_R = T_R \cdot \left[\left|\frac{\delta C}{C}\right| + \left|\frac{\delta \Im(Z_c)}{\Im(Z_c)}\right|\right] + \left|\frac{\delta \Re(Z_c)}{\Re(Z_c)}\right|,\tag{A.5}$$

with

$$T_R \equiv \left| \frac{\omega C \Im(Z_c)}{\alpha - \omega C \Im(Z_c)} \right|. \tag{A.6}$$

The B_R and B_G terms define the maximum boundary values of relative variations for R and G, respectively. To carry out the sensitivity analysis and verify the correctness of these expressions, each term $|\delta x/x|$, where x is G, R, C, $\Im(Z_c)$, or $\Re(Z_c)$, must be evaluated. The denominator, x, is extracted from the TR measurements based on the Z_c extraction method from [12]. To avoid the strict evaluation of the uncertainty δx based on measurement repeatability and quality of the measured structures, which is not straightforward, the numerator, δx , is computed here as the difference between the value of the parameter extracted using [12] and of that using [11]. Although this way does not provide a strict uncertainty analysis, it gives a good estimation of the error made using different Z_c extraction techniques.

Table 4 reports the value of each term present in (A.3) and (A.5). The computed values of the boundaries B_G and B_R are quite close to the observed variations in $|\delta G/G|$ and $|\delta R/R|$, which validates these expressions. From this table, the dominating uncertainty comes from $\Im(Z_c)$, because of its value very close to zero.

The observation that $\Im(Z_c)$ tends to zero at high frequency for low-loss substrates can be understood if simplified analytical expressions are used for R, L, G, and C. At sufficiently high frequency, assuming a constant material permittivity, C is constant, L tends to the constant value of external inductor of an ideal conductor [8], G is given by (22), and R is approximately proportional to \sqrt{f} due to skin effect:

$$R \approx k_0 + k_1 \sqrt{\omega}. \tag{A.7}$$

In the low-loss assumption, $\Re(Z_c)$ is approximately constant from (13) and

$$\Im(Z_c) \approx \frac{1}{\omega C} \left(\sqrt{\frac{L}{C}} \frac{F_{bot}}{\rho_{eff}} - \sqrt{\frac{C}{L}} k_0 \right) - \frac{k_1}{\sqrt{\omega L C}} + \frac{\epsilon_0 \epsilon_{r,eff}'' F_{bot}}{C} \sqrt{\frac{L}{C}}. \tag{A.8}$$

Taking the limit for $\omega \to \infty$:

$$\lim_{\omega \to \infty} \Im(Z_c) = \frac{\epsilon_0 \epsilon'_{r,eff}}{C} \sqrt{\frac{L}{C}}.$$
 (A.9)

Using the lumped-element equivalent circuit (cf. Fig. 1(b)) extracted from the CPW dimensions and measurements as explained in section "Validation up to 220 GHz", the right-hand side of (A.9) is evaluated at $0.33\,\Omega$.



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