International Journal of Microwave and Wireless Technologies

cambridge.org/mrf

Research Paper

Cite this article: Song K, Luo M, Zhong C, Chen Y (2020). High-isolation diplexer based on dual-mode substrate integrated waveguide resonator. International Journal of Microwave and Wireless Technologies **12**, 288–292. https:// doi.org/10.1017/S1759078719001387

Received: 30 August 2019 Revised: 24 September 2019 Accepted: 25 September 2019 First published online: 24 October 2019

Key words:

High isolation; substrate integrated waveguide; diplexer; dual-mode resonator

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High-isolation diplexer based on dual-mode substrate integrated waveguide resonator

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Abstract

A high-isolation diplexer based on a dual-mode substrate integrated waveguide (SIW) resonator is proposed in this paper. Based on the theory of the dual-mode resonator, the miniaturized diplexers are designed by using the SIW dual-mode resonators. The superior isolation of the diplexers is obtained because the two operating modes of the dual-mode SIW resonators are not directly coupled and there is no interference with each other. In order to further improve the isolation of the circuit, the number of the order of the diplexer is added. Equivalent circuits are given to analyze and design the dual-mode high-isolation diplexers. Detailed analyses are given according to the equivalent circuits. The dual-mode third-order and fourth-order diplexers are designed and fabricated. The measured results agree well with the simulated ones. The total sizes of the fabricated third-order and fourth-order diplexers are $1.78\lambda g \times 2.64\lambda g$ and $1.79\lambda g \times 3.63\lambda g$, respectively.

Introduction

At the background of the rapid development of the microwave communication systems, the demand for various high-performance passive or active circuits has increased greatly [1–23]. Among these different circuits, the diplexer, which is used to transfer signals of different frequency bands to different channels, shows its indispensable role in communication systems [1]. The traditional diplexer consists of two bandpass filters and a three-port matching network. In order to realize the miniaturization of the resonant unit, a variety of miniaturized structures have been proposed: step impedance resonator [2–4], dual-mode structure [5–14], and defective ground structure [15 16]. Because the dual-mode structure can reduce the number of the resonators and the overall size of the circuit, it is more widely used in microwave systems compared with other structures. Moreover, a dual-mode resonator based on the substrate integrated waveguide (SIW) structure has better frequency selection and lower insertion loss, making it a better choice for high-frequency circuit design [17–20].

What's more, another important component of the diplexer is the common part connecting the two channels. The traditional matching network takes the form of a T-shaped structure [21-23]. Though it can achieve high isolation and good impedance matching, it often takes up a lot of space [21-23]. So the design of common part in the form of a common resonator is introduced in order to miniaturize the circuit.

In this paper, a dual-mode diplexer with high isolation, compact size, and good out-of-band rejection is proposed. It is mainly designed by using the SIW dual-mode resonator. To get higher isolation of the diplexer, it is useful to increase the number of the order of the circuit. Finally, the presented dual-mode high-isolation diplexers are designed and fabricated. The total sizes of the fabricated third-order and fourth-order diplexers are $1.78\lambda g \times 2.64\lambda g$ and $1.79\lambda g \times 3.63\lambda g$, respectively.

Analysis and design

The structures of the presented dual-mode high-isolation diplexers have been shown in Fig. 1. The overall structures of the third-order and fourth-order circuits are shown in Figs 1(a) and 1 (b), respectively. The structures of the circuits are based on the dual-mode SIW resonators, which have the advantages of high quality factor Q and low loss. It can be seen that the number of the required resonant cavities is reduced by half due to the use of dual-passband filters which are based on SIW dual-mode resonators. In addition, the common port of the diplexer is designed with a common resonant cavity structure, so the size of the circuit is more compact without the additional matching circuit.

In order to realize the coupling of the two working modes between the resonant cavities, a coupling structure as shown in Fig. 1 is adopted. The coupling strength is controlled by the size



Fig. 1. Structures of the high-isolation diplexers. (a) Structure of the third-order diplexer and (b) structure of the fourth-order diplexer.



Fig. 2. The circuit topology of the multi-order diplexer.

of the coupling window *S* and the position of the coupling window *Sa*. The larger the coupling window, the stronger the coupling strength between the two resonators.

The circuit topology of the multi-order diplexer is shown in Fig. 2. R_L and R_H enclosed by dotted frames represent TE₁₀₂ mode and TE₂₀₁ mode of the dual-mode resonator respectively. Both R_1 and R_2 represent TE₁₀₁ modes, which are the dominant modes of the single-mode resonators connected at the end of the multi-order diplexer. The dotted lines in Fig. 2 indicate that many dual-mode resonators can be inserted to achieve multi-stage cascade. The use of a dual-mode resonators, but also eliminates additional matching circuits, which is helpful to achieve the miniaturization of the circuit.

The resonant frequencies f_{102} and f_{201} of the SIW rectangular resonator in TE₁₀₂ mode and TE₂₀₁ mode can be obtained by the following equations:

$$f_{102} = \frac{c}{2\sqrt{\varepsilon_r \mu_r}} \sqrt{\left(\frac{1}{2L}\right)^2 + \left(\frac{1}{W}\right)^2} \tag{1}$$

$$f_{201} = \frac{c}{2\sqrt{\varepsilon_r \mu_r}} \sqrt{\left(\frac{1}{L}\right)^2 + \left(\frac{1}{2W}\right)^2} \tag{2}$$

Here, *L* and *W* are the length and width of the rectangular resonator, respectively.

Figure 3 shows the electric field distributions of the fourthorder diplexer. It can be seen that the electric field distributions of the two paths are orthogonal and there is no direct coupling with each other. Because the two operating modes are not directly coupled in this circuit, the mutual interference between the two passbands is small and there is superior isolation of the diplexer.

In order to further separate the two resonant frequencies of the dual-mode resonator at the output ports and improve the isolation between the output ports, two single-mode resonators are connected at the output ports, which are operating in the dominant mode TE_{101} .

In order to get superior isolation and better out-of-band rejection of the proposed diplexer, it is necessary to increase the number of the order of the circuit.



Fig. 3. Electric field distributions of the fourth-order diplexer. (a) Electric field distribution of the low-passband situation and (b) electric field distribution of the high-passband situation.

Pin	Lin	Win	Wa	La	S	Sa	Wb
12.6	4.3	1.6	20.7	18.5	5.55	1.1	20.5
Lb	L2	S2	Win2	Lin2	S3	Win3	Lin3
18.5	10.1	4.08	0.2	3	4.18	0.2	2.5
L3	W50						
9.15	1.11						

Table 1. The final physical sizes of the third-order diplexer (unit: mm)

Table 2. The final physical sizes of the fourth-order diplexer (unit: mm)

Pin	Lin	Win	Wa	La	S	Sa
13.6	5.4	1	21.2	18.6	5.6	0.9
Lb	Wb	S1	Sal	Lc	Lc2	S2
18.5	21	5.1	1.4	18.55	10.4	3.85
Lin2	Win2	L3	S3	Lin3	Win3	W50
2.8	0.2	9.2	4.2	3.1	0.2	1.11



Fig. 4. Physical photos of the diplexers. (a) Third-order diplexer and (b) fourth-order diplexer.

Implementation and measurements

Through the above analyses, the two high-isolation dual-mode diplexers are designed and fabricated with the substrate Taconic RF-35. The related parameters of this substrate are as follows: dielectric constant ε_r of 3.5, a thickness of 0.508 mm and a loss tangent of 0.0018. The structures are optimized in Ansys-HFSS. Table 1 shows the final physical sizes of the third-order diplexer circuit. The final physical sizes of the fourth-order diplexer are shown in Table 2. Figure 4 shows the fabricated dual-mode high-isolation diplexers. What's more, all the ports are connected by the type-SubMiniature version A connectors.

The simulated and measured results of the third-order diplexer are given in Fig. 5. The measured center frequencies of the passbands and 3 dB relative bandwidths are 8.9 GHz/2.8% and 9.42 GHz/3.5%, respectively. The measured return losses of the low passband and high passband are less than 18 and 20 dB, respectively, and the insertion losses of the two passbands are 4 and 3.1 dB, respectively. The measured isolation of the entire frequency band is better than 35 dB. Figures 6(a) and 6(b) show the transmission, reflection, and isolation characteristics of the fourth-order diplexer. The measured center frequencies of the passbands and 3 dB relative bandwidths are 8.75 GHz/2.2% and 9.4 GHz/3.2%, respectively. The measured return losses of the low passband and high passband are less than 16.8 and 14 dB, respectively, and the insertion losses of the two passbands are 4.9 and 3.9 dB, respectively. The measured isolation of the entire frequency band is better than 42 dB. It can be seen that in the operating frequency band, the measured results agree well with the simulated ones, verifying the accuracy of the above design.

It can be seen from the simulated results that as the number of the order of the diplexer increases, the measured out-of-band rejection and isolation of the diplexer increase. However, as the number of the order of the diplexer increases, the size and the insertion loss of the circuit also increase. Therefore, it is necessary to make a trade-off based on the required performance when designing the circuit in practice. Table 3 shows the comparison with some prior diplexers. It can be seen that the proposed



Fig. 5. Simulated and measured results of the fabricated third-order diplexer. (a) Transmission and reflection characteristics and (b) isolation.



Fig. 6. Simulated and measured results of the fabricated fourth-order diplexer. (a) Transmission and reflection characteristics and (b) isolation.

Ref.	Return loss (dB)	Isolation (dB)	Freq. (GHz)	Insertion loss (dB)	Out-of-band rejection (dB)	Relative bandwidth (%)
[17]	>17/17	>20	9.7/10.3	<4/7	>29/28	1.1/2.4
[18]	>18/15	-	18.15/19	<3.05/2.3	>40/38	2.75/2.11
[20]	>12.9/13	>32	4.66/5.8	<1.6/2.3	>43/28	-
This work (third-order)	>18/20	>35	8.9/9.42	<4/3.1	>40/38	2.8/3.5
This work (fourth-order)	>16.8/14	>42	8.75/9.4	>4.9/3.9	>45/42	2.2/3.2

Table 3.	The	comparison	with	some	prior	diplexers
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diplexers have the advantages of high isolation, excellent out-of-band rejection, and good impedance matching.

Acknowledgements. The work for this grant was supported by the National Natural Science Foundation of China (Grant No: 61771094), by the Sichuan Science and Technology Program (Grant No: 2019JDRC0008), and by the Shenzhen Basic Research Project Foundation (Grant No: JCYJ201708162723776).

Conclusion

A high-isolation dual-mode diplexer based on the SIW structure has been presented. The dual-mode resonator structure has been used to reduce the size of the circuit. To analyze the proposed diplexers, the equivalent circuits have been used. The measured results of the dual-mode high-isolation diplexers agree well with the simulated ones. From the measured results, it can be seen that many advantages of the proposed diplexers can be summarized as follows: superior isolation, excellent impedance matching, and good out-of-band rejection.

References

- 1. Cameron RJ, Kudsia CM and Mansour RR (2007) Microwave Filters for Communication Systems: Fundamentals, Design, and Applications. Hoboken, NJ, USA: Wiley-Interscience, pp. 225–229.
- Makimoto M and Yamashita S (1979) Compact bandpass filters using stepped impedance resonators. *Proceedings of the IEEE* 67, 16–19.
- Hung WT, Chang SY and Chen SY (2011) High isolation, compact microstrip duplexers using quarter-wave stepped-impedance resonators. *Asia-Pacific Microwave Conference*, Yokohama, Japan, PP. 1747–1750.

- Liu H, Xu W and Zhang Z (2013) Compact diplexer using slotline stepped impedance resonator. *IEEE Microwave & Wireless Components* Letters 23, 75–77.
- Hong JS, Shaman H and Chun YH (2007) Dual-mode microstrip openloop resonators and filters. *IEEE Transactions on Microwave Theory & Techniques* 55, 1764–1770.
- Duan Q, Song K, Chen F and Fan Y (2015) Compact wide-stopband diplexer using dual mode resonators. *Electronics Letters* 51, 1085–1087.
- 7. Xu WQ, Ho MH and Hsu CG (2007) UMTS diplexer design using dualmode stripline ring resonators. *Electronics Letters* 43, 721–722.
- Cheng F, Lin XQ, Song K and Fan Y (2013) Compact diplexer with high isolation using the dual-mode substrate integrated waveguide resonator. *IEEE Microwave and Wireless Components Letters* 23, 459–461.
- Peng HS and Chiang YC (2015) Microstrip diplexer constructed with new types of dual-mode ring filters. *IEEE Microwave & Wireless Components Letters* 25, 7–9.
- Guan X, Yang F and Liu H (2014) Compact and high-isolation diplexer using dual-mode stub-loaded resonators. *IEEE Microwave & Wireless* Components Letters 24, 385–387.
- Jiang W, Peng YJ, Shen W and Wang GA (2015) Dual-mode dual-band balanced filter with high differential-mode frequency selectivity and enhanced common-mode suppression. *IEEE MTT-S International Microwave Symposium IEEE*, Phoenix, AZ, USA, pp. 1–3.
- 12. Wu HW, Huang SH and Chen YF (2013) Design of new quad-channel diplexer with compact circuit size. *IEEE Microwave & Wireless Components Letters* 23, 240–242.
- Li Z, Mansour RR and Yu M (2014) A compact waveguide diplexer employing dual-band resonators. *IEEE MTT-S International Microwave* Symposium (IMS 2014), Tampa, FL, USA, pp. 1–4.
- 14. Wu B and Qiu F. (2015) Multimode wideband diplexer using open- and short-ended stub-loaded hairpin resonator. *Microwave & Optical Technology Letters* 57, 1096–1099.
- Liu H, Yoshimasu T, Kurachi S, Chen J, Li ZF and Sun XW (2005) A novel microstrip diplexer design using defected ground structure. *International Conference on Communications, Circuits and Systems. Proceedings. IEEE Xplore* 2, 1100.
- Song K, Zhou YD, Chen YX, Patience SR, Guo S and Fan Y (2019) Compact high-isolation multiplexer with wide stopband using spiral defected ground resonator. *IEEE Access* 7, 31702–31710.
- Alejandro GL, Magdalena SP and Sai HY (2014) Compact diplexer with dual-mode SIW resonators. 44th European Microwave Conference, Rome, Italy, pp. 857–860.
- Zamzam K and Jens B (2013) Mode Matching Design of substrate integrated waveguide diplexers. *IEEE MTT-S International Microwave Symposium Digest (MTT)*, Seattle, WA, USA, pp. 1–3.
- He JX, Gao KD and Shao ZH (2012) A novel compact Ka-band highrejection diplexer based on substrate integrated waveguide. *International Conference on Computational Problem-Solving (ICCP)*, Leshan, China, pp. 193–197.
- Dong YD and Itoh T (2011) Substrate integrated waveguide loaded by complementary split-ring resonators for miniaturized diplexer design. *IEEE Microwave and Wireless Components Letters* 21, 10–12.
- Zhou Y, Deng HW and Zhao Y (2014) Compact balanced-to-balanced microstrip diplexer with high isolation and common-mode suppression. *IEEE Microwave & Wireless Components Letters* 24, 143–145.
- Song K, Yan YC, Zhong CL, Zhou YD and Fan Y (2019) Compact multimode-resonator diplexer with wide upper-stopband and high isolation. *Electromagnetics* 39, 262–270.
- Xue Q, Shi J and Chen JX (2011) Unbalanced-to-balanced and balanced-to-unbalanced diplexer with high selectivity and common-mode suppression. *IEEE Transactions on Microwave Theory & Techniques* 59, 2848–2855.



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